Experimental Evaluation of an Efficient Cache-Oblivious LCS Algorithm *

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Abstract

We present the results of an extensive computational study of an I/O-optimal cache-oblivious LCS (longest common subsequence) algorithm developed by Chowdhury and Ramachandran. Three variants of the algorithm were implemented (CO denoting the fastest variant) along with the widely used linear-space LCS algorithm by Dan Hirschberg (denoted Hi). Both algorithms were tested on both random and real-world (CFTR DNA) sequences consisting upto 2 million symbols each, and timing and caching data were obtained on three state-of-the-art architectures (Intel Xeon, AMD Opteron and SUN UltraSparc-III+). In our experiments:

- CO ran a factor of 2 to 6 times faster than Hi.
- Hi incurred upto 4,000 times more L1 cache misses and upto 30,000 times more L2 cache misses than CO when run on pairs of sequences consisting of 1 million symbols each.
- CO executed 40%-50% fewer machines instructions than Hi.
- Unlike Hi, CO was able to conceal the effects of caches on its running time; its actual running time could be predicted quite accurately from its theoretical time complexity.
- CO was less sensitive to alphabet size than Hi.

These results suggest that CO and the algorithmic technique employed by CO can be of practical use in many fields including sequence alignment in computational biology.

1 Introduction

Memory in modern computers is typically organized in a hierarchy with registers in the lowest level followed by L1 cache, L2 cache, L3 cache, main memory, and disk, with the access time of each memory level increasing with its level. The two-level I/O model [1] is a simple abstraction of this hierarchy that consists of an internal memory of size $M$, and an arbitrarily large external memory partitioned into blocks of size $B$. The I/O complexity of an algorithm is the number of blocks transferred between these two levels. The I/O model successfully captures the situation where I/O operations between two levels of the memory hierarchy dominate the running time of the algorithm. The cache-oblivious model [8] is an extension of this model with the additional requirement that algorithms must not use the knowledge of $M$ and $B$. A cache-oblivious algorithm is flexible and portable, and simultaneously adapts to all levels of a multi-level memory hierarchy. A well-designed cache-oblivious algorithm typically has the feature that whenever a block is brought into internal memory it contains as much useful data as

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possible (‘spatial locality’), and also the feature that as much useful work as possible is performed on this data before it is written back to external memory (‘temporal locality’).

The problem of finding the longest common subsequence (LCS) of two given sequences has a classic Dynamic Programming (DP) solution [6] that runs in $\Theta(mn)$ time, uses $\Theta(mn)$ space and performs $\Theta(\frac{mn^2}{B})$ block transfers when working on two sequences of lengths $m$ and $n$. The LCS problem arises in a wide range of applications in many apparently unrelated fields including computer science, molecular biology, mathematics, speech recognition, gas chromatography, bird song analysis, etc [24, 22]. Perhaps the widely used Unix file comparison program diff [14] is the most familiar application in computer science that uses an LCS algorithm. The LCS problem is especially prominent in molecular biology in sequence alignment. One of the central problems in genome analysis is to find a maximum-length subsequence of two genomes either under the standard LCS metric or under a refined metric that assigns costs to mismatches, or allows insertions and deletions. Many of these variants can be solved by a dynamic programming algorithm with the same structure as the one for LCS, but with somewhat different computation associated with the steps [6, 16, 26, 24, 22].

It has been shown in [2, 12, 17] that the LCS problem cannot be solved in $o(mn)$ time if the elementary comparison operation is of type ‘equal/unequal’ and the alphabet size is unrestricted. However, if the alphabet size is fixed the theoretically fastest known algorithm runs in $O\left(\frac{mn}{\log \min(m,n)}\right)$ time [19] which is unfortunately not suitable for practical implementations. Faster algorithms exist for different special cases of the problem [4].

In most applications, however, the quadratic space required by an LCS algorithm is a more constraining factor than its quadratic running time [10]. For example, one can wait for a week or even a month for finding an LCS of two sequences of length 1 million each, but one can hardly afford the several terabytes of RAM required by the algorithm. Fortunately, there are linear space implementations [11, 15, 3] of the LCS algorithm, but the I/O complexity remains $\Omega(\frac{mn}{B})$ and the running time roughly doubles. Hirschberg’s space-reduction technique [11] for the DP-based LCS algorithm has become the most widely used trick for reducing the space complexity of similar DP-based algorithms in computational biology [18, 20, 26, 16].

In [7] we present a cache-oblivious implementation of the basic dynamic programming LCS algorithm. Our algorithm continues to run in $O(mn)$ time and uses $O(m + n)$ space, but it performs only $O\left(\frac{mn}{MN}\right)$ block transfers. We show that our algorithm is I/O-optimal in that it performs the minimum number of block transfers (to within a constant factor) of any implementation of the dynamic programming algorithm for LCS. This algorithm can be adapted to solve the edit distance problem [13, 6] within the same bounds; this latter problem asks for the minimum cost of an edit sequence that transforms a given sequence into another one with the allowable edit operations being insertion, deletion and substitution of symbols each having a cost based on the symbol(s) on which it is to be applied.

**Our Results.** We present the results of an extensive empirical study of the cache-oblivious LCS algorithm given in [7]. We implemented three variants of the algorithm (denoting the fastest variant by CO) along with the widely used linear-space LCS algorithm by Hirschberg (denoted Hi) [11]. We ran both algorithms on both random and real-world sequences consisting up to 2 million symbols each, and obtained timing and caching data on three state-of-the-art architectures: Intel Xeon, AMD Opteron and SUN UltraSparc-III+.

On random sequences CO ran a factor of 2 to 6 times faster than Hi and consistently executed 40%-50% fewer machines instructions. As expected, CO exhibited a far better cache performance than Hi: when run on sequences of length 1 million or more CO incurred only a small fraction of cache misses ($\frac{1}{4000}$ for L1 cache and $\frac{1}{30,000}$ for L2 cache) compared to Hi. Unlike Hi, CO was able to conceal the effects of caches on its running time; its actual running time could be predicted quite accurately from
its theoretical time complexity. Moreover, CO appeared to be less sensitive to alphabet size than Hi. On real-world DNA sequences (CFTR gene sequences [25]), too, CO ran about 2 times faster than Hi.

Our experimental results suggest that CO and the algorithmic technique employed by CO can be of practical value in many application areas including sequence alignment in computational biology.

Related Work. In [4], Bergroth et al. conducted an empirical study of the running times of different LCS algorithms. However, they excluded linear-space algorithms from their study, and considered sequences of length at most 4000. If only the length of the LCS is needed, and not an actual subsequence, the technique for cache-oblivious stencil computation presented in [9] can achieve the same time, space and I/O bounds as our algorithm. Experimental results show that the algorithm runs up to 7 times faster than the standard algorithm [9].

In [5] an empirical study of a DP-based cache-oblivious optimal matrix chain multiplication algorithm was conducted. A cache-oblivious algorithm for Floyd-Warshall’s APSP algorithm is given in [21] and experimental results show that the algorithm runs up to 10 times faster than the standard Floyd-Warshall algorithm.

2 Experimental Setup

2.1 Test Data Sets. In our experiments we used two kinds of sequences:

- **Random Sequences.** We used random sequences of lengths ranging from $2^{10}$ (≈ one thousand) to $2^{21}$ (≈ 2 million) drawn from alphabets of sizes 26 (i.e., the English alphabet) and 4 (i.e., the set \{A, C, G, T\} of DNA bases).

- **Real-world Sequences.** We used sequences from 11 species (baboon, cat, chicken, chimpanzee, cow, dog, fugu, human, mouse, rat and zebrafish) generated for the genomic segment harboring the cystic fibrosis transmembrane conductance regulator (CFTR) gene [25]. The lengths of the sequences ranged from 0.42 million to 1.80 million.

2.2 Computing Environment. We ran our experiments on the following three architectures:

- **Intel Xeon.** A four-processor 3.06 GHz Intel Xeon shared memory machine with 4 GB of RAM and running Linux 2.4.29. Each processor had an 8 KB L1 data cache (4-way set associative) and an on-chip 512 KB unified L2 cache (8-way). The block size was 64 bytes for both caches.

- **AMD Opteron.** A dual processor 2.4 GHz AMD Opteron shared memory machine with 4 GB of RAM and running Linux 2.4.29. Each processor had a 64 KB L1 data cache (2-way) and an on-chip 1 MB unified L2 cache (8-way). The block size was 64 bytes for both caches.

- **SUN Blade.** A 1 GHz Sun Blade 2000/1000 (UltraSPARC-III+) with 1 GB of RAM and running SunOS 5.9. The processor had an on-chip 64 KB L1 data cache (4-way) and an off-chip 8 MB L2 cache (2-way). The block sizes were 32 bytes for the L1 cache and 512 bytes for the L2 cache.

We used the CacheGrind profiler [23] for simulating cache effects on Intel Xeon. The caching data on the Sun Blade was obtained using the cputrack utility that keeps track of hardware counters. All algorithms were implemented in C using a uniform programming style and compiled using gcc with optimization parameter -O3. Each machine was exclusively used for experiments (i.e., no other programs were running on them), and on multi-processor machines only a single processor was used.

2.3 Overview of Algorithms Implemented. A sequence $Z = \langle z_1, z_2, \ldots, z_k \rangle$ is called a subsequence of another sequence $X = \langle x_1, x_2, \ldots, x_m \rangle$ if there exists a strictly increasing function $f : [1, 2, \ldots, k] \rightarrow [1, 2, \ldots, m]$ such that for all $i \in [1, k]$, $z_i = x_{f(i)}$. A sequence $Z$ is a common subsequence of sequences $X$ and $Y$ if $Z$ is a subsequence of both $X$ and $Y$. Given two sequences $X$ and $Y$, the Longest Common Subsequence (LCS) problem asks for a maximum-length common subsequence of $X$ and $Y$. 
2.3.1 Classic Dynamic Programming Algorithm. Given two sequences \( X = \langle x_1, x_2, \ldots, x_m \rangle \) and \( Y = \langle y_1, y_2, \ldots, y_n \rangle \), we define \( c[i, j] \) (\( 0 \leq i \leq m, 0 \leq j \leq n \)) to be the length of an LCS of \( \langle x_1, x_2, \ldots, x_i \rangle \) and \( \langle y_1, y_2, \ldots, y_j \rangle \), which can be computed using the following recurrence relation (see, e.g., [6]):

\[
c[i, j] = \begin{cases} 
0 & \text{if } i = 0 \text{ or } j = 0, \\
\max(0, c[i-1, j-1] + 1) & \text{if } 1 \leq i, j \leq n \text{ and } x_i = y_j, \\
\max(c[i-1, j], c[i, j-1]) & \text{if } i > 0 \text{ and } x_i \neq y_j.
\end{cases}
\]

(2.1)

The classic dynamic programming solution to the LCS problem which we will refer to as CLASSIC-LCS, is based on this recurrence relation, and computes the entries of \( c[0 \ldots m, 0 \ldots n] \) in row-major order in \( \Theta(mn) \) time and incurs \( O\left(\frac{mn}{B}\right) \) cache misses. Further, after all entries of \( c[0 \ldots m, 0 \ldots n] \) are computed, we can trace back the sequence of decisions that led to the value computed for \( c[m, n] \), and thus recover an LCS of \( X \) and \( Y \) in \( \Theta(m+n) \) additional time, while incurring \( \Theta(m+n) \) I/Os. Observe that since the entries of any row of \( c \) depends only on the entries in the previous row, \( c[m, 0 \ldots n] \) can be computed using only \( O(n) \) extra space, and thus the length of an LCS can be computed in linear space. However, the algorithm needs \( \Theta(mn) \) space to compute an actual LCS sequence.

2.3.2 Hirschberg’s Linear Space Algorithm. Hirschberg [11] gives an \( \Theta(m+n) \) space algorithm, which finds an LCS in \( \Theta(mn) \) time and \( O\left(\frac{mn}{B}\right) \) I/Os. We briefly describe the algorithm below.

If \( n = 1 \), the LCS can be determined in a single scan of \( X \). Otherwise, let \( k = \left\lfloor \frac{n}{2} \right\rfloor \), \( Y_f = \langle y_1, y_2, \ldots, y_k \rangle \) and \( Y_b = \langle y_{n-1}, \ldots, y_{k+1} \rangle \). Hirschberg computes two arrays \( L_f[1 \ldots m] \) and \( L_b[1 \ldots m] \), where for \( 1 \leq i \leq m \), \( L_f[i] \) is the length of an LCS between \( \langle x_1, x_2, \ldots, x_i \rangle \) and \( Y_f \), and \( L_b[i] \) is the length of an LCS between \( \langle x_1, x_2, \ldots, x_i \rangle \) and \( Y_b \). Observe that both \( L_f \) and \( L_b \) can be computed in \( \Theta(mn) \) time using only \( O(m) \) extra space (see section 2.3.1). Let \( j \) (\( 1 \leq j \leq m \)) be the smallest index such that \( L_f[j] + L_b[j] = \max_{1 \leq l \leq m} (L_f[i] + L_b[i]) \). Then Hirschberg recursively computes an LCS \( Z_1 \) between \( \langle x_1, x_2, \ldots, x_j \rangle \) and \( \langle y_1, y_2, \ldots, y_k \rangle \), and \( Z_2 \) between \( \langle x_{j+1}, x_{j+2}, \ldots, x_m \rangle \) and \( \langle y_{k+1}, y_{k+2}, \ldots, y_n \rangle \), and returns \( Z = Z_1||Z_2 \) as an LCS of \( X \) and \( Y \).

Our Implementation. Our implementation of Hirschberg’s algorithm differs from the original algorithm in how the base case is handled. Once both \( m \) and \( n \) drop below some preset threshold value (BASE) we solve the problem using CLASSIC-LCS. Provided BASE = \( O\left(\sqrt{m+n}\right) \), the space usage remains linear, but the algorithm runs faster.

2.3.3 Cache-Efficient LCS Algorithm. In [7] we present an I/O-optimal cache-oblivious implementation of the dynamic programming algorithm defined by recurrence 2.1. The algorithm runs in \( \Theta(mn) \) time, uses \( \Theta(m+n) \) space and incurs \( \Theta\left(\frac{mn}{B^{3/2}}\right) \) I/Os. We describe the algorithm below, where we assume for convenience that \( n = m = 2^p \) where \( p \) is a nonnegative integer.

For any submatrix \( c[i_1 \ldots i_2, j_1 \ldots j_2] \) of \( c \) where \( i_2 \geq i_1 > 0 \) and \( j_2 \geq j_1 > 0 \), we refer to \( c[i_1 - 1, j_1 \ldots j_2] \) and \( c[i_1 \ldots i_2, j_1 - 1] \) as the input boundary of the submatrix, and \( c[i_2, j_1 \ldots j_2] \) and \( c[i_1 \ldots i_2, j_2] \) as the output boundary. The function LCS-OUTPUT-BOUNDARY (given in [7]) when called with parameters \( i, j, r \), and a one dimensional array \( D \) containing the input boundary of the submatrix \( c[i \ldots i + r - 1, j \ldots j + r - 1] \) returns the output boundary of that submatrix in \( D \). For simplicity of exposition, we assume that \( D \) can have negative indices, and entry \( c[i', j'] \) of \( c \) is stored in \( D[i' - j'] \).

The function works by recursively subdividing the input matrix into quadrants, and for an input matrix of dimension \( n \times n \) runs in time \( \bigO(n^2) \), uses \( \bigO(n) \) space and incurs \( \Theta\left(\frac{n^2}{B^{3/2}}\right) \) cache misses.

Recall that if all entries of \( c[1 \ldots n, 1 \ldots n] \) are available, one can trace back the sequence of decisions that led to the value computed for \( c[n, n] \), and thus retrieve the elements on an LCS of \( X \) and \( Y \). We can view this sequence of decisions as a path through \( c \) that starts at \( c[n, n] \) and ends at the input boundary of \( c[1 \ldots n, 1 \ldots n] \). We call this path an LCS Path.
Our algorithm traces an LCS path without storing all entries of \( c \); instead it only stores the boundaries of certain subproblems. It uses a recursive function \textsc{Recursive-LCS} (given below and in [7]) to construct the LCS. When called with parameters \( i, j, i_s, j_s \) and \( D \), \textsc{Recursive-LCS} assumes that the input boundary of the submatrix \( c[i \ldots i + r - 1, j \ldots j + r - 1] \) is stored in the one dimensional array \( D \) of length \( 2r + 1 \), i.e., entry \( c[i', j'] \) on the boundary is stored in \( D[i' - j'] \), where \( r = \max(i_s - i + 1, j_s - j + 1) \) is a non-negative integer \( p \). It also assumes that an LCS path intersects the output boundary of \( c[i \ldots i + r - 1, j \ldots j + r - 1] \) at \( c[i_s, j_s] \). This function traces the fragment of that path through this submatrix, and returns the LCS of \( X \) and \( Y \) along this subpath. It also finds the entry \( c[i', j'] \) at which this path intersects the input boundary of the given submatrix, and updates \( i_s \) and \( j_s \) to \( i' \) and \( j' \), respectively. If \( r \) is sufficiently small it solves the problem iteratively using \textsc{Classic-LCS}, otherwise it solves the problem recursively by dividing the input submatrix into four quadrants. It first calls \textsc{LCS-Output-Boundary} at most three times (at most once for each quadrant except the bottom-right one) in order to generate the input boundaries of the top-right and the bottom-left quadrants, and if required (if \( i_s \geq i + \frac{r}{2} \) and \( j_s \geq j + \frac{r}{2} \), for the bottom-right quadrant. Observe that the LCS path can pass through at most three quadrants of the current submatrix. This function locates those quadrants one after another based on the current values of \( i_s \) and \( j_s \) (i.e., based on which quadrant \( c[i_s, j_s] \) belongs to), and calls itself recursively in order to trace the fragment of the LCS path that passes through that quadrant. (note that the recursive calls modify \( i_s \) and \( j_s \).) The output of \textsc{Recursive-LCS} is the concatenation of these LCS fragments in the correct order.

The initial call is to \textsc{Recursive-LCS}(\( X, Y, 1, 1, n, n, D \)), with \( D[-n \ldots n] \) initialized to all zeros.

**Complexities.** It has been shown in [7] that the algorithm incurs \( O \left( 1 + \frac{n}{p} + \frac{n^2}{2nm} \right) \) cache misses while