

#|

Copyright (C) 1994 by Yuan Yu. All Rights Reserved.

This script is hereby placed in the public domain, and therefore unlimited editing and redistribution is permitted.

NO WARRANTY

Yuan Yu PROVIDES ABSOLUTELY NO WARRANTY. THE EVENT SCRIPT IS PROVIDED "AS IS" WITHOUT WARRANTY OF ANY KIND, EITHER EXPRESS OR IMPLIED, INCLUDING, BUT NOT LIMITED TO, ANY IMPLIED WARRANTIES OF MERCHANTABILITY AND FITNESS FOR A PARTICULAR PURPOSE. THE ENTIRE RISK AS TO THE QUALITY AND PERFORMANCE OF THE SCRIPT IS WITH YOU. SHOULD THE SCRIPT PROVE DEFECTIVE, YOU ASSUME THE COST OF ALL NECESSARY SERVICING, REPAIR OR CORRECTION.

IN NO EVENT WILL Yuan Yu BE LIABLE TO YOU FOR ANY DAMAGES, ANY LOST PROFITS, LOST MONIES, OR OTHER SPECIAL, INCIDENTAL OR CONSEQUENTIAL DAMAGES ARISING OUT OF THE USE OR INABILITY TO USE THIS SCRIPT (INCLUDING BUT NOT LIMITED TO LOSS OF DATA OR DATA BEING RENDERED INACCURATE OR LOSSES SUSTAINED BY THIRD PARTIES), EVEN IF YOU HAVE ADVISED US OF THE POSSIBILITY OF SUCH DAMAGES, OR FOR ANY CLAIM BY ANY OTHER PARTY.

|#

; Proof of the Correctness of a GCD Program

EVENT: Start with the library "mc20-2" using the compiled version.

#|

The following LISP program computes the greatest common divisor of two integers a and b. We, here, investigate the machine code of this program generated by a widely used C compiler, and verify the correctness of the code.

; computes the greatest common divisor by Euclid's algorithm.
(proclaim '(function fixnum-gcd (fixnum fixnum) fixnum))

```
(defun fixnum-gcd (x y)
  (declare (fixnum x y))
  (if (not (= x 0))
      (if (= y 0)
          x
          (fixnum-gcd y (mod x y)))
      y)
  (return-from fixnum-gcd x))
```

```

(if (> x y)
  (fixnum-gcd (the fixnum (rem x y)) y)
  (fixnum-gcd x (the fixnum (rem y x)))))
(return-from fixnum-gcd y)))

```

Here is the MC68020 assembly code of the above GCD program. The code is generated by AKCL.

```

0x14 <LI1>: linkw fp,#0
0x18 <LI1+4>: moveml d2-d3,sp@-
0x1c <LI1+8>: movel fp@(8),d3
0x20 <LI1+12>: movel fp@(12),d2
0x24 <LI1+16>: tstl d3
0x26 <LI1+18>: beq 0x44 <LI1+48>
0x28 <LI1+20>: tstl d2
0x2a <LI1+22>: bne 0x30 <LI1+28>
0x2c <LI1+24>: movel d3,d0
0x2e <LI1+26>: bra 0x46 <LI1+50>
0x30 <LI1+28>: cmpl d3,d2
0x32 <LI1+30>: bge 0x3c <LI1+40>
0x34 <LI1+32>: divsll d2,d0,d3
0x38 <LI1+36>: movel d0,d3
0x3a <LI1+38>: bra 0x24 <LI1+16>
0x3c <LI1+40>: divsll d3,d0,d2
0x40 <LI1+44>: movel d0,d2
0x42 <LI1+46>: bra 0x24 <LI1+16>
0x44 <LI1+48>: movel d2,d0
0x46 <LI1+50>: moveml fp@(-8),d2-d3
0x4c <LI1+56>: unlk fp
0x4e <LI1+58>: rts

```

The machine code of the above program is:

```

<LI1>:      0x4e56  0x0000  0x48e7  0x3000  0x262e  0x0008  0x242e  0x000c
<LI1+16>:    0x4a83  0x671c  0x4a82  0x6604  0x2003  0x6016  0xb483  0x6c08
<LI1+32>:    0x4c42  0x3800  0x2600  0x60e8  0x4c43  0x2800  0x2400  0x60e0
<LI1+48>:    0x2002  0x4cee  0x000c  0xffff  0x4e5e  0x4e75

```

```

' (78 86 0 0 72 231 48 0
  38 46 0 8 36 46 0 12
  74 131 103 28 74 130 102 4
  32 3 96 22 180 131 108 8
  76 66 56 0 38 0 96 232
  76 67 40 0 36 0 96 224

```

```

32 2 76 238 0 12 255 248
78 94 78 117)
|#

; now we start the correctness proof of this GCD program, defined by
; (gcd-code).

DEFINITION:
GCD-CODE
= '(78 86 0 0 72 231 48 0 38 46 0 8 36 46 0 12 74 131
    103 28 74 130 102 4 32 3 96 22 180 131 108 8 76 66
    56 0 38 0 96 232 76 67 40 0 36 0 96 224 32 2 76 238
    0 12 255 248 78 94 78 117)

; the functional description of the program (gcd-code).

DEFINITION:
gcd(a, b)
= if a  $\simeq$  0 then fix(b)
  elseif b  $\simeq$  0 then a
  elseif b < a then gcd(a mod b, b)
  else gcd(a, b mod a) endif

DEFINITION:
gcd-t1(a, b)
= if a  $\simeq$  0 then 6
  elseif b  $\simeq$  0 then 9
  elseif b < a then splus(9, gcd-t1(a mod b, b))
  else splus(9, gcd-t1(a, b mod a)) endif

DEFINITION: gcd-t(a, b) = splus(4, gcd-t1(a, b))

DEFINITION:
gcd-induct(s, a, b)
= if (a  $\simeq$  0)  $\vee$  (b  $\simeq$  0) then t
  elseif b < a then gcd-induct(stepn(s, 9), a mod b, b)
  else gcd-induct(stepn(s, 9), a, b mod a) endif

DEFINITION:
gcd-statep(s, a, b)
= ((mc-status(s) = 'running)
    $\wedge$  evenp(mc-pc(s))
    $\wedge$  rom-addrp(mc-pc(s), mc-mem(s), 60)
    $\wedge$  mcode-addrp(mc-pc(s), mc-mem(s), GCD-CODE)
    $\wedge$  ram-addrp(sub(32, 12, read-sp(s)), mc-mem(s), 24)

```

$$\begin{aligned}
& \wedge (a = \text{iread-mem}(\text{add}(32, \text{read-sp}(s), 4), \text{mc-mem}(s), 4)) \\
& \wedge (b = \text{iread-mem}(\text{add}(32, \text{read-sp}(s), 8), \text{mc-mem}(s), 4)) \\
& \wedge (a \in \mathbf{N}) \\
& \wedge (b \in \mathbf{N})
\end{aligned}$$

DEFINITION:

$$\begin{aligned}
& \text{gcd-s0p}(s, a, b) \\
= & ((\text{mc-status}(s) = \text{'running'}) \\
& \wedge \text{evenp}(\text{mc-pc}(s)) \\
& \wedge \text{rom-addrp}(\text{sub}(32, 16, \text{mc-pc}(s)), \text{mc-mem}(s), 60) \\
& \wedge \text{mcode-addrp}(\text{sub}(32, 16, \text{mc-pc}(s)), \text{mc-mem}(s), \text{GCD-CODE}) \\
& \wedge \text{ram-addrp}(\text{sub}(32, 8, \text{read-an}(32, 6, s)), \text{mc-mem}(s), 24) \\
& \wedge (a = \text{iread-dn}(32, 3, s)) \\
& \wedge (b = \text{iread-dn}(32, 2, s)) \\
& \wedge (a \in \mathbf{N}) \\
& \wedge (b \in \mathbf{N}))
\end{aligned}$$

; s --> s0.

THEOREM: gcd-s-s0

$$\begin{aligned}
& \text{gcd-statep}(s, a, b) \\
\rightarrow & (\text{gcd-s0p}(\text{stepn}(s, 4), a, b) \\
& \wedge (\text{linked-rts-addr}(\text{stepn}(s, 4)) = \text{rts-addr}(s)) \\
& \wedge (\text{linked-a6}(\text{stepn}(s, 4)) = \text{read-an}(32, 6, s)) \\
& \wedge (\text{read-rn}(32, 14, \text{mc-rfile}(\text{stepn}(s, 4))) \\
& \quad = \text{sub}(32, 4, \text{read-sp}(s))) \\
& \wedge (\text{movem-saved}(\text{stepn}(s, 4), 4, 8, 2) \\
& \quad = \text{readm-rn}(32, \text{'(2 3)}, \text{mc-rfile}(s))))
\end{aligned}$$

THEOREM: gcd-s-s0-rfile

$$\begin{aligned}
& (\text{gcd-statep}(s, a, b) \wedge \text{d4-7a2-5p}(rn)) \\
\rightarrow & (\text{read-rn}(\text{oplen}, rn, \text{mc-rfile}(\text{stepn}(s, 4))) \\
& \quad = \text{read-rn}(\text{oplen}, rn, \text{mc-rfile}(s)))
\end{aligned}$$

THEOREM: gcd-s-s0-mem

$$\begin{aligned}
& (\text{gcd-statep}(s, a, b) \wedge \text{disjoint}(x, l, \text{sub}(32, 12, \text{read-sp}(s)), 24)) \\
\rightarrow & (\text{read-mem}(x, \text{mc-mem}(\text{stepn}(s, 4)), l) = \text{read-mem}(x, \text{mc-mem}(s), l))
\end{aligned}$$

; s0 --> exit.

; base case: s0 --> exit.

THEOREM: gcd-s0-sn-base-1

$$\begin{aligned}
& (\text{gcd-s0p}(s, a, b) \wedge (a \simeq 0)) \\
\rightarrow & ((\text{mc-status}(\text{stepn}(s, 6)) = \text{'running'}) \\
& \wedge (\text{mc-pc}(\text{stepn}(s, 6)) = \text{linked-rts-addr}(s))
\end{aligned}$$

$$\begin{aligned}
& \wedge \text{ (iread-dn } (32, 0, \text{stepn}(s, 6)) = \text{fix}(b)) \\
& \wedge \text{ (read-rn } (32, 14, \text{mc-rfile}(\text{stepn}(s, 6))) = \text{linked-a6}(s)) \\
& \wedge \text{ (read-rn } (32, 15, \text{mc-rfile}(\text{stepn}(s, 6))) \\
& \quad = \text{add}(32, \text{read-an}(32, 6, s), 8)) \\
& \wedge \text{ (read-mem}(x, \text{mc-mem}(\text{stepn}(s, 6)), l) \\
& \quad = \text{read-mem}(x, \text{mc-mem}(s), l))
\end{aligned}$$

THEOREM: gcd-s0-sn-base-rfile-1

$$\begin{aligned}
& (\text{gcd-s0p}(s, a, b) \wedge (a \simeq 0) \wedge \text{d2-7a2-5p}(rn) \wedge (\text{oplen} \leq 32)) \\
& \rightarrow (\text{read-rn}(\text{oplen}, rn, \text{mc-rfile}(\text{stepn}(s, 6))) \\
& \quad = \text{if d4-7a2-5p}(rn) \text{ then read-rn}(\text{oplen}, rn, \text{mc-rfile}(s)) \\
& \quad \text{else get-vlst}(\text{oplen}, 0, rn, '(2\ 3), \text{movem-saved}(s, 4, 8, 2)) \text{ endif})
\end{aligned}$$

THEOREM: gcd-s0-sn-base-2

$$\begin{aligned}
& (\text{gcd-s0p}(s, a, b) \wedge (a \not\simeq 0) \wedge (b \simeq 0)) \\
& \rightarrow ((\text{mc-status}(\text{stepn}(s, 9)) = \text{'running'}) \\
& \quad \wedge (\text{mc-pc}(\text{stepn}(s, 9)) = \text{linked-rt-s-addr}(s)) \\
& \quad \wedge (\text{iread-dn}(32, 0, \text{stepn}(s, 9)) = \text{fix}(a)) \\
& \quad \wedge (\text{read-rn}(32, 14, \text{mc-rfile}(\text{stepn}(s, 9))) = \text{linked-a6}(s)) \\
& \quad \wedge (\text{read-rn}(32, 15, \text{mc-rfile}(\text{stepn}(s, 9))) \\
& \quad \quad = \text{add}(32, \text{read-an}(32, 6, s), 8)) \\
& \quad \wedge (\text{read-mem}(x, \text{mc-mem}(\text{stepn}(s, 9)), l) \\
& \quad \quad = \text{read-mem}(x, \text{mc-mem}(s), l))
\end{aligned}$$

THEOREM: gcd-s0-sn-base-rfile-2

$$\begin{aligned}
& (\text{gcd-s0p}(s, a, b) \\
& \quad \wedge (a \not\simeq 0) \\
& \quad \wedge (b \simeq 0) \\
& \quad \wedge \text{d2-7a2-5p}(rn) \\
& \quad \wedge (\text{oplen} \leq 32)) \\
& \rightarrow (\text{read-rn}(\text{oplen}, rn, \text{mc-rfile}(\text{stepn}(s, 9))) \\
& \quad = \text{if d4-7a2-5p}(rn) \text{ then read-rn}(\text{oplen}, rn, \text{mc-rfile}(s)) \\
& \quad \text{else get-vlst}(\text{oplen}, 0, rn, '(2\ 3), \text{movem-saved}(s, 4, 8, 2)) \text{ endif})
\end{aligned}$$

; induction case: s0 --> s0.

EVENT: Enable integerp.

EVENT: Enable iremainder.

THEOREM: gcd-s0-s0-1

$$\begin{aligned}
& (\text{gcd-s0p}(s, a, b) \wedge (a \not\simeq 0) \wedge (b \not\simeq 0) \wedge (b < a)) \\
& \rightarrow (\text{gcd-s0p}(\text{stepn}(s, 9), a \bmod b, b) \\
& \quad \wedge (\text{read-rn}(\text{oplen}, 14, \text{mc-rfile}(\text{stepn}(s, 9))))
\end{aligned}$$

$$\begin{aligned}
&= \text{read-rn}(\text{oplen}, 14, \text{mc-rfile}(s)) \\
\wedge & (\text{linked-a6}(\text{stepn}(s, 9)) = \text{linked-a6}(s)) \\
\wedge & (\text{linked-rtts-addr}(\text{stepn}(s, 9)) = \text{linked-rtts-addr}(s)) \\
\wedge & (\text{movem-saved}(\text{stepn}(s, 9), 4, 8, 2) = \text{movem-saved}(s, 4, 8, 2)) \\
\wedge & (\text{read-mem}(x, \text{mc-mem}(\text{stepn}(s, 9)), l) \\
&= \text{read-mem}(x, \text{mc-mem}(s, l)))
\end{aligned}$$

THEOREM: gcd-s0-s0-2

$$\begin{aligned}
&(\text{gcd-s0p}(s, a, b) \wedge (a \neq 0) \wedge (b \neq 0) \wedge (b \neq a)) \\
\rightarrow & (\text{gcd-s0p}(\text{stepn}(s, 9), a, b \bmod a) \\
&\wedge (\text{read-rn}(\text{oplen}, 14, \text{mc-rfile}(\text{stepn}(s, 9))) \\
&= \text{read-rn}(\text{oplen}, 14, \text{mc-rfile}(s))) \\
&\wedge (\text{linked-a6}(\text{stepn}(s, 9)) = \text{linked-a6}(s)) \\
&\wedge (\text{linked-rtts-addr}(\text{stepn}(s, 9)) = \text{linked-rtts-addr}(s)) \\
&\wedge (\text{movem-saved}(\text{stepn}(s, 9), 4, 8, 2) = \text{movem-saved}(s, 4, 8, 2)) \\
&\wedge (\text{read-mem}(x, \text{mc-mem}(\text{stepn}(s, 9)), l) \\
&= \text{read-mem}(x, \text{mc-mem}(s, l)))
\end{aligned}$$

THEOREM: gcd-s0-s0-rfile-1

$$\begin{aligned}
&(\text{gcd-s0p}(s, a, b) \wedge (a \neq 0) \wedge (b \neq 0) \wedge (b < a) \wedge \text{d4-7a2-5p}(rn)) \\
\rightarrow & (\text{read-rn}(\text{oplen}, rn, \text{mc-rfile}(\text{stepn}(s, 9))) \\
&= \text{read-rn}(\text{oplen}, rn, \text{mc-rfile}(s)))
\end{aligned}$$

THEOREM: gcd-s0-s0-rfile-2

$$\begin{aligned}
&(\text{gcd-s0p}(s, a, b) \wedge (a \neq 0) \wedge (b \neq 0) \wedge (b \neq a) \wedge \text{d4-7a2-5p}(rn)) \\
\rightarrow & (\text{read-rn}(\text{oplen}, rn, \text{mc-rfile}(\text{stepn}(s, 9))) \\
&= \text{read-rn}(\text{oplen}, rn, \text{mc-rfile}(s)))
\end{aligned}$$

; put together.

THEOREM: gcd-s0-sn

$$\begin{aligned}
&\text{gcd-s0p}(s, a, b) \\
\rightarrow & ((\text{mc-status}(\text{stepn}(s, \text{gcd-t1}(a, b))) = \text{'running'}) \\
&\wedge (\text{mc-pc}(\text{stepn}(s, \text{gcd-t1}(a, b))) = \text{linked-rtts-addr}(s)) \\
&\wedge (\text{iread-dn}(32, 0, \text{stepn}(s, \text{gcd-t1}(a, b))) = \text{gcd}(a, b)) \\
&\wedge (\text{read-rn}(32, 14, \text{mc-rfile}(\text{stepn}(s, \text{gcd-t1}(a, b)))) \\
&= \text{linked-a6}(s)) \\
&\wedge (\text{read-rn}(32, 15, \text{mc-rfile}(\text{stepn}(s, \text{gcd-t1}(a, b)))) \\
&= \text{add}(32, \text{read-an}(32, 6, s), 8)) \\
&\wedge (\text{read-mem}(x, \text{mc-mem}(\text{stepn}(s, \text{gcd-t1}(a, b))), k) \\
&= \text{read-mem}(x, \text{mc-mem}(s, k)))
\end{aligned}$$

THEOREM: gcd-s0-sn-rfile

$$\begin{aligned}
&(\text{gcd-s0p}(s, a, b) \wedge \text{d2-7a2-5p}(rn) \wedge (\text{oplen} \leq 32)) \\
\rightarrow & (\text{read-rn}(\text{oplen}, rn, \text{mc-rfile}(\text{stepn}(s, \text{gcd-t1}(a, b)))) \\
&= \text{if } \text{d4-7a2-5p}(rn) \text{ then } \text{read-rn}(\text{oplen}, rn, \text{mc-rfile}(s)) \\
&\text{ else } \text{get-vlst}(\text{oplen}, 0, rn, '2\ 3), \text{movem-saved}(s, 4, 8, 2) \text{ endif})
\end{aligned}$$

; the correctness of gcd.

THEOREM: gcd-correctness

gcd-statep(s, a, b)
 $\rightarrow$ ((mc-status(stepn($s, \text{gcd-t}(a, b)$))) = 'running)
 $\wedge$ (mc-pc(stepn($s, \text{gcd-t}(a, b)$))) = rts-addr(s)
 $\wedge$ (iread-dn(32, 0, stepn($s, \text{gcd-t}(a, b)$))) = gcd(a, b)
 $\wedge$ (read-rn(32, 14, mc-rfile(stepn($s, \text{gcd-t}(a, b)$)))
 $=$ read-rn(32, 14, mc-rfile(s)))
 $\wedge$ (read-rn(32, 15, mc-rfile(stepn($s, \text{gcd-t}(a, b)$)))
 $=$ add(32, read-an(32, 7, s), 4)))

THEOREM: gcd-rfile

(gcd-statep(s, a, b) $\wedge$ d2-7a2-5p(rn) $\wedge$ (oplen $\leq$ 32))
 $\rightarrow$ (read-rn(oplen, rn , mc-rfile(stepn($s, \text{gcd-t}(a, b)$)))
 $=$ read-rn(oplen, rn , mc-rfile(s)))

THEOREM: gcd-mem

(gcd-statep(s, a, b) $\wedge$ disjoint(x, k , sub(32, 12, read-sp(s)), 24))
 $\rightarrow$ (read-mem(x , mc-mem(stepn($s, \text{gcd-t}(a, b)$))), k)
 $=$ read-mem(x , mc-mem(s)), k))

EVENT: Disable gcd-t.

; tp prove that (gcd a b) does compute the greatest common divisor, we need
; to prove the following two theorems:
; 1. (gcd a b) divides a and (gcd a b) divides b.
; i.e. (gcd a b) is a common divisor of a and b.
; 2. any divisor of a and b is no greater than (gcd a b).

THEOREM: remainder-remainder

(($b \bmod c$) = 0) $\rightarrow$ ((($a \bmod b$) $\bmod c$) = ($a \bmod c$))

THEOREM: gcd-is-cd

(($a \bmod \text{gcd}(a, b)$) = 0) $\wedge$ (($b \bmod \text{gcd}(a, b)$) = 0)

THEOREM: gcd-the-greatest

(($a \neq 0$) $\wedge$ ($b \neq 0$) $\wedge$ (($a \bmod x$) = 0) $\wedge$ (($b \bmod x$) = 0))
 $\rightarrow$ (gcd(a, b) $\nless x$)

; a simple timing analysis.

THEOREM: lessp-times-lessp

(($a < b$) $\wedge$ ($c \neq 0$)) $\rightarrow$ ($a < (b * c)$)

THEOREM: remainder-shrink-half

$$((b \leq a) \wedge (b \neq 0)) \rightarrow ((a \div 2) \not\leq (a \bmod b))$$

THEOREM: gcd-t-shrink-1

$$((b \leq a) \wedge (b \neq 0)) \rightarrow ((\log(2, a) - 1) \not\leq \log(2, a \bmod b))$$

THEOREM: gcd-t-shrink-2

$$\begin{aligned} & ((b \leq a) \wedge (b \neq 0) \wedge (a \neq 1)) \\ \rightarrow & ((9 * (x + \log(2, a))) \\ & \not\leq (9 + (9 * (x + \log(2, a \bmod b))))) \end{aligned}$$

DEFINITION:

gcd-t2(a, b)

```
=  if a ≈ 0 then 6
    elseif b ≈ 0 then 9
    elseif b < a then 9 + gcd-t2(a mod b, b)
    elseif a < b then 9 + gcd-t2(a, b mod a)
    else 18 endif
```

THEOREM: gcd-t2-ub

$$\text{gcd-t2}(a, b) \leq (18 + (9 * (\log(2, a) + \log(2, b))))$$

THEOREM: gcd-t1-t2

$$\text{gcd-t1}(a, b) = \text{gcd-t2}(a, b)$$

THEOREM: gcd-t-ub

$$\text{gcd-t}(a, b) \leq (22 + (9 * (\log(2, a) + \log(2, b))))$$

THEOREM: gcd-t-ubound-la

$$\begin{aligned} & ((a \leq a1) \wedge (b \leq b1)) \\ \rightarrow & ((22 + (9 * (\log(2, a) + \log(2, b)))) \\ & \leq (22 + (9 * (\log(2, a1) + \log(2, b1))))) \end{aligned}$$

THEOREM: gcd-t-ubound

$$((a < \exp(2, 32)) \wedge (b < \exp(2, 32))) \rightarrow (\text{gcd-t}(a, b) \leq 598)$$

Index

add, 4–7

d2-7a2-5p, 5–7

d4-7a2-5p, 4–6

disjoint, 4, 7

evenp, 3, 4

exp, 8

gcd, 3, 6, 7

gcd-code, 3, 4

gcd-correctness, 7

gcd-induct, 3

gcd-is-cd, 7

gcd-mem, 7

gcd-rfile, 7

gcd-s-s0, 4

gcd-s-s0-mem, 4

gcd-s-s0-rfile, 4

gcd-s0-s0-1, 5

gcd-s0-s0-2, 6

gcd-s0-s0-rfile-1, 6

gcd-s0-s0-rfile-2, 6

gcd-s0-sn, 6

gcd-s0-sn-base-1, 4

gcd-s0-sn-base-2, 5

gcd-s0-sn-base-rfile-1, 5

gcd-s0-sn-base-rfile-2, 5

gcd-s0-sn-rfile, 6

gcd-s0p, 4–6

gcd-statep, 3, 4, 7

gcd-t, 3, 7, 8

gcd-t-shrink-1, 8

gcd-t-shrink-2, 8

gcd-t-ub, 8

gcd-t-ubound, 8

gcd-t-ubound-la, 8

gcd-t1, 3, 6, 8

gcd-t1-t2, 8

gcd-t2, 8

gcd-t2-ub, 8

gcd-the-greatest, 7

get-vlst, 5, 6

iread-dn, 4–7

iread-mem, 4

lessp-times-lessp, 7

linked-a6, 4–6

linked-rts-addr, 4–6

log, 8

mc-mem, 3–7

mc-pc, 3–7

mc-rfile, 4–7

mc-status, 3–7

mcode-addrp, 3, 4

movem-saved, 4–6

ram-addrp, 3, 4

read-an, 4–7

read-mem, 4–7

read-rn, 4–7

read-sp, 3, 4, 7

readm-rn, 4

remainder-remainder, 7

remainder-shrink-half, 8

rom-addrp, 3, 4

rts-addr, 4, 7

splus, 3

stepn, 3–7

sub, 3, 4, 7