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|#

; Proof of the Correctness of an Integer Square Root Program
;

EVENT: Start with the library "mc20-2" using the compiled version.

#|

This is a revisit to our ISQRT proof. Dr. Don Good talked with Dr. Steve Zeigler, vice president for Ada products at Verdix, and they agreed to let me publicize the machine codes generated by their present Ada compiler.

The following Ada function ISQRT computes the integer square root of a given nonnegative integer i. This is our third proof about ISQRT.

```
function isqrt (i:integer) return integer is
  j : integer := (i / 2);
begin
  while ((i / j) < j) loop
    j := (j + (i / j)) / 2;
```

```

    end loop;
    return j;
end isqrt;

```

Here is the MC68020 assembly code of the above ISQRT program. The code is from Dr. Steve Zeigler and generated by their present Ada compiler.

```

1  function isqrt (i:integer) return integer is
00000: link.w      a6, #-04
2      j : integer := (i / 2);
00004: move.l      d2, d1
00006: bge.b       06    -> 0e
00008: addi.l      #01, d1
0000e: asr.l       #01, d1
3 begin
4      while not ((i / j) >= j) loop
00010: move.l      d2, d0
00012: divsl.l     d1, d0:d0
00016: trapv
00018: cmp.l       d0, d1
0001a: ble.b       01c   -> 038
5      j := (j + (i / j)) / 2;
0001c: add.l       d1, d0
0001e: trapv
00020: move.l      d0, d1
00022: bge.b       06    -> 02a
00024: addi.l      #01, d1
0002a: asr.l       #01, d1
4      while not ((i / j) >= j) loop
0002c: move.l      d2, d0
0002e: divsl.l     d1, d0:d0
00032: trapv
6 end loop;
00034: cmp.l       d0, d1
00036: bgt.b       -01c   -> 01c
7 return j;
00038: move.l      d1, d0
0003a: unlk        a6
0003c: rts
8 end isqrt;

```

```

0x4e56  0xffffc  0x2202  0x6c06  0x0681  0x0000  0x0001  0xe281
0x2002  0x4c41   0x0800  0x4e76  0xb280  0x6f1c  0xd081  0x4e76
0x2200  0x6c06  0x0681  0x0000  0x0001  0xe281  0x2002  0x4c41

```

```

0x0800 0x4e76 0xb280 0x6ee4 0x2001 0x4e5e 0x4e75

'(78     86     255    252    34      2      108     6
 6     129     0      0      0      1      226    129
 32     2      76     65     8      0      78     118
 178    128    111     28    208    129    78     118
 34     0     108     6      6     129      0      0
 0     1     226    129    32      2      76     65
 8     0      78     118   178    128    110    228
 32     1      78     94     78    117)
|#
; in the logic, the above program is defined by (isqrt-code).

DEFINITION:
ISQRT-CODE
= '(78 86 255 252 34 2 108 6 6 129 0 0 0 1 226 129 32 2
 76 65 8 0 78 118 178 128 111 28 208 129 78 118 34 0
 108 6 6 129 0 0 0 1 226 129 32 2 76 65 8 0 78 118
 178 128 110 228 32 1 78 94 78 117)

DEFINITION: sq(x) = (x * x)

; isqrt1 is a function in the Logic simulating the loop of the above code.

DEFINITION:
isqrt1(i, j)
= if j ≈ 0 then fix(i)
  elseif (i ÷ j) < j then isqrt1(i, (j + (i ÷ j)) ÷ 2)
  else fix(j) endif

; isqrt specifies the semantics of ISQRT in the Logic. To see why
; the function isqrt computes the square root for any nonnegative
; integer input, please refer to the proof in file isqrt.events.

DEFINITION:
isqrt(i)
= let j1 be ((i ÷ 2) + (i ÷ (i ÷ 2))) ÷ 2
  in
  if i < sq(i ÷ 2) then isqrt1(i, j1)
  else i ÷ 2 endif endlet

; the computation time of this program.

DEFINITION:
```

```

isqrt1-t (i, j)
=  if j ≤ 0 then 0
   elseif (i ÷ j) < j
     then splus(10, isqrt1-t (i, (j + (i ÷ j)) ÷ 2))
   else 8 endif

```

DEFINITION:

```

isqrt-t (i)
=  let j1 be ((i ÷ 2) + (i ÷ (i ÷ 2))) ÷ 2
   in
   if i < sq(i ÷ 2) then splus(14, isqrt1-t (i, j1))
   else 12 endif endlet

; enable a few functions.

```

EVENT: Enable iplus.

EVENT: Enable integerp.

EVENT: Enable iquotient.

EVENT: Enable ilessp.

```
; disable a few functions.
```

EVENT: Disable remainder.

EVENT: Disable quotient.

THEOREM: isqrt-no-overflow
 $(\text{int-rangep}(2 * j, n) \wedge ((i \div j) < j)) \rightarrow \text{int-rangep}(j + (i \div j), n)$

THEOREM: j-nonzerop
 $((1 < i) \wedge (0 < j)) \rightarrow (((j + (i \div j)) \div 2) \neq 0)$

THEOREM: j-int-rangep
 $(\text{int-rangep}(2 * j, n) \wedge ((i \div j) < j)) \rightarrow \text{int-rangep}(2 * ((j + (i \div j)) \div 2), n)$

```
; an induction hint.
```

DEFINITION:
isqrt-induct (s, i, j)
= **if** $j \leq 0$ **then** t
elseif $(i \div j) < j$
then isqrt-induct (stepn ($s, 10$), $i, (j + (i \div j)) \div 2$)
else t **endif**
; the properties of the initial state.

DEFINITION:
isqrt-statep (s, i)
= ((mc-status (s) = 'running)
 \wedge evenp (mc-pc (s))
 \wedge rom-addrp (mc-pc (s), mc-mem (s), 70)
 \wedge mcode-addrp (mc-pc (s), mc-mem (s), ISQRT-CODE)
 \wedge ram-addrp (sub (32, 8, read-sp (s)), mc-mem (s), 12)
 \wedge ($i = \text{iread-dn} (32, 2, s)$)
 \wedge illessp (1, i))
; an intermediate state s0.

DEFINITION:
isqrt-s0p (s, i, j)
= ((mc-status (s) = 'running)
 \wedge evenp (mc-pc (s))
 \wedge rom-addrp (sub (32, 44, mc-pc (s)), mc-mem (s), 70)
 \wedge mcode-addrp (sub (32, 44, mc-pc (s)), mc-mem (s), ISQRT-CODE)
 \wedge ram-addrp (sub (32, 4, read-an (32, 6, s)), mc-mem (s), 12)
 \wedge ($i = \text{iread-dn} (32, 2, s)$)
 \wedge ($j = \text{iread-dn} (32, 1, s)$)
 \wedge int-rangep ($2 * j, 32$)
 \wedge illessp (1, i)
 \wedge illessp (0, j))

THEOREM: initial-j-int-rangep
int-rangep (i, n) \rightarrow int-rangep ($2 * (i \div 2), n$)
; from the initial state to exit.

THEOREM: isqrt-s-exit
(isqrt-statep (s, i) \wedge ($i \not\leq ((i \div 2) * (i \div 2))$))
 \rightarrow ((mc-status (stepn ($s, 12$)) = 'running)
 \wedge (mc-pc (stepn ($s, 12$)) = rts-addr (s))
 \wedge (iread-dn (32, 0, stepn ($s, 12$)) = ($i \div 2$))
 \wedge (read-rn (32, 14, mc-rfile (stepn ($s, 12$))))

```

=   read-rn (32, 14, mc-rfile (s)))
 $\wedge$  (read-rn (32, 15, mc-rfile (stepn (s, 12))))
=   add (32, read-sp (s), 4))

```

THEOREM: isqrt-s-exit-rfile
 $(\text{isqrt-statep} (s, i) \wedge (i < ((i \div 2) * (i \div 2))) \wedge \text{d2-7a2-5p} (rn))$
 $\rightarrow (\text{read-rn} (oplen, rn, \text{mc-rfile} (\text{stepn} (s, 12))))$
 $= \text{read-rn} (oplen, rn, \text{mc-rfile} (s)))$

THEOREM: isqrt-s-exit-mem
 $(\text{isqrt-statep} (s, i)$
 $\wedge (i < ((i \div 2) * (i \div 2)))$
 $\wedge \text{disjoint} (x, k, \text{sub} (32, 8, \text{read-sp} (s)), 12))$
 $\rightarrow (\text{read-mem} (x, \text{mc-mem} (\text{stepn} (s, 12)), k) = \text{read-mem} (x, \text{mc-mem} (s), k))$

; from the initial state to s0.

THEOREM: isqrt-s-s0
let $j1$ **be** $((i \div 2) + (i \div (i \div 2))) \div 2$
in
 $(\text{isqrt-statep} (s, i) \wedge (i < ((i \div 2) * (i \div 2))))$
 $\rightarrow (\text{isqrt-s0p} (\text{stepn} (s, 14), i, j1)$
 $\wedge (\text{linked-rts-addr} (\text{stepn} (s, 14)) = \text{rts-addr} (s))$
 $\wedge (\text{linked-a6} (\text{stepn} (s, 14)) = \text{read-an} (32, 6, s))$
 $\wedge (\text{read-rn} (32, 14, \text{mc-rfile} (\text{stepn} (s, 14))))$
 $= \text{sub} (32, 4, \text{read-sp} (s)))$ **endlet**

THEOREM: isqrt-s-s0-rfile
 $(\text{isqrt-statep} (s, i) \wedge (i < ((i \div 2) * (i \div 2))) \wedge \text{d2-7a2-5p} (rn))$
 $\rightarrow (\text{read-rn} (oplen, rn, \text{mc-rfile} (\text{stepn} (s, 14))))$
 $= \text{read-rn} (oplen, rn, \text{mc-rfile} (s)))$

THEOREM: isqrt-s-s0-mem
 $(\text{isqrt-statep} (s, i)$
 $\wedge (i < ((i \div 2) * (i \div 2)))$
 $\wedge \text{disjoint} (x, k, \text{sub} (32, 8, \text{read-sp} (s)), 12))$
 $\rightarrow (\text{read-mem} (x, \text{mc-mem} (\text{stepn} (s, 14)), k) = \text{read-mem} (x, \text{mc-mem} (s), k))$

; from s0 to exit (base case), or from s0 to s0 (induction case).
; base case: s0 --> exit.

THEOREM: isqrt-s0-exit-base
 $(\text{isqrt-s0p} (s, i, j) \wedge ((i \div j) \neq j))$
 $\rightarrow ((\text{mc-status} (\text{stepn} (s, 8)) = \text{'running})$
 $\wedge (\text{mc-pc} (\text{stepn} (s, 8)) = \text{linked-rts-addr} (s)))$

```

 $\wedge$  (iread-dn (32, 0, stepn ( $s$ , 8)) =  $j$ )
 $\wedge$  (read-rn (32, 14, mc-rfile (stepn ( $s$ , 8))) = linked-a6 ( $s$ ))
 $\wedge$  (read-rn (32, 15, mc-rfile (stepn ( $s$ , 8)))
      = add (32, read-an (32, 6,  $s$ ), 8)))

```

THEOREM: isqrt1-s0-exit-rfile-base
 $(\text{isqrt-s0p} (s, i, j) \wedge ((i \div j) \not< j) \wedge \text{d2-7a2-5p} (rn))$
 $\rightarrow (\text{read-rn} (\text{oplen}, rn, \text{mc-rfile} (\text{stepn} (s, 8))))$
 $= \text{read-rn} (\text{oplen}, rn, \text{mc-rfile} (s)))$

THEOREM: isqrt1-s0-exit-mem-base
 $(\text{isqrt-s0p} (s, i, j) \wedge ((i \div j) \not< j))$
 $\rightarrow (\text{read-mem} (x, \text{mc-mem} (\text{stepn} (s, 8))), k) = \text{read-mem} (x, \text{mc-mem} (s), k))$

; induction case: s0 --> s0.

THEOREM: isqrt-s0-s0
 $(\text{isqrt-s0p} (s, i, j) \wedge ((i \div j) < j))$
 $\rightarrow (\text{isqrt-s0p} (\text{stepn} (s, 10), i, (j + (i \div j)) \div 2)$
 $\quad \wedge (\text{read-rn} (\text{oplen}, 14, \text{mc-rfile} (\text{stepn} (s, 10))))$
 $\quad = \text{read-rn} (\text{oplen}, 14, \text{mc-rfile} (s)))$
 $\quad \wedge (\text{linked-a6} (\text{stepn} (s, 10)) = \text{linked-a6} (s))$
 $\quad \wedge (\text{linked-rts-addr} (\text{stepn} (s, 10)) = \text{linked-rts-addr} (s)))$

THEOREM: isqrt-s0-s0-rfile
 $(\text{isqrt-s0p} (s, i, j) \wedge ((i \div j) < j) \wedge \text{d2-7a2-5p} (rn))$
 $\rightarrow (\text{read-rn} (\text{oplen}, rn, \text{mc-rfile} (\text{stepn} (s, 10))))$
 $= \text{read-rn} (\text{oplen}, rn, \text{mc-rfile} (s)))$

THEOREM: isqrt-s0-s0-mem
 $(\text{isqrt-s0p} (s, i, j))$
 $\wedge ((i \div j) < j)$
 $\wedge \text{disjoint} (x, k, \text{sub} (32, 8, \text{read-an} (32, 6, s)), 20))$
 $\rightarrow (\text{read-mem} (x, \text{mc-mem} (\text{stepn} (s, 10))), k) = \text{read-mem} (x, \text{mc-mem} (s), k))$

THEOREM: isqrt-s0p-j_nonzerop
 $\text{isqrt-s0p} (s, i, j) \rightarrow ((j \neq 0) \wedge (j \in \mathbb{N}))$

EVENT: Disable isqrt-statep.

EVENT: Disable isqrt-s0p.

; put together: s0 --> exit.

THEOREM: isqrt-s0-exit

```
let  $sn$  be stepn( $s$ , isqrt1-t( $i, j$ ))
in
isqrt-s0p( $s, i, j$ )
→ ((mc-status( $sn$ ) = 'running)
   ∧ (mc-pc( $sn$ ) = linked-rts-addr( $s$ ))
   ∧ (iread-dn(32, 0,  $sn$ ) = isqrt1( $i, j$ ))
   ∧ (read-rn(32, 14, mc-rfile( $sn$ )) = linked-a6( $s$ ))
   ∧ (read-rn(32, 15, mc-rfile( $sn$ ))
       = add(32, read-an(32, 6,  $s$ ), 8))) endlet
```

THEOREM: isqrt-s0-exit-rfile

```
(isqrt-s0p( $s, i, j$ ) ∧ d2-7a2-5p( $rn$ ))
→ (read-rn( $oplen, rn, mc\text{-}rfile(\text{stepn}(s, \text{isqrt1-t}(i, j)))$ )
   = read-rn( $oplen, rn, mc\text{-}rfile(s)$ ))
```

THEOREM: isqrt-s0-exit-mem

```
(isqrt-s0p( $s, i, j$ ) ∧ disjoint( $x, k, \text{sub}(32, 8, \text{read-an}(32, 6, s)), 20$ ))
→ (read-mem( $x, mc\text{-}mem(\text{stepn}(s, \text{isqrt1-t}(i, j))), k$ )
   = read-mem( $x, mc\text{-}mem(s), k$ ))
```

EVENT: Disable isqrt-s0p-j_nonzerop.

```
; ISQRT program is correct.
; after an execution of this program, the machine state satisfies:
; 0. normal exit.
; 1. the pc is returned to the next instruction of the caller.
; 2. the result -- ISQRT(i), is stored in D0.
; 3. a6, used by LINK, is restored to its original content.
; 4. the stack pointer sp(a7) is updated correctly to pop off one frame.
; the registers not touched by this program still have their original values.
; the memory is correctly changed -- the local effect on memory.
```

THEOREM: isqrt-correctness

```
let  $sn$  be stepn( $s$ , isqrt-t( $i$ ))
in
isqrt-statep( $s, i$ )
→ ((mc-status( $sn$ ) = 'running)
   ∧ (mc-pc( $sn$ ) = rts-addr( $s$ ))
   ∧ (read-rn(32, 14, mc-rfile( $sn$ )) = read-an(32, 6,  $s$ ))
   ∧ (read-rn(32, 15, mc-rfile( $sn$ ))
       = add(32, read-an(32, 7,  $s$ ), 4)))
   ∧ (d2-7a2-5p( $rn$ )
       → (read-rn( $oplen, rn, mc\text{-}rfile(sn)$ )))
```

```

=   read-rn (oplen, rn, mc-rfile (s)))
 $\wedge$  (disjoint (x, k, sub (32, 12, read-sp (s)), 20)
 $\rightarrow$  (read-mem (x, mc-mem (sn), k)
 $=$  read-mem (x, mc-mem (s), k)))
 $\wedge$  (iread-dn (32, 0, sn) = isqrt (i))) endlet

```

EVENT: Disable isqrt-t.

; start to prove isqrt correct.

THEOREM: isqrt1-lower-bound
 $(j \not\geq 0) \rightarrow (i \not\leq \text{sq}(\text{isqrt1}(i, j)))$

THEOREM: quotient-by-2
 $((x \div 2) + (x \div 2)) \not\leq (x - 1)$

THEOREM: main-trick
 $\text{sq}(1 + ((j + k) \div 2)) \not\leq ((j * k) + j)$

THEOREM: sq-add1-non-zero
 $\text{sq}(1 + x) \neq 0$

THEOREM: main
 $(j \not\geq 0) \rightarrow (i < \text{sq}(1 + ((j + (i \div j)) \div 2)))$

THEOREM: isqrt1-upper-bound
 $(i < \text{sq}(1 + j)) \rightarrow (i < \text{sq}(1 + \text{isqrt1}(i, j)))$

THEOREM: isqrt->isqrt1
 $(1 < i) \rightarrow (i < \text{sq}(1 + (i \div 2)))$

; (isqrt i) is the square root of i: $(\text{isqrt } i)^2 \leq i < [(\text{isqrt } i)+1]^2$.

THEOREM: isqrt-logic-correctness
 $(1 < i) \rightarrow ((i < \text{sq}(1 + \text{isqrt}(i))) \wedge (i \not\leq \text{sq}(\text{isqrt}(i))))$

; a simple time analysis.

THEOREM: quotient-mono-1
 $((y \leq z) \wedge (y \not\geq 0) \wedge (z \not\geq 0)) \rightarrow ((x \div y) \not\leq (x \div z))$

THEOREM: mean-lessp-1
 $(x \not\leq y) \rightarrow (x \not\leq ((x + y) \div 2))$

THEOREM: isqrt1-t-la-0
let $j1$ **be** $(j + (i \div j)) \div 2$
in
 $((i \div j) \leq j) \wedge (1 < i) \wedge (0 < j)$
 $\rightarrow ((x - (i \div j)) \not\prec (x - (i \div j1)))$ **endlet**

THEOREM: mean-difference-1-1
 $(i \leq j) \rightarrow (((j + i) \div 2) - i) = ((j - i) \div 2)$

THEOREM: isqrt1-t-la-1
let $j1$ **be** $(j + (i \div j)) \div 2$
in
 $((i \div j) < j) \wedge (1 < i) \wedge (0 < j)$
 $\rightarrow (((j - (i \div j)) \div 2) \not\prec (j1 - (i \div j1)))$ **endlet**

THEOREM: isqrt1-t-la-2
let $j1$ **be** $(j + (i \div j)) \div 2$
in
 $((i \div j) < j) \wedge (1 < i) \wedge (0 < j)$
 $\rightarrow (\log(2, (j - (i \div j)) \div 2) \not\prec \log(2, j1 - (i \div j1)))$ **endlet**

THEOREM: isqrt-t1-ubound
 $((1 < i) \wedge (0 < j))$
 $\rightarrow ((18 + (10 * \log(2, j - (i \div j)))) \not\prec \text{isqrt1-t}(i, j))$

THEOREM: isqrt-t->isqrt1-t
 $(1 < i) \rightarrow ((4 + \text{isqrt1-t}(i, i \div 2)) \not\prec \text{isqrt-t}(i))$

THEOREM: log-mono
 $\log(b, y) \not\prec \log(b, y - x)$

THEOREM: isqrt-t-ubound-la
 $(1 < i) \rightarrow ((12 + (10 * \log(2, i))) \not\prec \text{isqrt-t}(i))$

THEOREM: isqrt-t-ubound
 $((i < \exp(2, 31)) \wedge (1 < i)) \rightarrow (\text{isqrt-t}(i) \leq 322)$

Index

- add, 6–8
- d2-7a2-5p, 6–8
- disjoint, 6–9
- evenp, 5
- exp, 10
- ilessp, 5
- initial-j-int-rangep, 5
- int-rangep, 4, 5
- iread-dn, 5, 7–9
- isqrt, 3, 9
- isqrt->isqrt1, 9
- isqrt-code, 3, 5
- isqrt-correctness, 8
- isqrt-induct, 5
- isqrt-logic-correctness, 9
- isqrt-no-overflow, 4
- isqrt-s-exit, 5
- isqrt-s-exit-mem, 6
- isqrt-s-exit-rfile, 6
- isqrt-s-s0, 6
- isqrt-s-s0-mem, 6
- isqrt-s-s0-rfile, 6
- isqrt-s0-exit, 8
- isqrt-s0-exit-base, 6
- isqrt-s0-exit-mem, 8
- isqrt-s0-exit-rfile, 8
- isqrt-s0-s0, 7
- isqrt-s0-s0-mem, 7
- isqrt-s0-s0-rfile, 7
- isqrt-s0p, 5–8
- isqrt-s0p-j_nonzerop, 7
- isqrt-statep, 5, 6, 8
- isqrt-t, 4, 8, 10
- isqrt-t->isqrt1-t, 10
- isqrt-t-ubound, 10
- isqrt-t-ubound-la, 10
- isqrt-t1-ubound, 10
- isqrt1, 3, 8, 9
- isqrt1-lower-bound, 9
- isqrt1-s0-exit-mem-base, 7
- isqrt1-s0-exit-rfile-base, 7
- isqrt1-t, 3, 4, 8, 10
- isqrt1-t-la-0, 10
- isqrt1-t-la-1, 10
- isqrt1-t-la-2, 10
- isqrt1-upper-bound, 9
- j-int-rangep, 4
- j-nonzerop, 4
- linked-a6, 6–8
- linked-rts-addr, 6–8
- log, 10
- log-mono, 10
- main, 9
- main-trick, 9
- mc-mem, 5–9
- mc-pc, 5, 6, 8
- mc-rfile, 5–9
- mc-status, 5, 6, 8
- mcode-addrp, 5
- mean-difference-1-1, 10
- mean-lessp-1, 9
- quotient-by-2, 9
- quotient-mono-1, 9
- ram-addrp, 5
- read-an, 5–8
- read-mem, 6–9
- read-rn, 5–9
- read-sp, 5, 6, 9
- rom-addrp, 5
- rts-addr, 5, 6, 8
- splus, 4
- sq, 3, 4, 9
- sq-add1-non-zero, 9
- stepn, 5–8
- sub, 5–9