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|#

EVENT: Start with the library "strcmp" using the compiled version.

```
;           Proof of the Correctness of the STRCOLL Function
|#
```

This is part of our effort to verify the Berkeley string library. The Berkeley string library is widely used as part of the Berkeley Unix OS.

This is the source code of strcoll function in the Berkeley string library.

```
/*
 * Compare strings according to LC_COLLATE category of current locale.
 */
strcoll(s1, s2)
    const char *s1, *s2;
{
    /* LC_COLLATE is unimplemented, hence always "C" */
    return (strcmp(s1, s2));
}
```

The MC68020 assembly code of the C function strcoll on SUN-3 is given as follows. This binary is generated by "gcc -O".

```

0x2388 <strcoll>:    linkw fp,#0
0x238c <strcoll+4>:   movev fp@(12),sp@-
0x2390 <strcoll+8>:   movev fp@(8),sp@-
0x2394 <strcoll+12>:  jsr @#0x2358 <strcmp>
0x239a <strcoll+18>:  unlk fp
0x239c <strcoll+20>:  rts

```

The machine code of the above program is:

```

<strcoll>: 0x4e56 0x0000 0x2f2e 0x000c 0x2f2e 0x0008 0x4eb9 0x0000
<strcoll+16>: 0x2358 0x4e5e 0x4e75

'(78     86      0       0       47      46      0       12
 47     46      0       8       78      185     0       0
 35     88      78      94      78      117)
|#
;
```

; in the logic, the above program is defined by (strcoll-code).

DEFINITION:

```

STRCOLL-CODE
= '(78 86 0 0 47 46 0 12 47 46 0 8 78 185 -1 -1 -1 -1 78
  94 78 117)
```

CONSERVATIVE AXIOM: strcoll-load

strcoll-loadp ( $s$ )

```

= (evenp (STRCOLL-ADDR)
  ∧ (STRCOLL-ADDR ∈  $\mathbb{N}$ )
  ∧ nat-rangep (STRCOLL-ADDR, 32)
  ∧ rom-addrp (STRCOLL-ADDR, mc-mem ( $s$ ), 22)
  ∧ mcode-addrp (STRCOLL-ADDR, mc-mem ( $s$ ), STRCOLL-CODE)
  ∧ strcmp-loadp ( $s$ )
  ∧ (pc-read-mem (add (32, STRCOLL-ADDR, 14), mc-mem ( $s$ ), 4)
    = STRCMP-ADDR))
```

Simultaneously, we introduce the new function symbols *strcoll-loadp* and *strcoll-addr*.

THEOREM: stepn-strcoll-loadp

strcoll-loadp (stepn ( $s$ ,  $n$ )) = strcoll-loadp ( $s$ )

; the computation time of the program.

DEFINITION:

$\text{strcoll-t}(n1, lst1, lst2) = \text{splus}(4, \text{splus}(\text{strcmp-t}(n1, lst1, lst2), 2))$

; the preconditions of the initial state.

DEFINITION:

$\text{strcoll-statep}(s, str1, n1, lst1, str2, n2, lst2)$   
 $= ((\text{mc-status}(s) = \text{'running})$   
 $\wedge \text{strcoll-loadp}(s)$   
 $\wedge (\text{mc-pc}(s) = \text{STRCOLL-ADDR})$   
 $\wedge \text{ram-addrp}(\text{sub}(32, 24, \text{read-sp}(s)), \text{mc-mem}(s), 36)$   
 $\wedge \text{ram-addrp}(str1, \text{mc-mem}(s), n1)$   
 $\wedge \text{mem-lst}(1, str1, \text{mc-mem}(s), n1, lst1)$   
 $\wedge \text{ram-addrp}(str2, \text{mc-mem}(s), n2)$   
 $\wedge \text{mem-lst}(1, str2, \text{mc-mem}(s), n2, lst2)$   
 $\wedge \text{disjoint}(\text{sub}(32, 24, \text{read-sp}(s)), 36, str1, n1)$   
 $\wedge \text{disjoint}(\text{sub}(32, 24, \text{read-sp}(s)), 36, str2, n2)$   
 $\wedge (str1 = \text{read-mem}(\text{add}(32, \text{read-sp}(s), 4), \text{mc-mem}(s), 4))$   
 $\wedge (str2 = \text{read-mem}(\text{add}(32, \text{read-sp}(s), 8), \text{mc-mem}(s), 4))$   
 $\wedge \text{stringp}(0, n1, lst1)$   
 $\wedge (n1 \leq n2)$   
 $\wedge (n1 \in \mathbf{N})$   
 $\wedge (n2 \in \mathbf{N})$   
 $\wedge \text{uint-rangep}(n2, 32))$

; the intermediate state right before the execution of the subroutine strcmp.

DEFINITION:

$\text{strcoll-s0p}(s, str1, n1, lst1, str2, n2, lst2)$   
 $= ((\text{mc-status}(s) = \text{'running})$   
 $\wedge \text{strcoll-loadp}(s)$   
 $\wedge (\text{mc-pc}(s) = \text{STRCMP-ADDR})$   
 $\wedge (\text{rts-addr}(s) = \text{add}(32, \text{STRCOLL-ADDR}, 18))$   
 $\wedge \text{ram-addrp}(\text{sub}(32, 20, \text{read-an}(32, 6, s)), \text{mc-mem}(s), 36)$   
 $\wedge \text{ram-addrp}(str1, \text{mc-mem}(s), n1)$   
 $\wedge \text{mem-lst}(1, str1, \text{mc-mem}(s), n1, lst1)$   
 $\wedge \text{ram-addrp}(str2, \text{mc-mem}(s), n2)$   
 $\wedge \text{mem-lst}(1, str2, \text{mc-mem}(s), n2, lst2)$   
 $\wedge \text{disjoint}(\text{sub}(32, 20, \text{read-an}(32, 6, s)), 36, str1, n1)$   
 $\wedge \text{disjoint}(\text{sub}(32, 20, \text{read-an}(32, 6, s)), 36, str2, n2)$   
 $\wedge (str1 = \text{read-mem}(\text{add}(32, \text{read-sp}(s), 4), \text{mc-mem}(s), 4))$   
 $\wedge (str2 = \text{read-mem}(\text{add}(32, \text{read-sp}(s), 8), \text{mc-mem}(s), 4))$   
 $\wedge \text{equal}^*(\text{read-sp}(s), \text{sub}(32, 12, \text{read-an}(32, 6, s)))$

```

 $\wedge \text{stringp}(0, n1, lst1)$ 
 $\wedge (n1 \leq n2)$ 
 $\wedge (n1 \in \mathbf{N})$ 
 $\wedge (n2 \in \mathbf{N})$ 
 $\wedge \text{uint-rangep}(n2, 32))$ 

; the intermediate state right after the execution of the subroutine strcmp.

```

DEFINITION:

```

strcoll-s1p( $s, str1, n1, lst1, str2, n2, lst2$ )
= ((mc-status( $s$ ) = 'running)
 $\wedge \text{strcoll-loadp}(s)$ 
 $\wedge (\text{mc-pc}(s) = \text{add}(32, \text{STRCOLL-ADDR}, 18))$ 
 $\wedge \text{ram-addrp}(\text{sub}(32, 20, \text{read-an}(32, 6, s)), \text{mc-mem}(s), 36)$ 
 $\wedge (\text{iread-dn}(32, 0, s) = \text{strcmp}(0, n1, lst1, lst2)))$ 

```

```
; from the initial state s to s0: s --> s0.
```

THEOREM: strcoll-s-s0

```

strcoll-statep( $s, str1, n1, lst1, str2, n2, lst2$ )
 $\rightarrow \text{strcoll-s0p}(\text{stepn}(s, 4), str1, n1, lst1, str2, n2, lst2)$ 

```

THEOREM: strcoll-s-s0-else

```

strcoll-statep( $s, str1, n1, lst1, str2, n2, lst2$ )
 $\rightarrow ((\text{linked-rts-addr}(\text{stepn}(s, 4)) = \text{rts-addr}(s))$ 
 $\wedge (\text{linked-a6}(\text{stepn}(s, 4)) = \text{read-an}(32, 6, s))$ 
 $\wedge (\text{read-rn}(32, 14, \text{mc-rfile}(\text{stepn}(s, 4))))$ 
 $= \text{sub}(32, 4, \text{read-sp}(s)))$ 

```

THEOREM: strcoll-s-s0-rfile

```

(strcoll-statep( $s, str1, n1, lst1, str2, n2, lst2$ )  $\wedge \text{d2-7a2-5p}(rn))$ 
 $\rightarrow (\text{read-rn}(oplen, rn, \text{mc-rfile}(\text{stepn}(s, 4))))$ 
 $= \text{read-rn}(oplen, rn, \text{mc-rfile}(s)))$ 

```

THEOREM: strcoll-s-s0-mem

```

(strcoll-statep( $s, str1, n1, lst1, str2, n2, lst2$ )
 $\wedge \text{disjoint}(x, k, \text{sub}(32, 24, \text{read-sp}(s)), 36))$ 
 $\rightarrow (\text{read-mem}(x, \text{mc-mem}(\text{stepn}(s, 4)), k) = \text{read-mem}(x, \text{mc-mem}(s), k))$ 

```

```
; from s0 to s1: s0 --> s1. by strcmp.
```

THEOREM: strcoll-s0p-strcmp-statep

```

strcoll-s0p( $s, str1, n1, lst1, str2, n2, lst2$ )
 $\rightarrow \text{strcmp-statep}(s, str1, n1, lst1, str2, n2, lst2)$ 

```

THEOREM: strcoll-s0-s1  
**let**  $s_1$  **be** stepn( $s$ , strcmp-t( $n_1, lst_1, lst_2$ ))  
**in**  
 strcoll-s0p( $s, str_1, n_1, lst_1, str_2, n_2, lst_2$ )  
 $\rightarrow$  strcoll-s1p( $s_1, str_1, n_1, lst_1, str_2, n_2, lst_2$ ) **endlet**

THEOREM: strcoll-s0-s1-else  
**let**  $s_1$  **be** stepn( $s$ , strcmp-t( $n_1, lst_1, lst_2$ ))  
**in**  
 strcoll-s0p( $s, str_1, n_1, lst_1, str_2, n_2, lst_2$ )  
 $\rightarrow$  ((read-rn(32, 14, mc-rfile( $s_1$ )))  
 $=$  read-rn(32, 14, mc-rfile( $s$ )))  
 $\wedge$  (linked-rts-addr( $s_1$ ) = linked-rts-addr( $s$ ))  
 $\wedge$  (linked-a6( $s_1$ ) = linked-a6( $s$ ))) **endlet**

THEOREM: strcoll-s0-s1-rfile  
**let**  $s_1$  **be** stepn( $s$ , strcmp-t( $n_1, lst_1, lst_2$ ))  
**in**  
 (strcoll-s0p( $s, str_1, n_1, lst_1, str_2, n_2, lst_2$ )  
 $\wedge$  d2-7a2-5p( $rn$ )  
 $\wedge$  ( $oplen \leq 32$ ))  
 $\rightarrow$  (read-rn( $oplen, rn, mc\text{-rfile}(s_1)$ )  
 $=$  read-rn( $oplen, rn, mc\text{-rfile}(s)$ )) **endlet**

THEOREM: strcoll-s0-s1-mem  
**let**  $s_1$  **be** stepn( $s$ , strcmp-t( $n_1, lst_1, lst_2$ ))  
**in**  
 (strcoll-s0p( $s, str_1, n_1, lst_1, str_2, n_2, lst_2$ )  
 $\wedge$  disjoint( $x, k, sub(32, 20, read-an(32, 6, s)), 36$ ))  
 $\rightarrow$  (read-mem( $x, mc\text{-mem}(s_1), k$ ) = read-mem( $x, mc\text{-mem}(s), k$ )) **endlet**  
;  
**from**  $s_1$  **to exit:**  $s_1 \dashrightarrow sn$ .

THEOREM: strcoll-s1-sn  
 strcoll-s1p( $s, str_1, n_1, lst_1, str_2, n_2, lst_2$ )  
 $\rightarrow$  ((mc-status(stepn( $s, 2$ )) = 'running)  
 $\wedge$  (mc-pc(stepn( $s, 2$ )) = linked-rts-addr( $s$ ))  
 $\wedge$  (iread-dn(32, 0, stepn( $s, 2$ )) = strcmp(0,  $n_1, lst_1, lst_2$ ))  
 $\wedge$  (read-rn(32, 14, mc-rfile(stepn( $s, 2$ ))) = linked-a6( $s$ ))  
 $\wedge$  (read-rn(32, 15, mc-rfile(stepn( $s, 2$ )))  
 $=$  add(32, read-an(32, 6,  $s$ ), 8))  
 $\wedge$  (read-mem( $x, mc\text{-mem}(stepn(s, 2)), k$ )  
 $=$  read-mem( $x, mc\text{-mem}(s), k$ )))

THEOREM: strcoll-s1-sn-rfile

```

(strcoll-s1p(s, str1, n1, lst1, str2, n2, lst2) ∧ d2-7a2-5p(rn))
→ (read-rn(oplen, rn, mc-rfile(stepn(s, 2))))
= read-rn(oplen, rn, mc-rfile(s)))

```

; the correctness of strcoll.

THEOREM: strcoll-correctness

```

let sn be stepn(s, strcoll-t(n1, lst1, lst2))
in
strcoll-statep(s, str1, n1, lst1, str2, n2, lst2)
→ ((mc-status(sn) = 'running)
   ∧ (mc-pc(sn) = rts-addr(s))
   ∧ (read-rn(32, 14, mc-rfile(sn))
      = read-rn(32, 14, mc-rfile(s)))
   ∧ (read-rn(32, 15, mc-rfile(sn))
      = add(32, read-an(32, 7, s), 4))
   ∧ ((d2-7a2-5p(rn) ∧ (oplen ≤ 32))
      → (read-rn(oplen, rn, mc-rfile(sn))
          = read-rn(oplen, rn, mc-rfile(s))))
   ∧ (disjoint(x, k, sub(32, 24, read-sp(s)), 36)
      → (read-mem(x, mc-mem(sn), k)
          = read-mem(x, mc-mem(s), k)))
   ∧ (iread-dn(32, 0, sn) = strcoll(n1, lst1, lst2))) endlet

```

EVENT: Disable strcoll-t.

```

; some properties of strcoll.
; the same as strcmp.

```

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