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|#

EVENT: Start with the library "mc20-2" using the compiled version.

; Proof of the Correctness of the STRLEN Function

#|

This is part of our effort to verify the Berkeley string library. The Berkeley string library is widely used as part of the Berkeley Unix OS.

This is the source code of strlen function in the Berkeley string library.

```
/* find the length of str[] */
size_t
strlen(str)
const char *str;
{
    register const char *s;

    for (s = str; *s; ++s);
    return(s - str);
}
```

```
}

```

The MC68020 assembly code of the C function `strlen` on SUN-3 is given as follows. This binary is generated by "gcc -O".

```
0x25b0 <strlen>:      linkw fp,#0
0x25b4 <strlen+4>:     movel fp@(8),d0
0x25b8 <strlen+8>:     moveal d0,a0
0x25ba <strlen+10>:    tstb a0@
0x25bc <strlen+12>:    beq 0x25c4 <strlen+20>
0x25be <strlen+14>:    addqw #1,a0
0x25c0 <strlen+16>:    tstb a0@
0x25c2 <strlen+18>:    bne 0x25be <strlen+14>
0x25c4 <strlen+20>:    subl a0,d0
0x25c6 <strlen+22>:    negl d0
0x25c8 <strlen+24>:    unlk fp
0x25ca <strlen+26>:    rts

```

The machine code of the above program is:

```
<strlen>:      0x4e56  0x0000  0x202e  0x0008  0x2040  0x4a10  0x6706  0x5248
<strlen+16>:  0x4a10  0x66fa  0x9088  0x4480  0x4e5e  0x4e75

```

```
'(78      86      0      0      32      46      0      8
   32      64      74      16     103      6      82      72
   74      16     102     250     144     136     68     128
   78      94      78     117)
|#

```

; in the logic, the above program is defined by (strlen-code).

DEFINITION:

STRLEN-CODE

```
= '(78 86 0 0 32 46 0 8 32 64 74 16 103 6 82 72 74 16
    102 250 144 136 68 128 78 94 78 117)

```

CONSERVATIVE AXIOM: `strlen-load`

`strlen-loadp(s)`

```
= (evenp (STRLEN-ADDR)
   ∧ (STRLEN-ADDR ∈ ℕ)
   ∧ nat-range (STRLEN-ADDR, 32)
   ∧ rom-addrp (STRLEN-ADDR, mc-mem(s), 28)
   ∧ mcode-addrp (STRLEN-ADDR, mc-mem(s), STRLEN-CODE))

```

Simultaneously, we introduce the new function symbols *strlen-loadp* and *strlen-*

addr.

THEOREM: $\text{stepn-strlen-loadp}$
 $\text{strlen-loadp}(\text{stepn}(s, n)) = \text{strlen-loadp}(s)$

; the computation time of the program.

DEFINITION:

$\text{strlen1-t}(i, n, lst)$
 $=$ **if** $i < n$
 then if $\text{get-nth}(i, lst) = \text{NULL}$ **then** 6
 else $\text{splus}(3, \text{strlen1-t}(1 + i, n, lst))$ **endif**
 else 0 **endif**

DEFINITION:

$\text{strlen-t}(n, lst)$
 $=$ **if** $\text{get-nth}(0, lst) = 0$ **then** 9
 else $\text{splus}(6, \text{strlen1-t}(1, n, lst))$ **endif**

; an induction hint.

DEFINITION:

$\text{strlen-induct}(s, i^*, i, n, lst)$
 $=$ **if** $i < n$
 then if $\text{get-nth}(i, lst) = \text{NULL}$ **then** t
 else $\text{strlen-induct}(\text{stepn}(s, 3), \text{add}(32, i^*, 1), 1 + i, n, lst)$ **endif**
 else t **endif**

; the preconditions of the initial state.

DEFINITION:

$\text{strlen-statep}(s, str, n, lst)$
 $=$ $((\text{mc-status}(s) = \text{'running'})$
 $\wedge \text{evenp}(\text{mc-pc}(s))$
 $\wedge \text{rom-addrp}(\text{mc-pc}(s), \text{mc-mem}(s), 28)$
 $\wedge \text{mcode-addrp}(\text{mc-pc}(s), \text{mc-mem}(s), \text{STRLEN-CODE})$
 $\wedge \text{ram-addrp}(\text{sub}(32, 4, \text{read-sp}(s)), \text{mc-mem}(s), 12)$
 $\wedge \text{ram-addrp}(str, \text{mc-mem}(s), n)$
 $\wedge \text{mem-lst}(1, str, \text{mc-mem}(s), n, lst)$
 $\wedge \text{disjoint}(str, n, \text{sub}(32, 4, \text{read-sp}(s)), 12)$
 $\wedge (str = \text{read-mem}(\text{add}(32, \text{read-sp}(s), 4), \text{mc-mem}(s), 4))$
 $\wedge (\text{slen}(0, n, lst) < n)$
 $\wedge (n \in \mathbf{N})$
 $\wedge \text{uint-rangep}(n, 32))$

; an intermediate state.

DEFINITION:

$\text{strlen-s0p}(s, \text{str}, i^*, i, n, \text{lst})$
 $= ((\text{mc-status}(s) = \text{'running'})$
 $\wedge \text{evenp}(\text{mc-pc}(s))$
 $\wedge \text{rom-addrp}(\text{sub}(32, 16, \text{mc-pc}(s)), \text{mc-mem}(s), 28)$
 $\wedge \text{mcode-addrp}(\text{sub}(32, 16, \text{mc-pc}(s)), \text{mc-mem}(s), \text{STRLEN-CODE})$
 $\wedge \text{ram-addrp}(\text{read-an}(32, 6, s), \text{mc-mem}(s), 12)$
 $\wedge \text{ram-addrp}(\text{str}, \text{mc-mem}(s), n)$
 $\wedge \text{mem-lst}(1, \text{str}, \text{mc-mem}(s), n, \text{lst})$
 $\wedge \text{disjoint}(\text{str}, n, \text{read-an}(32, 6, s), 12)$
 $\wedge \text{equal}^*(\text{read-an}(32, 0, s), \text{add}(32, \text{str}, i^*))$
 $\wedge (\text{str} = \text{read-dn}(32, 0, s))$
 $\wedge (i = \text{nat-to-uint}(i^*))$
 $\wedge (\text{slen}(i, n, \text{lst}) < n)$
 $\wedge (i^* \in \mathbf{N})$
 $\wedge \text{nat-range}p(i^*, 32)$
 $\wedge (n \in \mathbf{N})$
 $\wedge \text{uint-range}p(n, 32))$

; from the initial state s to exit: $s \dashrightarrow \text{sn}$.

THEOREM: strlen-s-sn

$(\text{strlen-statep}(s, \text{str}, n, \text{lst}) \wedge (\text{get-nth}(0, \text{lst}) = 0))$
 $\rightarrow ((\text{mc-status}(\text{stepn}(s, 9)) = \text{'running'})$
 $\wedge (\text{mc-pc}(\text{stepn}(s, 9)) = \text{rts-addr}(s))$
 $\wedge (\text{nat-to-uint}(\text{read-dn}(32, 0, \text{stepn}(s, 9))) = 0)$
 $\wedge (\text{read-rn}(32, 15, \text{mc-rfile}(\text{stepn}(s, 9)))$
 $\quad = \text{add}(32, \text{read-an}(32, 7, s), 4))$
 $\wedge (\text{read-rn}(32, 14, \text{mc-rfile}(\text{stepn}(s, 9))) = \text{read-an}(32, 6, s)))$

THEOREM: strlen-s-sn-rfile

$(\text{strlen-statep}(s, \text{str}, n, \text{lst}) \wedge (\text{get-nth}(0, \text{lst}) = 0) \wedge \text{d2-7a2-5p}(rn))$
 $\rightarrow (\text{read-rn}(\text{oplen}, rn, \text{mc-rfile}(\text{stepn}(s, 9)))$
 $\quad = \text{read-rn}(\text{oplen}, rn, \text{mc-rfile}(s)))$

THEOREM: strlen-s-sn-mem

$(\text{strlen-statep}(s, \text{str}, n, \text{lst})$
 $\wedge (\text{get-nth}(0, \text{lst}) = 0)$
 $\wedge \text{disjoint}(x, k, \text{sub}(32, 4, \text{read-sp}(s)), 12))$
 $\rightarrow (\text{read-mem}(x, \text{mc-mem}(\text{stepn}(s, 9)), k) = \text{read-mem}(x, \text{mc-mem}(s), k))$

; from the initial state s to $s0$: $s \dashrightarrow s0$.

THEOREM: strlen-s-s0

$(\text{strlen-statep}(s, \text{str}, n, \text{lst}) \wedge (\text{get-nth}(0, \text{lst}) \neq 0))$
 $\rightarrow \text{strlen-s0p}(\text{stepn}(s, 6), \text{str}, 1, 1, n, \text{lst})$

THEOREM: strlen-s-s0-else

$$\begin{aligned}
& (\text{strlen-statep}(s, \text{str}, n, \text{lst}) \wedge (\text{get-nth}(0, \text{lst}) \neq 0)) \\
\rightarrow & ((\text{linked-rtts-addr}(\text{stepn}(s, 6)) = \text{rts-addr}(s)) \\
& \wedge (\text{linked-a6}(\text{stepn}(s, 6)) = \text{read-an}(32, 6, s)) \\
& \wedge (\text{read-rn}(32, 14, \text{mc-rfile}(\text{stepn}(s, 6))) \\
& = \text{sub}(32, 4, \text{read-sp}(s))))
\end{aligned}$$

THEOREM: strlen-s-s0-rfile

$$\begin{aligned}
& (\text{strlen-statep}(s, \text{str}, n, \text{lst}) \wedge (\text{get-nth}(0, \text{lst}) \neq 0) \wedge \text{d2-7a2-5p}(rn)) \\
\rightarrow & (\text{read-rn}(\text{oplen}, rn, \text{mc-rfile}(\text{stepn}(s, 6))) \\
& = \text{read-rn}(\text{oplen}, rn, \text{mc-rfile}(s)))
\end{aligned}$$

THEOREM: strlen-s-s0-mem

$$\begin{aligned}
& (\text{strlen-statep}(s, \text{str}, n, \text{lst}) \\
& \wedge (\text{get-nth}(0, \text{lst}) \neq 0) \\
& \wedge \text{disjoint}(x, k, \text{sub}(32, 4, \text{read-sp}(s)), 12)) \\
\rightarrow & (\text{read-mem}(x, \text{mc-mem}(\text{stepn}(s, 6)), k) = \text{read-mem}(x, \text{mc-mem}(s), k))
\end{aligned}$$

; from s0 to exit (base case), from s0 to s0 (induction case).
; base case: s0 --> exit.

THEOREM: strlen-s0-sn-base

$$\begin{aligned}
& (\text{strlen-s0p}(s, \text{str}, i^*, i, n, \text{lst}) \wedge (\text{get-nth}(i, \text{lst}) = 0)) \\
\rightarrow & ((\text{mc-status}(\text{stepn}(s, 6)) = \text{'running'}) \\
& \wedge (\text{mc-pc}(\text{stepn}(s, 6)) = \text{linked-rtts-addr}(s)) \\
& \wedge (\text{nat-to-uint}(\text{read-dn}(32, 0, \text{stepn}(s, 6))) = i) \\
& \wedge (\text{read-rn}(32, 14, \text{mc-rfile}(\text{stepn}(s, 6))) = \text{linked-a6}(s)) \\
& \wedge (\text{read-rn}(32, 15, \text{mc-rfile}(\text{stepn}(s, 6))) \\
& = \text{add}(32, \text{read-an}(32, 6, s), 8)) \\
& \wedge (\text{read-mem}(x, \text{mc-mem}(\text{stepn}(s, 6)), k) \\
& = \text{read-mem}(x, \text{mc-mem}(s), k)))
\end{aligned}$$

THEOREM: strlen-s0-sn-rfile-base

$$\begin{aligned}
& (\text{strlen-s0p}(s, \text{str}, i^*, i, n, \text{lst}) \wedge \text{d2-7a2-5p}(rn) \wedge (\text{get-nth}(i, \text{lst}) = 0)) \\
\rightarrow & (\text{read-rn}(\text{oplen}, rn, \text{mc-rfile}(\text{stepn}(s, 6))) \\
& = \text{read-rn}(\text{oplen}, rn, \text{mc-rfile}(s)))
\end{aligned}$$

; induction case: s0 --> s0.

THEOREM: strlen-s0-s0

$$\begin{aligned}
& (\text{strlen-s0p}(s, \text{str}, i^*, i, n, \text{lst}) \wedge (\text{get-nth}(i, \text{lst}) \neq 0)) \\
\rightarrow & (\text{strlen-s0p}(\text{stepn}(s, 3), \text{str}, \text{add}(32, i^*, 1), 1 + i, n, \text{lst}) \\
& \wedge (\text{read-rn}(32, 14, \text{mc-rfile}(\text{stepn}(s, 3))) \\
& = \text{read-rn}(32, 14, \text{mc-rfile}(s))) \\
& \wedge (\text{linked-a6}(\text{stepn}(s, 3)) = \text{linked-a6}(s))
\end{aligned}$$

$$\begin{aligned}
& \wedge \text{ (linked-rtts-addr (stepn } (s, 3)) = \text{linked-rtts-addr } (s)) \\
& \wedge \text{ (read-mem } (x, \text{mc-mem (stepn } (s, 3)), k) \\
& \quad = \text{ read-mem } (x, \text{mc-mem } (s), k)))
\end{aligned}$$

THEOREM: strlen-s0-s0-rfile

$$\begin{aligned}
& (\text{strlen-s0p } (s, \text{str}, i^*, i, n, \text{lst}) \wedge \text{d2-7a2-5p } (rn) \wedge (\text{get-nth } (i, \text{lst}) \neq 0)) \\
& \rightarrow (\text{read-rn } (o\text{plen}, rn, \text{mc-rfile (stepn } (s, 3))) \\
& \quad = \text{ read-rn } (o\text{plen}, rn, \text{mc-rfile } (s)))
\end{aligned}$$

; put together (s0 --> exit).

THEOREM: strlen-s0p-info

$$\text{strlen-s0p } (s, \text{str}, i^*, i, n, \text{lst}) \rightarrow ((i < n) = \mathbf{t})$$

THEOREM: strlen-s0-sn

$$\begin{aligned}
& \mathbf{let} \text{ } sn \text{ } \mathbf{be} \text{ } \text{stepn } (s, \text{strlen1-t } (i, n, \text{lst})) \\
& \mathbf{in} \\
& \text{strlen-s0p } (s, \text{str}, i^*, i, n, \text{lst}) \\
& \rightarrow ((\text{mc-status } (sn) = \mathbf{'running'}) \\
& \quad \wedge (\text{mc-pc } (sn) = \text{linked-rtts-addr } (s)) \\
& \quad \wedge (\text{nat-to-uint } (\text{read-dn } (32, 0, sn)) = \text{strlen } (i, n, \text{lst})) \\
& \quad \wedge (\text{read-rn } (32, 14, \text{mc-rfile } (sn)) = \text{linked-a6 } (s)) \\
& \quad \wedge (\text{read-rn } (32, 15, \text{mc-rfile } (sn)) \\
& \quad \quad = \text{ add } (32, \text{read-an } (32, 6, s), 8)) \\
& \quad \wedge (\text{read-mem } (x, \text{mc-mem } (sn), k) = \text{read-mem } (x, \text{mc-mem } (s), k))) \mathbf{endlet}
\end{aligned}$$

EVENT: Disable strlen-s0p-info.

THEOREM: strlen-s0-sn-rfile

$$\begin{aligned}
& (\text{strlen-s0p } (s, \text{str}, i^*, i, n, \text{lst}) \wedge \text{d2-7a2-5p } (rn)) \\
& \rightarrow (\text{read-rn } (o\text{plen}, rn, \text{mc-rfile (stepn } (s, \text{strlen1-t } (i, n, \text{lst})))) \\
& \quad = \text{ read-rn } (o\text{plen}, rn, \text{mc-rfile } (s)))
\end{aligned}$$

; the correctness of the STRLEN program.

THEOREM: strlen-statep-info

$$\text{strlen-statep } (s, \text{str}, n, \text{lst}) \rightarrow (n \neq 0)$$

THEOREM: strlen-correctness

$$\begin{aligned}
& \mathbf{let} \text{ } sn \text{ } \mathbf{be} \text{ } \text{stepn } (s, \text{strlen-t } (n, \text{lst})) \\
& \mathbf{in} \\
& \text{strlen-statep } (s, \text{str}, n, \text{lst}) \\
& \rightarrow ((\text{mc-status } (sn) = \mathbf{'running'}) \\
& \quad \wedge (\text{mc-pc } (sn) = \text{rts-addr } (s)) \\
& \quad \wedge (\text{read-rn } (32, 14, \text{mc-rfile } (sn))
\end{aligned}$$

```

      = read-rn (32, 14, mc-rfile (s)))
 $\wedge$  (read-rn (32, 15, mc-rfile (sn))
      = add (32, read-sp (s), 4))
 $\wedge$  (d2-7a2-5p (rn)
       $\rightarrow$  (read-rn (oplen, rn, mc-rfile (sn))
          = read-rn (oplen, rn, mc-rfile (s))))
 $\wedge$  (disjoint (x, k, sub (32, 4, read-sp (s)), 12)
       $\rightarrow$  (read-mem (x, mc-mem (sn), k)
          = read-mem (x, mc-mem (s), k)))
 $\wedge$  (uread-dn (32, 0, sn) = strlen (0, n, lst))) endlet

```

EVENT: Disable strlen-t.

```

; some properties of strlen.
; see the file cstring.events.

```

EVENT: Make the library "**strlen**" and compile it.

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