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|#

EVENT: Start with the library "mc20-2" using the compiled version.

; Proof of the Correctness of the STRNCAT Function

#|

This is part of our effort to verify the Berkeley string library. The Berkeley string library is widely used as part of the Berkeley Unix OS.

This is the source code of strncat function in the Berkeley string library.

```
char *
strncat(dst, src, n)
char *dst;
const char *src;
register size_t n;
{
    if (n != 0) {
        register char *d = dst;
        register const char *s = src;
```

```

while (*d != 0)
d++;
do {
if ((*d = *s++) == 0)
break;
d++;
} while (--n != 0);
*d = 0;
}
return (dst);
}

```

The MC68020 assembly code of the C function `strncat` on SUN-3 is given as follows. This binary is generated by "gcc -O".

```

0x25d0 <strncat>:      linkw fp,#0
0x25d4 <strncat+4>:    movel d2,sp@-
0x25d6 <strncat+6>:    movel fp@(8),d2
0x25da <strncat+10>:   movel fp@(16),d1
0x25de <strncat+14>:   beq 0x25fe <strncat+46>
0x25e0 <strncat+16>:   moveal d2,a0
0x25e2 <strncat+18>:   moveal fp@(12),a1
0x25e6 <strncat+22>:   tstb a0@
0x25e8 <strncat+24>:   beq 0x25f0 <strncat+32>
0x25ea <strncat+26>:   addqw #1,a0
0x25ec <strncat+28>:   tstb a0@
0x25ee <strncat+30>:   bne 0x25ea <strncat+26>
0x25f0 <strncat+32>:   moveb a1@+,d0
0x25f2 <strncat+34>:   moveb d0,a0@
0x25f4 <strncat+36>:   beq 0x25fc <strncat+44>
0x25f6 <strncat+38>:   addqw #1,a0
0x25f8 <strncat+40>:   subl #1,d1
0x25fa <strncat+42>:   bne 0x25f0 <strncat+32>
0x25fc <strncat+44>:   clrb a0@
0x25fe <strncat+46>:   movel d2,d0
0x2600 <strncat+48>:   movel fp@(-4),d2
0x2604 <strncat+52>:   unlk fp
0x2606 <strncat+54>:   rts

```

The machine code of the above program is:

```

<strncat>:      0x4e56  0x0000  0x2f02  0x242e  0x0008  0x222e  0x0010  0x671e
<strncat+16>:   0x2042  0x226e  0x000c  0x4a10  0x6706  0x5248  0x4a10  0x66fa

```

```
<strncat+32>: 0x1019 0x1080 0x6706 0x5248 0x5381 0x66f4 0x4210 0x2002
<strncat+48>: 0x242e 0xffffc 0x4e5e 0x4e75
```

```
'(78      86      0      0      47      2      36      46
  0       8      34      46      0      16     103      30
 32      66      34     110      0      12      74      16
103      6      82      72      74      16     102     250
 16      25      16     128     103      6      82      72
 83     129     102     244      66      16      32      2
 36      46     255     252      78      94      78     117)
|#
```

```
; in the logic, the above program is defined by (strncat-code).
```

DEFINITION:

STRNCAT-CODE

```
= '(78 86 0 0 47 2 36 46 0 8 34 46 0 16 103 30 32 66 34
    110 0 12 74 16 103 6 82 72 74 16 102 250 16 25 16
    128 103 6 82 72 83 129 102 244 66 16 32 2 36 46 255
    252 78 94 78 117)
```

```
; the computation time of the program.
```

DEFINITION:

strncat-t0(*i*, *n1*, *lst1*)

```
=  if i < n1
    then if get-nth(i, lst1) = 0 then 2
        else splus(3, strncat-t0(1 + i, n1, lst1)) endif
    else 0 endif
```

DEFINITION:

strncat-t1(*n1*, *lst1*) = splus(10, strncat-t0(1, *n1*, *lst1*))

DEFINITION:

strncat-t2(*j*, *n*, *lst2*)

```
=  if get-nth(j, lst2) = 0 then 8
    elseif (n - 1) = 0 then 11
    else splus(6, strncat-t2(1 + j, n - 1, lst2)) endif
```

DEFINITION:

strncat-t(*n1*, *lst1*, *n*, *lst2*)

```
=  if n = 0 then 9
    elseif get-nth(0, lst1) = 0 then splus(9, strncat-t2(0, n, lst2))
    else splus(strncat-t1(n1, lst1), strncat-t2(0, n, lst2)) endif
```

```
; two induction hints.
```

DEFINITION:

```

strncat-induct0(s, i*, i, n1, lst1)
=  if i < n1
    then if get-nth(i, lst1) = 0 then t
        else strncat-induct0(stepn(s, 3),
                                add(32, i*, 1),
                                1 + i,
                                n1,
                                lst1) endif
    else t endif

```

DEFINITION:

```

strncat-induct1(s, i*, i, lst1, j*, j, n, lst2)
=  if get-nth(j, lst2) = 0 then t
    elseif (n - 1) = 0 then t
    else strncat-induct1(stepn(s, 6),
                        add(32, i*, 1),
                        1 + i,
                        put-nth(get-nth(j, lst2), i, lst1),
                        add(32, j*, 1),
                        1 + j,
                        n - 1,
                        lst2) endif

```

; the preconditions of the initial state.

DEFINITION:

```

strncat-statep(s, str1, n1, lst1, str2, n, lst2)
=  ((mc-status(s) = 'running')
    ∧ evenp(mc-pc(s))
    ∧ rom-addrp(mc-pc(s), mc-mem(s), 56)
    ∧ mcode-addrp(mc-pc(s), mc-mem(s), STRNCAT-CODE)
    ∧ ram-addrp(sub(32, 8, read-sp(s)), mc-mem(s), 24)
    ∧ ram-addrp(str1, mc-mem(s), n1)
    ∧ mem-lst(1, str1, mc-mem(s), n1, lst1)
    ∧ ram-addrp(str2, mc-mem(s), n)
    ∧ mem-lst(1, str2, mc-mem(s), n, lst2)
    ∧ disjoint(sub(32, 8, read-sp(s)), 24, str1, n1)
    ∧ disjoint(sub(32, 8, read-sp(s)), 24, str2, n)
    ∧ disjoint(str1, n1, str2, n)
    ∧ (str1 = read-mem(add(32, read-sp(s), 4), mc-mem(s), 4))
    ∧ (str2 = read-mem(add(32, read-sp(s), 8), mc-mem(s), 4))
    ∧ (n = uread-mem(add(32, read-sp(s), 12), mc-mem(s), 4))
    ∧ ((1 + (slen(0, n1, lst1) + n)) < n1)
    ∧ (n1 ∈ N)

```

$\wedge$ uint-range $p(n1, 32)$
 ; an intermediate state s0.
 DEFINITION:
 strncat-s0p($s, i^*, i, str1, n1, lst1, str2, n, lst2$)
 = ((mc-status(s) = 'running')
 $\wedge$ evenp(mc-pc(s))
 $\wedge$ rom-addrp(sub(32, 28, mc-pc(s)), mc-mem(s), 56)
 $\wedge$ mcode-addrp(sub(32, 28, mc-pc(s)), mc-mem(s), STRNCAT-CODE)
 $\wedge$ ram-addrp(sub(32, 4, read-an(32, 6, s)), mc-mem(s), 24)
 $\wedge$ ram-addrp($str1$, mc-mem(s), $n1$)
 $\wedge$ mem-lst(1, $str1$, mc-mem(s), $n1$, $lst1$)
 $\wedge$ ram-addrp($str2$, mc-mem(s), n)
 $\wedge$ mem-lst(1, $str2$, mc-mem(s), n , $lst2$)
 $\wedge$ disjoint(sub(32, 4, read-an(32, 6, s)), 24, $str1$, $n1$)
 $\wedge$ disjoint(sub(32, 4, read-an(32, 6, s)), 24, $str2$, n)
 $\wedge$ disjoint($str1$, $n1$, $str2$, n)
 $\wedge$ equal*(read-an(32, 0, s), add(32, $str1$, i^*))
 $\wedge$ ($str1$ = read-dn(32, 2, s))
 $\wedge$ ($str2$ = read-an(32, 1, s))
 $\wedge$ (n = nat-to-uint(read-dn(32, 1, s)))
 $\wedge$ ($i^* \in \mathbf{N}$)
 $\wedge$ nat-range $p(i^*, 32)$
 $\wedge$ (i = nat-to-uint(i^*))
 $\wedge$ ((1 + (slen(i , $n1$, $lst1$) + n)) < $n1$)
 $\wedge$ ($n1 \in \mathbf{N}$)
 $\wedge$ uint-range $p(n1, 32)$
 $\wedge$ ($n \neq 0$)
 ; an intermediate state s1.

DEFINITION:
 strncat-s1p($s, i^*, i, str1, n1, lst1, j^*, j, str2, n, lst2, i_-, n_-$)
 = ((mc-status(s) = 'running')
 $\wedge$ evenp(mc-pc(s))
 $\wedge$ rom-addrp(sub(32, 32, mc-pc(s)), mc-mem(s), 56)
 $\wedge$ mcode-addrp(sub(32, 32, mc-pc(s)), mc-mem(s), STRNCAT-CODE)
 $\wedge$ ram-addrp(sub(32, 4, read-an(32, 6, s)), mc-mem(s), 24)
 $\wedge$ ram-addrp($str1$, mc-mem(s), $n1$)
 $\wedge$ mem-lst(1, $str1$, mc-mem(s), $n1$, $lst1$)
 $\wedge$ ram-addrp($str2$, mc-mem(s), n_-)
 $\wedge$ mem-lst(1, $str2$, mc-mem(s), n_- , $lst2$)
 $\wedge$ disjoint(sub(32, 4, read-an(32, 6, s)), 24, $str1$, $n1$)
 $\wedge$ disjoint(sub(32, 4, read-an(32, 6, s)), 24, $str2$, n_-)

$\wedge$ disjoint(*str1*, *n1*, *str2*, *n*)
 $\wedge$ equal*(read-an(32, 0, *s*), add(32, *str1*, *i**))
 $\wedge$ equal*(read-an(32, 1, *s*), add(32, *str2*, *j**))
 $\wedge$ (*str1* = read-dn(32, 2, *s*))
 $\wedge$ (*n* = nat-to-uint(read-dn(32, 1, *s*)))
 $\wedge$ ((*j* + *n*) $\leq$ *n*)
 $\wedge$ ((1 + (*i* + *n*)) < *n1*)
 $\wedge$ (*i* $\leq$ (*i* + *j*))
 $\wedge$ (*n* $\neq$ 0)
 $\wedge$ (*i** $\in \mathbf{N}$)
 $\wedge$ nat-range(*i**, 32)
 $\wedge$ (*i* = nat-to-uint(*i**))
 $\wedge$ (*j** $\in \mathbf{N}$)
 $\wedge$ nat-range(*j**, 32)
 $\wedge$ (*j* = nat-to-uint(*j**))
 $\wedge$ (*n1* < 4294967296)
 $\wedge$ (*n* $\in \mathbf{N}$)
 $\wedge$ (*n* < 4294967296)

; from the initial state *s* to exit: *s* --> *sn*, when *n* = 0.

THEOREM: strncat-s-sn

(strncat-statep(*s*, *str1*, *n1*, *lst1*, *str2*, *n*, *lst2*) $\wedge$ (*n* = 0))
 $\rightarrow$ ((mc-status(stepn(*s*, 9)) = 'running')
 $\wedge$ (mc-pc(stepn(*s*, 9)) = rts-addr(*s*))
 $\wedge$ (read-dn(32, 0, stepn(*s*, 9)) = *str1*)
 $\wedge$ mem-lst(1, *str1*, mc-mem(stepn(*s*, 9)), *n1*, *lst1*)
 $\wedge$ (read-rn(32, 15, mc-rfile(stepn(*s*, 9)))
 = add(32, read-an(32, 7, *s*), 4))
 $\wedge$ (read-rn(32, 14, mc-rfile(stepn(*s*, 9))) = read-an(32, 6, *s*)))

THEOREM: strncat-s-sn-rfile

(strncat-statep(*s*, *str1*, *n1*, *lst1*, *str2*, *n*, *lst2*)
 $\wedge$ (*n* = 0)
 $\wedge$ (*oplen* $\leq$ 32)
 $\wedge$ d2-7a2-5p(*rn*)
 $\rightarrow$ (read-rn(*oplen*, *rn*, mc-rfile(stepn(*s*, 9)))
 = read-rn(*oplen*, *rn*, mc-rfile(*s*)))

THEOREM: strncat-s-sn-mem

(strncat-statep(*s*, *str1*, *n1*, *lst1*, *str2*, *n*, *lst2*)
 $\wedge$ (*n* = 0)
 $\wedge$ disjoint(*x*, *k*, sub(32, 8, read-sp(*s*), 24))
 $\rightarrow$ (read-mem(*x*, mc-mem(stepn(*s*, 9)), *k*) = read-mem(*x*, mc-mem(*s*), *k*))

; from the initial state s to $s0$: $s \dashrightarrow s0$, when $n = 0$, $lst1[0] = 0$.

THEOREM: strncat-s-s0

$$\begin{aligned}
& (\text{strncat-statep}(s, str1, n1, lst1, str2, n, lst2) \\
& \quad \wedge (n \neq 0) \\
& \quad \wedge (\text{get-nth}(0, lst1) \neq 0)) \\
\rightarrow & (\text{strncat-s0p}(\text{stepn}(s, 10), 1, 1, str1, n1, lst1, str2, n, lst2) \\
& \quad \wedge (\text{linked-rtts-addr}(\text{stepn}(s, 10)) = \text{rtts-addr}(s)) \\
& \quad \wedge (\text{linked-a6}(\text{stepn}(s, 10)) = \text{read-an}(32, 6, s)) \\
& \quad \wedge (\text{read-rn}(32, 14, \text{mc-rfile}(\text{stepn}(s, 10))) \\
& \quad \quad = \text{sub}(32, 4, \text{read-sp}(s))) \\
& \quad \wedge (\text{rn-saved}(\text{stepn}(s, 10)) = \text{read-dn}(32, 2, s)))
\end{aligned}$$

THEOREM: strncat-s-s0-rfile

$$\begin{aligned}
& (\text{strncat-statep}(s, str1, n1, lst1, str2, n, lst2) \\
& \quad \wedge (n \neq 0) \\
& \quad \wedge (\text{get-nth}(0, lst1) \neq 0) \\
& \quad \wedge \text{d3-7a2-5p}(rn)) \\
\rightarrow & (\text{read-rn}(\text{oplen}, rn, \text{mc-rfile}(\text{stepn}(s, 10))) \\
& \quad = \text{read-rn}(\text{oplen}, rn, \text{mc-rfile}(s)))
\end{aligned}$$

THEOREM: strncat-s-s0-mem

$$\begin{aligned}
& (\text{strncat-statep}(s, str1, n1, lst1, str2, n, lst2) \\
& \quad \wedge (n \neq 0) \\
& \quad \wedge (\text{get-nth}(0, lst1) \neq 0) \\
& \quad \wedge \text{disjoint}(x, k, \text{sub}(32, 8, \text{read-sp}(s)), 24)) \\
\rightarrow & (\text{read-mem}(x, \text{mc-mem}(\text{stepn}(s, 10)), k) = \text{read-mem}(x, \text{mc-mem}(s), k))
\end{aligned}$$

; from initial state s to $s1$, when $n = 0$, $lst1[0] = 0$.

THEOREM: strncat-s-s1

$$\begin{aligned}
& (\text{strncat-statep}(s, str1, n1, lst1, str2, n, lst2) \\
& \quad \wedge (n \neq 0) \\
& \quad \wedge (\text{get-nth}(0, lst1) = 0)) \\
\rightarrow & \text{strncat-s1p}(\text{stepn}(s, 9), 0, 0, str1, n1, lst1, 0, 0, str2, n, lst2, 0, n)
\end{aligned}$$

THEOREM: strncat-s-s1-else

$$\begin{aligned}
& (\text{strncat-statep}(s, str1, n1, lst1, str2, n, lst2) \\
& \quad \wedge (n \neq 0) \\
& \quad \wedge (\text{get-nth}(0, lst1) = 0)) \\
\rightarrow & ((\text{linked-rtts-addr}(\text{stepn}(s, 9)) = \text{rtts-addr}(s)) \\
& \quad \wedge (\text{linked-a6}(\text{stepn}(s, 9)) = \text{read-an}(32, 6, s)) \\
& \quad \wedge (\text{read-rn}(32, 14, \text{mc-rfile}(\text{stepn}(s, 9))) \\
& \quad \quad = \text{sub}(32, 4, \text{read-sp}(s))) \\
& \quad \wedge (\text{rn-saved}(\text{stepn}(s, 9)) = \text{read-rn}(32, 2, \text{mc-rfile}(s))))
\end{aligned}$$

THEOREM: strncat-s-s1-rfile

$$\begin{aligned}
& (\text{strncat-statep}(s, \text{str1}, n1, \text{lst1}, \text{str2}, n, \text{lst2}) \\
& \quad \wedge (n \neq 0) \\
& \quad \wedge (\text{get-nth}(0, \text{lst1}) = 0) \\
& \quad \wedge \text{d3-7a2-5p}(rn)) \\
& \rightarrow (\text{read-rn}(\text{oplen}, rn, \text{mc-rfile}(\text{stepn}(s, 9))) \\
& \quad = \text{read-rn}(\text{oplen}, rn, \text{mc-rfile}(s)))
\end{aligned}$$

THEOREM: strncat-s-s1-mem

$$\begin{aligned}
& (\text{strncat-statep}(s, \text{str1}, n1, \text{lst1}, \text{str2}, n, \text{lst2}) \\
& \quad \wedge (n \neq 0) \\
& \quad \wedge (\text{get-nth}(0, \text{lst1}) = 0) \\
& \quad \wedge \text{disjoint}(x, k, \text{sub}(32, 8, \text{read-sp}(s)), 24)) \\
& \rightarrow (\text{read-mem}(x, \text{mc-mem}(\text{stepn}(s, 9)), k) = \text{read-mem}(x, \text{mc-mem}(s), k))
\end{aligned}$$

; from s0 to s1: s0 --> s1.
; base case: s0 --> s1, when lst1[i] = 0.

THEOREM: strncat-s0-s1-base

$$\begin{aligned}
& (\text{strncat-s0p}(s, i^*, i, \text{str1}, n1, \text{lst1}, \text{str2}, n, \text{lst2}) \wedge (\text{get-nth}(i, \text{lst1}) = 0)) \\
& \rightarrow (\text{strncat-s1p}(\text{stepn}(s, 2), i^*, i, \text{str1}, n1, \text{lst1}, 0, 0, \text{str2}, n, \text{lst2}, i, n) \\
& \quad \wedge (\text{read-rn}(32, 14, \text{mc-rfile}(\text{stepn}(s, 2))) \\
& \quad = \text{read-rn}(32, 14, \text{mc-rfile}(s))) \\
& \quad \wedge (\text{linked-a6}(\text{stepn}(s, 2)) = \text{linked-a6}(s)) \\
& \quad \wedge (\text{linked-rtts-addr}(\text{stepn}(s, 2)) = \text{linked-rtts-addr}(s)) \\
& \quad \wedge (\text{rn-saved}(\text{stepn}(s, 2)) = \text{rn-saved}(s)) \\
& \quad \wedge (\text{read-mem}(x, \text{mc-mem}(\text{stepn}(s, 2)), k) \\
& \quad = \text{read-mem}(x, \text{mc-mem}(s), k)))
\end{aligned}$$

THEOREM: strncat-s0-s1-rfile-base

$$\begin{aligned}
& (\text{strncat-s0p}(s, i^*, i, \text{str1}, n1, \text{lst1}, \text{str2}, n, \text{lst2}) \\
& \quad \wedge (\text{get-nth}(i, \text{lst1}) = 0) \\
& \quad \wedge \text{d3-7a2-5p}(rn)) \\
& \rightarrow (\text{read-rn}(\text{oplen}, rn, \text{mc-rfile}(\text{stepn}(s, 2))) \\
& \quad = \text{read-rn}(\text{oplen}, rn, \text{mc-rfile}(s)))
\end{aligned}$$

; induction case: s0 --> s0, when lst1[i] = 0.

THEOREM: strncat-s0-s0

$$\begin{aligned}
& (\text{strncat-s0p}(s, i^*, i, \text{str1}, n1, \text{lst1}, \text{str2}, n, \text{lst2}) \wedge (\text{get-nth}(i, \text{lst1}) \neq 0)) \\
& \rightarrow (\text{strncat-s0p}(\text{stepn}(s, 3), \text{add}(32, i^*, 1), 1 + i, \text{str1}, n1, \text{lst1}, \text{str2}, n, \text{lst2}) \\
& \quad \wedge (\text{read-rn}(32, 14, \text{mc-rfile}(\text{stepn}(s, 3))) \\
& \quad = \text{read-rn}(32, 14, \text{mc-rfile}(s))) \\
& \quad \wedge (\text{linked-a6}(\text{stepn}(s, 3)) = \text{linked-a6}(s)) \\
& \quad \wedge (\text{linked-rtts-addr}(\text{stepn}(s, 3)) = \text{linked-rtts-addr}(s))
\end{aligned}$$

$$\begin{aligned}
& \wedge \text{ (rn-saved (stepn (s, 3)) = rn-saved (s))} \\
& \wedge \text{ (read-mem (x, mc-mem (stepn (s, 3)), k)} \\
& \quad = \text{ read-mem (x, mc-mem (s), k))}
\end{aligned}$$

THEOREM: strncat-s0-s0-rfile

$$\begin{aligned}
& \text{(strncat-s0p (s, } i^*, i, \text{str1, n1, lst1, str2, n, lst2)} \\
& \wedge \text{ (get-nth (i, lst1) } \neq 0) \\
& \wedge \text{ d3-7a2-5p (rn))} \\
& \rightarrow \text{ (read-rn (oplen, rn, mc-rfile (stepn (s, 3)))} \\
& \quad = \text{ read-rn (oplen, rn, mc-rfile (s))})
\end{aligned}$$

; put together. s0 --> s1.

THEOREM: strncat-s0p-info

$$\text{strncat-s0p (s, } i^*, i, \text{str1, n1, lst1, str2, n, lst2)} \rightarrow ((i < n1) = \mathbf{t})$$

THEOREM: strncat-s0-s1

$$\begin{aligned}
& \text{strncat-s0p (s, } i^*, i, \text{str1, n1, lst1, str2, n, lst2)} \\
& \rightarrow \text{ (strncat-s1p (stepn (s, strncat-t0 (i, n1, lst1)),} \\
& \quad \text{strlen* (i*, i, n1, lst1),} \\
& \quad \text{strlen (i, n1, lst1),} \\
& \quad \text{str1,} \\
& \quad \text{n1,} \\
& \quad \text{lst1,} \\
& \quad \text{0,} \\
& \quad \text{0,} \\
& \quad \text{str2,} \\
& \quad \text{n,} \\
& \quad \text{lst2,} \\
& \quad \text{strlen (i, n1, lst1),} \\
& \quad \text{n)} \\
& \wedge \text{ (read-rn (32, 14, mc-rfile (stepn (s, strncat-t0 (i, n1, lst1))))} \\
& \quad = \text{ read-rn (32, 14, mc-rfile (s))}) \\
& \wedge \text{ (linked-a6 (stepn (s, strncat-t0 (i, n1, lst1))) = linked-a6 (s))} \\
& \wedge \text{ (linked-rtts-addr (stepn (s, strncat-t0 (i, n1, lst1)))} \\
& \quad = \text{ linked-rtts-addr (s)}) \\
& \wedge \text{ (rn-saved (stepn (s, strncat-t0 (i, n1, lst1))) = rn-saved (s))} \\
& \wedge \text{ (read-mem (x, mc-mem (stepn (s, strncat-t0 (i, n1, lst1))), k)} \\
& \quad = \text{ read-mem (x, mc-mem (s), k))}
\end{aligned}$$

EVENT: Disable strncat-s0p-info.

THEOREM: strncat-s0-s1-rfile

$$\begin{aligned}
& \text{(strncat-s0p (s, } i^*, i, \text{str1, n1, lst1, str2, n, lst2)} \wedge \text{ d3-7a2-5p (rn))} \\
& \rightarrow \text{ (read-rn (oplen, rn, mc-rfile (stepn (s, strncat-t0 (i, n1, lst1))))} \\
& \quad = \text{ read-rn (oplen, rn, mc-rfile (s))})
\end{aligned}$$

; put together: $s \dashrightarrow s1$.

THEOREM: strncat-s-s1-1
 $(\text{strncat-statep}(s, \text{str1}, n1, \text{lst1}, \text{str2}, n, \text{lst2})$
 $\wedge (n \neq 0)$
 $\wedge (\text{get-nth}(0, \text{lst1}) \neq 0))$
 $\rightarrow \text{strncat-s1p}(\text{stepn}(s, \text{strncat-t1}(n1, \text{lst1})),$
 $\text{strlen}^*(1, 1, n1, \text{lst1}),$
 $\text{strlen}(1, n1, \text{lst1}),$
 $\text{str1},$
 $n1,$
 $\text{lst1},$
 $0,$
 $0,$
 $\text{str2},$
 $n,$
 $\text{lst2},$
 $\text{strlen}(1, n1, \text{lst1}),$
 $n)$

THEOREM: strncat-s-s1-else-1
let $s1$ **be** $\text{stepn}(s, \text{strncat-t1}(n1, \text{lst1}))$
in
 $(\text{strncat-statep}(s, \text{str1}, n1, \text{lst1}, \text{str2}, n, \text{lst2})$
 $\wedge (n \neq 0)$
 $\wedge (\text{get-nth}(0, \text{lst1}) \neq 0))$
 $\rightarrow ((\text{linked-rtts-addr}(s1) = \text{rtts-addr}(s))$
 $\wedge (\text{linked-a6}(s1) = \text{read-an}(32, 6, s))$
 $\wedge (\text{read-rn}(32, 14, \text{mc-rfile}(s1))$
 $= \text{sub}(32, 4, \text{read-sp}(s)))$
 $\wedge (\text{rn-saved}(s1) = \text{read-rn}(32, 2, \text{mc-rfile}(s))))$ **endlet**

THEOREM: strncat-s-s1-rfile-1
 $(\text{strncat-statep}(s, \text{str1}, n1, \text{lst1}, \text{str2}, n, \text{lst2})$
 $\wedge (n \neq 0)$
 $\wedge (\text{get-nth}(0, \text{lst1}) \neq 0)$
 $\wedge \text{d3-7a2-5p}(rn))$
 $\rightarrow (\text{read-rn}(\text{oplen}, rn, \text{mc-rfile}(\text{stepn}(s, \text{strncat-t1}(n1, \text{lst1}))))$
 $= \text{read-rn}(\text{oplen}, rn, \text{mc-rfile}(s)))$

THEOREM: strncat-s-s1-mem-1
 $(\text{strncat-statep}(s, \text{str1}, n1, \text{lst1}, \text{str2}, n, \text{lst2})$
 $\wedge (n \neq 0)$
 $\wedge (\text{get-nth}(0, \text{lst1}) \neq 0)$
 $\wedge \text{disjoint}(x, k, \text{sub}(32, 8, \text{read-sp}(s)), 24))$

→ (read-mem(x , mc-mem(stepn(s , strncat-t1($n1$, $lst1$))), k)
 = read-mem(x , mc-mem(s), k))

; from s1 to exit: s1 --> sn. By induction.
 ; base case 1: s1 --> sn, when lst2[j] = 0.

THEOREM: strncat-s1-sn-base1

(strncat-slp(s , i^* , i , $str1$, $n1$, $lst1$, j^* , j , $str2$, n , $lst2$, i_- , n_-)
 ∧ (get-nth(j , $lst2$) = 0))
 → ((mc-status(stepn(s , 8)) = 'running')
 ∧ (mc-pc(stepn(s , 8)) = linked-rtts-addr(s))
 ∧ (read-dn(32, 0, stepn(s , 8)) = $str1$)
 ∧ mem-lst(1, $str1$, mc-mem(stepn(s , 8)), $n1$, put-nth(0, i , $lst1$))
 ∧ (read-rn(32, 14, mc-rfile(stepn(s , 8))) = linked-a6(s))
 ∧ (read-rn(32, 15, mc-rfile(stepn(s , 8)))
 = add(32, read-an(32, 6, s), 8)))

THEOREM: strncat-s0-sn-rfile-base1

(strncat-slp(s , i^* , i , $str1$, $n1$, $lst1$, j^* , j , $str2$, n , $lst2$, i_- , n_-)
 ∧ (get-nth(j , $lst2$) = 0)
 ∧ (oplen ≤ 32)
 ∧ d2-7a2-5p(rn))
 → (read-rn(oplen, rn , mc-rfile(stepn(s , 8)))
 = if d3-7a2-5p(rn) then read-rn(oplen, rn , mc-rfile(s))
 else head(rn-saved(s), oplen) endif)

THEOREM: strncat-s1-sn-mem-base1

(strncat-slp(s , i^* , i , $str1$, $n1$, $lst1$, j^* , j , $str2$, n , $lst2$, i_- , n_-)
 ∧ (get-nth(j , $lst2$) = 0)
 ∧ disjoint(x , k , $str1$, $n1$))
 → (read-mem(x , mc-mem(stepn(s , 8)), k) = read-mem(x , mc-mem(s), k))

; base case 2: s1 --> sn, when lst2[j] = 0, n-1 = 0.

THEOREM: strncat-s1-sn-base2

(strncat-slp(s , i^* , i , $str1$, $n1$, $lst1$, j^* , j , $str2$, n , $lst2$, i_- , n_-)
 ∧ (get-nth(j , $lst2$) ≠ 0)
 ∧ (($n - 1$) = 0))
 → ((mc-status(stepn(s , 11)) = 'running')
 ∧ (mc-pc(stepn(s , 11)) = linked-rtts-addr(s))
 ∧ (read-dn(32, 0, stepn(s , 11)) = $str1$)
 ∧ mem-lst(1,
 $str1$,
 mc-mem(stepn(s , 11)),
 $n1$,

$\text{put-nth}(0, 1 + i, \text{put-nth}(\text{get-nth}(j, \text{lst2}), i, \text{lst1})))$
 $\wedge \text{ (read-rn}(32, 14, \text{mc-rfile}(\text{stepn}(s, 11))) = \text{linked-a6}(s))$
 $\wedge \text{ (read-rn}(32, 15, \text{mc-rfile}(\text{stepn}(s, 11)))$
 $\quad = \text{add}(32, \text{read-an}(32, 6, s), 8))$

THEOREM: strncat-s1-sn-rfile-base2

$(\text{strncat-slp}(s, i^*, i, \text{str1}, n1, \text{lst1}, j^*, j, \text{str2}, n, \text{lst2}, i_-, n_-)$
 $\wedge \text{ (get-nth}(j, \text{lst2}) \neq 0)$
 $\wedge \text{ ((}n - 1) = 0)$
 $\wedge \text{ (oplen} \leq 32)$
 $\wedge \text{ d2-7a2-5p}(rn))$
 $\rightarrow \text{ (read-rn}(\text{oplen}, rn, \text{mc-rfile}(\text{stepn}(s, 11)))$
 $\quad = \text{ if d3-7a2-5p}(rn) \text{ then read-rn}(\text{oplen}, rn, \text{mc-rfile}(s))$
 $\quad \text{ else head}(\text{rn-saved}(s), \text{oplen}) \text{ endif})$

THEOREM: strncat-s1-sn-mem-base2

$(\text{strncat-slp}(s, i^*, i, \text{str1}, n1, \text{lst1}, j^*, j, \text{str2}, n, \text{lst2}, i_-, n_-)$
 $\wedge \text{ (get-nth}(j, \text{lst2}) \neq 0)$
 $\wedge \text{ ((}n - 1) = 0)$
 $\wedge \text{ disjoint}(x, k, \text{str1}, n1))$
 $\rightarrow \text{ (read-mem}(x, \text{mc-mem}(\text{stepn}(s, 11)), k) = \text{read-mem}(x, \text{mc-mem}(s), k))$

; induction case: s1 --> s1, when lst2[j] = 0, n-1 = 0.

THEOREM: strncat-s1-s1

$(\text{strncat-slp}(s, i^*, i, \text{str1}, n1, \text{lst1}, j^*, j, \text{str2}, n, \text{lst2}, i_-, n_-)$
 $\wedge \text{ (get-nth}(j, \text{lst2}) \neq 0)$
 $\wedge \text{ ((}n - 1) \neq 0)$
 $\rightarrow \text{ (strncat-slp}(\text{stepn}(s, 6),$
 $\quad \text{add}(32, i^*, 1),$
 $\quad 1 + i,$
 $\quad \text{str1},$
 $\quad n1,$
 $\quad \text{put-nth}(\text{get-nth}(j, \text{lst2}), i, \text{lst1}),$
 $\quad \text{add}(32, j^*, 1),$
 $\quad 1 + j,$
 $\quad \text{str2},$
 $\quad n - 1,$
 $\quad \text{lst2},$
 $\quad i_-,$
 $\quad n_-)$
 $\wedge \text{ (read-rn}(32, 14, \text{mc-rfile}(\text{stepn}(s, 6)))$
 $\quad = \text{read-rn}(32, 14, \text{mc-rfile}(s)))$
 $\wedge \text{ (linked-a6}(\text{stepn}(s, 6)) = \text{linked-a6}(s))$
 $\wedge \text{ (linked-rt-s-addr}(\text{stepn}(s, 6)) = \text{linked-rt-s-addr}(s))$
 $\wedge \text{ (rn-saved}(\text{stepn}(s, 6)) = \text{rn-saved}(s))$

THEOREM: strncat-s1-s1-rfile

```
(strncat-s1p(s, i*, i, str1, n1, lst1, j*, j, str2, n, lst2, i-, n-)
  ∧ (get-nth(j, lst2) ≠ 0)
  ∧ ((n - 1) ≠ 0)
  ∧ d3-7a2-5p(rn))
→ (read-rn(oplen, rn, mc-rfile(stepn(s, 6)))
    = read-rn(oplen, rn, mc-rfile(s)))
```

THEOREM: strncat-s1-s1-mem

```
(strncat-s1p(s, i*, i, str1, n1, lst1, j*, j, str2, n, lst2, i-, n-)
  ∧ (get-nth(j, lst2) ≠ 0)
  ∧ ((n - 1) ≠ 0)
  ∧ disjoint(x, k, str1, n1))
→ (read-mem(x, mc-mem(stepn(s, 6)), k) = read-mem(x, mc-mem(s), k))
```

; put together. s1 --> sn.

THEOREM: strncat-s1-sn

```
let sn be stepn(s, strncat-t2(j, n, lst2))
in
strncat-s1p(s, i*, i, str1, n1, lst1, j*, j, str2, n, lst2, i-, n-)
→ ((mc-status(sn) = 'running')
    ∧ (mc-pc(sn) = linked-rtts-addr(s))
    ∧ (read-dn(32, 0, sn) = str1)
    ∧ mem-lst(1, str1, mc-mem(sn), n1, strcpy2(i, lst1, j, n, lst2))
    ∧ (read-rn(32, 14, mc-rfile(sn)) = linked-a6(s))
    ∧ (read-rn(32, 15, mc-rfile(sn))
        = add(32, read-an(32, 6, s), 8))) endlet
```

THEOREM: strncat-s1-sn-rfile

```
(strncat-s1p(s, i*, i, str1, n1, lst1, j*, j, str2, n, lst2, i-, n-)
  ∧ (oplen ≤ 32)
  ∧ d2-7a2-5p(rn))
→ (read-rn(oplen, rn, mc-rfile(stepn(s, strncat-t2(j, n, lst2))))
    = if d3-7a2-5p(rn) then read-rn(oplen, rn, mc-rfile(s))
      else head(rn-saved(s), oplen) endif)
```

THEOREM: strncat-s1-sn-mem

```
(strncat-s1p(s, i*, i, str1, n1, lst1, j*, j, str2, n, lst2, i-, n-)
  ∧ disjoint(x, k, str1, n1))
→ (read-mem(x, mc-mem(stepn(s, strncat-t2(j, n, lst2))), k)
    = read-mem(x, mc-mem(s), k))
```

; the correctness of strncat.

THEOREM: strncat-correctness

```

let sn be stepn(s, strncat-t(n1, lst1, n, lst2))
in
strncat-statep(s, str1, n1, lst1, str2, n, lst2)
→ ((mc-status(sn) = 'running)
   ∧ (mc-pc(sn) = rts-addr(s))
   ∧ (read-an(32, 6, sn) = read-an(32, 6, s))
   ∧ (read-an(32, 7, sn) = add(32, read-an(32, 7, s), 4))
   ∧ ((d2-7a2-5p(rn) ∧ (oplen ≤ 32))
      → (read-rn(oplen, rn, mc-rfile(sn))
          = read-rn(oplen, rn, mc-rfile(s))))
   ∧ ((disjoint(x, k, sub(32, 8, read-sp(s)), 24)
      ∧ disjoint(x, k, str1, n1))
      → (read-mem(x, mc-mem(sn), k)
          = read-mem(x, mc-mem(s), k)))
   ∧ (read-dn(32, 0, sn) = str1)
   ∧ mem-lst(1, str1, mc-mem(sn), n1, strncat(n1, lst1, n, lst2))) endlet

```

EVENT: Disable strncat-t.

```

; some properties of strncat.
; see file cstring.events.

```

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