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EVENT: Start with the library "strlen" using the compiled version.

; Proof of the Correctness of the STRNCMP Function

#|

This is part of our effort to verify the Berkeley string library. The Berkeley string library is widely used as part of the Berkeley Unix OS.

This is the source code of strncmp function in the Berkeley string library.

```
/* compare at most char s1[] to char s2[] */
int
strncmp(s1, s2, n)
register const char *s1, *s2;
register size_t n;
{

if (n == 0)
return (0);
```

```

do {
if (*s1 != *s2++)
return (*(unsigned char *)s1 - *(unsigned char *)--s2);
if (*s1++ == 0)
break;
} while (--n != 0);
return (0);
}

```

The MC68020 assembly code of the C function `strncmp` on SUN-3 is given as follows. This binary is generated by "gcc -O".

```

0x2608 <strncmp>:      linkw fp,#0
0x260c <strncmp+4>:    moveml d2-d3,sp@-
0x2610 <strncmp+8>:    moveal fp@(8),a0
0x2614 <strncmp+12>:   moveal fp@(12),a1
0x2618 <strncmp+16>:   movel fp@(16),d0
0x261c <strncmp+20>:   beq 0x2642 <strncmp+58>
0x261e <strncmp+22>:   andil #255,d1
0x2624 <strncmp+28>:   andil #255,d2
0x262a <strncmp+34>:   moveb a0@,d3
0x262c <strncmp+36>:   cmpb a1@+,d3
0x262e <strncmp+38>:   beq 0x263a <strncmp+50>
0x2630 <strncmp+40>:   moveb a0@,d1
0x2632 <strncmp+42>:   moveb a1@-,d2
0x2634 <strncmp+44>:   movel d1,d0
0x2636 <strncmp+46>:   subl d2,d0
0x2638 <strncmp+48>:   bra 0x2644 <strncmp+60>
0x263a <strncmp+50>:   tstb a0@+
0x263c <strncmp+52>:   beq 0x2642 <strncmp+58>
0x263e <strncmp+54>:   subl #1,d0
0x2640 <strncmp+56>:   bne 0x262a <strncmp+34>
0x2642 <strncmp+58>:   clrl d0
0x2644 <strncmp+60>:   moveml fp@(-8),d2-d3
0x264a <strncmp+66>:   unlk fp
0x264c <strncmp+68>:   rts

```

The machine code of the above program is:

```

<strncmp>:      0x4e56  0x0000  0x48e7  0x3000  0x206e  0x0008  0x226e  0x000c
<strncmp+16>:  0x202e  0x0010  0x6724  0x0281  0x0000  0x00ff  0x0282  0x0000
<strncmp+32>:  0x00ff  0x1610  0xb619  0x670a  0x1210  0x1421  0x2001  0x9082
<strncmp+48>:  0x600a  0x4a18  0x6704  0x5380  0x66e8  0x4280  0x4cee  0x000c
<strncmp+64>:  0xffff  0x4e5e  0x4e75

```

```

' (78      86      0      0      72      231      48      0
   32      110     0      8      34      110     0      12
   32      46      0      16     103      36      2      129
   0       0       0     255      2      130      0      0
   0     255     22      16     182      25     103      10
  18      16     20      33      32      1     144     130
  96      10     74      24     103      4      83      128
 102     232     66     128      76     238      0      12
 255     248     78      94      78     117)
|#

```

; in the logic, the above program is defined by (strncmp-code).

DEFINITION:

STRNCMP-CODE

```

= ' (78 86 0 0 72 231 48 0 32 110 0 8 34 110 0 12 32 46 0
    16 103 36 2 129 0 0 0 255 2 130 0 0 0 255 22 16 182
    25 103 10 18 16 20 33 32 1 144 130 96 10 74 24 103 4
    83 128 102 232 66 128 76 238 0 12 255 248 78 94 78
    117)

```

CONSERVATIVE AXIOM: strncmp-load

strncmp-loadp(s)

```

= (evenp (STRNCMP-ADDR)
   ∧ (STRNCMP-ADDR ∈ ℕ)
   ∧ nat-range (STRNCMP-ADDR, 32)
   ∧ rom-addrp (STRNCMP-ADDR, mc-mem( $s$ ), 70)
   ∧ mcode-addrp (STRNCMP-ADDR, mc-mem( $s$ ), STRNCMP-CODE))

```

Simultaneously, we introduce the new function symbols *strncmp-loadp* and *strncmp-addr*.

THEOREM: stepn-strncmp-loadp

strncmp-loadp(stepn(s , n)) = strncmp-loadp(s)

; the computation time of the program.

DEFINITION:

strncmp1-t(i , n , $lst1$, $lst2$)

```

= if get-nth( $i$ ,  $lst1$ ) = get-nth( $i$ ,  $lst2$ )
  then if get-nth( $i$ ,  $lst1$ ) = 0 then 9
    elseif ( $n - 1$ ) = 0 then 11
    else splus(7, strncmp1-t(1 +  $i$ ,  $n - 1$ ,  $lst1$ ,  $lst2$ )) endif
  else 11 endif

```

DEFINITION:

strncmp-t($n, lst1, lst2$)

= **if** $n \simeq 0$ **then** 10

else splus(8, strncmp1-t(0, $n, lst1, lst2$)) **endif**

; an induction hint.

DEFINITION:

strncmp-induct($s, i^*, i, n, lst1, lst2$)

= **if** get-nth($i, lst1$) = get-nth($i, lst2$)

then if get-nth($i, lst1$) = 0 **then t**

elseif $(n - 1) = 0$ **then t**

else strncmp-induct(stepn($s, 7$),
 add(32, $i^*, 1$),
 $1 + i$,
 $n - 1$,
 $lst1$,
 $lst2$) **endif**

else t endif

; the preconditions of the initial state.

DEFINITION:

strncmp-statep($s, str1, n1, lst1, str2, n2, lst2, n$)

= ((mc-status(s) = 'running)

$\wedge$ evenp(mc-pc(s))

$\wedge$ rom-addrp(mc-pc(s), mc-mem(s), 70)

$\wedge$ mcode-addrp(mc-pc(s), mc-mem(s), STRNCMP-CODE)

$\wedge$ ram-addrp(sub(32, 12, read-sp(s)), mc-mem(s), 28)

$\wedge$ ram-addrp($str1$, mc-mem(s), $n1$)

$\wedge$ mem-lst(1, $str1$, mc-mem(s), $n1, lst1$)

$\wedge$ ram-addrp($str2$, mc-mem(s), $n2$)

$\wedge$ mem-lst(1, $str2$, mc-mem(s), $n2, lst2$)

$\wedge$ (slen(0, $n, lst1$) < $n1$)

$\wedge$ (slen(0, $n, lst2$) < $n2$)

$\wedge$ disjoint(sub(32, 12, read-sp(s)), 28, $str1, n1$)

$\wedge$ disjoint(sub(32, 12, read-sp(s)), 28, $str2, n2$)

$\wedge$ ($str1$ = read-mem(add(32, read-sp(s), 4), mc-mem(s), 4))

$\wedge$ ($str2$ = read-mem(add(32, read-sp(s), 8), mc-mem(s), 4))

$\wedge$ (n = uread-mem(add(32, read-sp(s), 12), mc-mem(s), 4))

$\wedge$ ($n1 \in \mathbf{N}$)

$\wedge$ ($n2 \in \mathbf{N}$)

; an intermediate state.

DEFINITION:

$\text{strncmp-s0p}(s, i^*, i, \text{str1}, n1, \text{lst1}, \text{str2}, n2, \text{lst2}, n, n_-)$
 $= ((\text{mc-status}(s) = \text{'running'})$
 $\wedge \text{evenp}(\text{mc-pc}(s))$
 $\wedge \text{rom-addrp}(\text{sub}(32, 34, \text{mc-pc}(s)), \text{mc-mem}(s), 70)$
 $\wedge \text{mcode-addrp}(\text{sub}(32, 34, \text{mc-pc}(s)), \text{mc-mem}(s), \text{STRNCMP-CODE})$
 $\wedge \text{ram-addrp}(\text{sub}(32, 8, \text{read-an}(32, 6, s)), \text{mc-mem}(s), 28)$
 $\wedge \text{ram-addrp}(\text{str1}, \text{mc-mem}(s), n1)$
 $\wedge \text{mem-lst}(1, \text{str1}, \text{mc-mem}(s), n1, \text{lst1})$
 $\wedge \text{ram-addrp}(\text{str2}, \text{mc-mem}(s), n2)$
 $\wedge \text{mem-lst}(1, \text{str2}, \text{mc-mem}(s), n2, \text{lst2})$
 $\wedge (\text{slen}(i, n_-, \text{lst1}) < n1)$
 $\wedge (\text{slen}(i, n_-, \text{lst2}) < n2)$
 $\wedge \text{disjoint}(\text{sub}(32, 8, \text{read-an}(32, 6, s)), 28, \text{str1}, n1)$
 $\wedge \text{disjoint}(\text{sub}(32, 8, \text{read-an}(32, 6, s)), 28, \text{str2}, n2)$
 $\wedge \text{equal}^*(\text{read-an}(32, 0, s), \text{add}(32, \text{str1}, i^*))$
 $\wedge \text{equal}^*(\text{read-an}(32, 1, s), \text{add}(32, \text{str2}, i^*))$
 $\wedge \text{nat-range}(\text{read-rn}(32, 1, \text{mc-rfile}(s)), 8)$
 $\wedge \text{nat-range}(\text{read-rn}(32, 2, \text{mc-rfile}(s)), 8)$
 $\wedge (n = \text{nat-to-uint}(\text{read-dn}(32, 0, s)))$
 $\wedge (i^* \in \mathbf{N})$
 $\wedge \text{nat-range}(i^*, 32)$
 $\wedge (i = \text{nat-to-uint}(i^*))$
 $\wedge (n_- \in \mathbf{N})$
 $\wedge ((i + n) \leq n_-)$
 $\wedge (n \neq 0)$
 $\wedge \text{uint-range}(n_-, 32))$

; from the initial state s to exit: $s \dashrightarrow \text{sn}$, when $n = 0$.

THEOREM: strncmp-s-sn

$(\text{strncmp-statep}(s, \text{str1}, n1, \text{lst1}, \text{str2}, n2, \text{lst2}, n) \wedge (n \simeq 0))$
 $\rightarrow ((\text{mc-status}(\text{stepn}(s, 10)) = \text{'running'})$
 $\wedge (\text{mc-pc}(\text{stepn}(s, 10)) = \text{rts-addr}(s))$
 $\wedge (\text{iread-dn}(32, 0, \text{stepn}(s, 10)) = 0)$
 $\wedge (\text{read-rn}(32, 15, \text{mc-rfile}(\text{stepn}(s, 10)))$
 $\quad = \text{add}(32, \text{read-an}(32, 7, s), 4))$
 $\wedge (\text{read-rn}(32, 14, \text{mc-rfile}(\text{stepn}(s, 10))) = \text{read-an}(32, 6, s)))$

THEOREM: $\text{strncmp-s-sn-rfile}$

$(\text{strncmp-statep}(s, \text{str1}, n1, \text{lst1}, \text{str2}, n2, \text{lst2}, n)$
 $\wedge (n \simeq 0)$
 $\wedge (\text{oplen} \leq 32)$
 $\wedge \text{d2-7a2-5p}(rn))$
 $\rightarrow (\text{read-rn}(\text{oplen}, rn, \text{mc-rfile}(\text{stepn}(s, 10))))$

$$= \text{read-rn}(\text{oplen}, rn, \text{mc-rfile}(s))$$

THEOREM: strncmp-s-sn-mem

$$\begin{aligned} & (\text{strncmp-statep}(s, \text{str1}, n1, \text{lst1}, \text{str2}, n2, \text{lst2}, n) \\ & \wedge (n \simeq 0) \\ & \wedge \text{disjoint}(x, k, \text{sub}(32, 12, \text{read-sp}(s)), 28)) \\ \rightarrow & (\text{read-mem}(x, \text{mc-mem}(\text{stepn}(s, 10)), k) = \text{read-mem}(x, \text{mc-mem}(s), k)) \end{aligned}$$

; from the initial state s to s0: s --> s0.

THEOREM: strncmp-s-s0

$$\begin{aligned} & (\text{strncmp-statep}(s, \text{str1}, n1, \text{lst1}, \text{str2}, n2, \text{lst2}, n) \wedge (n \not\simeq 0)) \\ \rightarrow & \text{strncmp-s0p}(\text{stepn}(s, 8), 0, 0, \text{str1}, n1, \text{lst1}, \text{str2}, n2, \text{lst2}, n, n) \end{aligned}$$

THEOREM: strncmp-s-s0-else

$$\begin{aligned} & (\text{strncmp-statep}(s, \text{str1}, n1, \text{lst1}, \text{str2}, n2, \text{lst2}, n) \wedge (n \not\simeq 0)) \\ \rightarrow & ((\text{linked-rtt-addr}(\text{stepn}(s, 8)) = \text{rtt-addr}(s)) \\ & \wedge (\text{linked-a6}(\text{stepn}(s, 8)) = \text{read-an}(32, 6, s)) \\ & \wedge (\text{read-rn}(32, 14, \text{mc-rfile}(\text{stepn}(s, 8))) \\ & \quad = \text{sub}(32, 4, \text{read-sp}(s))) \\ & \wedge (\text{movem-saved}(\text{stepn}(s, 8), 4, 8, 2) \\ & \quad = \text{readm-rn}(32, '2\ 3', \text{mc-rfile}(s)))) \end{aligned}$$

THEOREM: strncmp-s-s0-rfile

$$\begin{aligned} & (\text{strncmp-statep}(s, \text{str1}, n1, \text{lst1}, \text{str2}, n2, \text{lst2}, n) \wedge (n \not\simeq 0) \wedge \text{d4-7a2-5p}(rn)) \\ \rightarrow & (\text{read-rn}(\text{oplen}, rn, \text{mc-rfile}(\text{stepn}(s, 8))) \\ & \quad = \text{read-rn}(\text{oplen}, rn, \text{mc-rfile}(s))) \end{aligned}$$

THEOREM: strncmp-s-s0-mem

$$\begin{aligned} & (\text{strncmp-statep}(s, \text{str1}, n1, \text{lst1}, \text{str2}, n2, \text{lst2}, n) \\ & \wedge (n \not\simeq 0) \\ & \wedge \text{disjoint}(x, k, \text{sub}(32, 12, \text{read-sp}(s)), 28)) \\ \rightarrow & (\text{read-mem}(x, \text{mc-mem}(\text{stepn}(s, 8)), k) = \text{read-mem}(x, \text{mc-mem}(s), k)) \end{aligned}$$

; from s0 to exit: s0 --> sn.

; base case 1: s0 --> sn, when lst1[i] =\= lst2[i].

THEOREM: strncmp-s0-sn-base1

$$\begin{aligned} & (\text{strncmp-s0p}(s, i^*, i, \text{str1}, n1, \text{lst1}, \text{str2}, n2, \text{lst2}, n, n_-) \\ & \wedge (\text{get-nth}(i, \text{lst1}) \neq \text{get-nth}(i, \text{lst2}))) \\ \rightarrow & ((\text{mc-status}(\text{stepn}(s, 11)) = \text{'running'}) \\ & \wedge (\text{mc-pc}(\text{stepn}(s, 11)) = \text{linked-rtt-addr}(s)) \\ & \wedge (\text{iread-dn}(32, 0, \text{stepn}(s, 11)) \\ & \quad = \text{idifference}(\text{get-nth}(i, \text{lst1}), \text{get-nth}(i, \text{lst2}))) \\ & \wedge (\text{read-rn}(32, 14, \text{mc-rfile}(\text{stepn}(s, 11))) = \text{linked-a6}(s)) \end{aligned}$$

$\wedge$ (read-rn (32, 15, mc-rfile (stepn (s, 11)))
 $=$ add (32, read-an (32, 6, s), 8))
 $\wedge$ (read-mem (x, mc-mem (stepn (s, 11)), k)
 $=$ read-mem (x, mc-mem (s), k)))

THEOREM: strncmp-s0-sn-rfile-base1

(strncmp-s0p (s, i*, i, str1, n1, lst1, str2, n2, lst2, n, n_-)
 $\wedge$ (get-nth (i, lst1) $\neq$ get-nth (i, lst2))
 $\wedge$ (oplen $\leq$ 32)
 $\wedge$ d2-7a2-5p (rn))
 $\rightarrow$ (read-rn (oplen, rn, mc-rfile (stepn (s, 11)))
 $=$ if d4-7a2-5p (rn) then read-rn (oplen, rn, mc-rfile (s))
 else get-vlst (oplen, 0, rn, ' (2 3), movem-saved (s, 4, 8, 2)) endif)

; base case 2: s0 --> sn, when lst[i] = lst2[i] and lst[i] = 0.

THEOREM: strncmp-s0-sn-base2

(strncmp-s0p (s, i*, i, str1, n1, lst1, str2, n2, lst2, n, n_-)
 $\wedge$ (get-nth (i, lst1) = get-nth (i, lst2))
 $\wedge$ (get-nth (i, lst1) = 0))
 $\rightarrow$ ((mc-status (stepn (s, 9)) = 'running)
 $\wedge$ (mc-pc (stepn (s, 9)) = linked-rtb-addr (s))
 $\wedge$ (iread-dn (32, 0, stepn (s, 9)) = 0)
 $\wedge$ (read-rn (32, 14, mc-rfile (stepn (s, 9))) = linked-a6 (s))
 $\wedge$ (read-rn (32, 15, mc-rfile (stepn (s, 9)))
 $=$ add (32, read-an (32, 6, s), 8))
 $\wedge$ (read-mem (x, mc-mem (stepn (s, 9)), k)
 $=$ read-mem (x, mc-mem (s), k)))

THEOREM: strncmp-s0-sn-rfile-base2

(strncmp-s0p (s, i*, i, str1, n1, lst1, str2, n2, lst2, n, n_-)
 $\wedge$ (get-nth (i, lst1) = get-nth (i, lst2))
 $\wedge$ (get-nth (i, lst1) = 0)
 $\wedge$ (oplen $\leq$ 32)
 $\wedge$ d2-7a2-5p (rn))
 $\rightarrow$ (read-rn (oplen, rn, mc-rfile (stepn (s, 9)))
 $=$ if d4-7a2-5p (rn) then read-rn (oplen, rn, mc-rfile (s))
 else get-vlst (oplen, 0, rn, ' (2 3), movem-saved (s, 4, 8, 2)) endif)

; base case 3: s0 --> sn, when lst[i] = lst2[i], lst[i] $\neq$ 0, and n-1 = 0.

THEOREM: strncmp-s0-sn-base3

(strncmp-s0p (s, i*, i, str1, n1, lst1, str2, n2, lst2, n, n_-)
 $\wedge$ (get-nth (i, lst1) = get-nth (i, lst2))
 $\wedge$ (get-nth (i, lst1) $\neq$ 0)

$\wedge ((n - 1) = 0)$
 $\rightarrow ((\text{mc-status}(\text{stepn}(s, 11)) = \text{'running'})$
 $\quad \wedge (\text{mc-pc}(\text{stepn}(s, 11)) = \text{linked-rt-s-addr}(s))$
 $\quad \wedge (\text{iread-dn}(32, 0, \text{stepn}(s, 11)) = 0)$
 $\quad \wedge (\text{read-rn}(32, 14, \text{mc-rfile}(\text{stepn}(s, 11))) = \text{linked-a6}(s))$
 $\quad \wedge (\text{read-rn}(32, 15, \text{mc-rfile}(\text{stepn}(s, 11)))$
 $\quad \quad = \text{add}(32, \text{read-an}(32, 6, s), 8))$
 $\quad \wedge (\text{read-mem}(x, \text{mc-mem}(\text{stepn}(s, 11)), k)$
 $\quad \quad = \text{read-mem}(x, \text{mc-mem}(s), k))$

THEOREM: strncmp-s0-sn-rfile-base3

$(\text{strncmp-s0p}(s, i^*, i, \text{str1}, n1, \text{lst1}, \text{str2}, n2, \text{lst2}, n, n_-)$
 $\quad \wedge (\text{get-nth}(i, \text{lst1}) = \text{get-nth}(i, \text{lst2}))$
 $\quad \wedge (\text{get-nth}(i, \text{lst1}) \neq 0)$
 $\quad \wedge ((n - 1) = 0)$
 $\quad \wedge (\text{oplen} \leq 32)$
 $\quad \wedge \text{d2-7a2-5p}(rn))$
 $\rightarrow (\text{read-rn}(\text{oplen}, rn, \text{mc-rfile}(\text{stepn}(s, 11)))$
 $\quad = \text{if d4-7a2-5p}(rn) \text{ then read-rn}(\text{oplen}, rn, \text{mc-rfile}(s))$
 $\quad \text{else get-vlst}(\text{oplen}, 0, rn, '(2\ 3), \text{movem-saved}(s, 4, 8, 2)) \text{ endif})$

; induction case: $s0 \rightarrow s0$, $\text{lst}[i] = \text{lst2}[i]$, $\text{lst}[i] \neq 0$ and $n-1 \neq 0$.

THEOREM: strncmp-s0-s0

$(\text{strncmp-s0p}(s, i^*, i, \text{str1}, n1, \text{lst1}, \text{str2}, n2, \text{lst2}, n, n_-)$
 $\quad \wedge (\text{get-nth}(i, \text{lst1}) = \text{get-nth}(i, \text{lst2}))$
 $\quad \wedge (\text{get-nth}(i, \text{lst1}) \neq 0)$
 $\quad \wedge ((n - 1) \neq 0))$
 $\rightarrow (\text{strncmp-s0p}(\text{stepn}(s, 7),$
 $\quad \text{add}(32, i^*, 1),$
 $\quad 1 + i,$
 $\quad \text{str1},$
 $\quad n1,$
 $\quad \text{lst1},$
 $\quad \text{str2},$
 $\quad n2,$
 $\quad \text{lst2},$
 $\quad n - 1,$
 $\quad n_-)$
 $\quad \wedge (\text{read-rn}(32, 14, \text{mc-rfile}(\text{stepn}(s, 7)))$
 $\quad \quad = \text{read-rn}(32, 14, \text{mc-rfile}(s)))$
 $\quad \wedge (\text{linked-a6}(\text{stepn}(s, 7)) = \text{linked-a6}(s))$
 $\quad \wedge (\text{linked-rt-s-addr}(\text{stepn}(s, 7)) = \text{linked-rt-s-addr}(s))$
 $\quad \wedge (\text{movem-saved}(\text{stepn}(s, 7), 4, 8, 2) = \text{movem-saved}(s, 4, 8, 2))$
 $\quad \wedge (\text{read-mem}(x, \text{mc-mem}(\text{stepn}(s, 7)), k)$

$$= \text{read-mem}(x, \text{mc-mem}(s), k))$$

THEOREM: strncmp-s0-s0-rfile

$$\begin{aligned} & (\text{strncmp-s0p}(s, i^*, i, \text{str1}, n1, \text{lst1}, \text{str2}, n2, \text{lst2}, n, n_-) \\ & \wedge (\text{get-nth}(i, \text{lst1}) = \text{get-nth}(i, \text{lst2})) \\ & \wedge (\text{get-nth}(i, \text{lst1}) \neq 0) \\ & \wedge ((n - 1) \neq 0) \\ & \wedge \text{d4-7a2-5p}(rn)) \\ \rightarrow & (\text{read-rn}(\text{oplen}, rn, \text{mc-rfile}(\text{stepn}(s, 7))) \\ & = \text{read-rn}(\text{oplen}, rn, \text{mc-rfile}(s))) \end{aligned}$$

; put together. s0 --> exit.

THEOREM: strncmp-s0-sn

$$\begin{aligned} & \text{let } sn \text{ be stepn}(s, \text{strncmp1-t}(i, n, \text{lst1}, \text{lst2})) \\ & \text{in} \\ & \text{strncmp-s0p}(s, i^*, i, \text{str1}, n1, \text{lst1}, \text{str2}, n2, \text{lst2}, n, n_-) \\ \rightarrow & ((\text{mc-status}(sn) = \text{'running'}) \\ & \wedge (\text{mc-pc}(sn) = \text{linked-rtts-addr}(s)) \\ & \wedge (\text{iread-dn}(32, 0, sn) = \text{strncmp1}(i, n, \text{lst1}, \text{lst2})) \\ & \wedge (\text{read-rn}(32, 14, \text{mc-rfile}(sn)) = \text{linked-a6}(s)) \\ & \wedge (\text{read-rn}(32, 15, \text{mc-rfile}(sn)) \\ & \quad = \text{add}(32, \text{read-an}(32, 6, s), 8)) \\ & \wedge (\text{read-mem}(x, \text{mc-mem}(sn), k) = \text{read-mem}(x, \text{mc-mem}(s), k))) \text{ endlet} \end{aligned}$$

THEOREM: strncmp-s0-sn-rfile

$$\begin{aligned} & \text{let } sn \text{ be stepn}(s, \text{strncmp1-t}(i, n, \text{lst1}, \text{lst2})) \\ & \text{in} \\ & (\text{strncmp-s0p}(s, i^*, i, \text{str1}, n1, \text{lst1}, \text{str2}, n2, \text{lst2}, n, n_-) \\ & \wedge \text{d2-7a2-5p}(rn) \\ & \wedge (\text{oplen} \leq 32)) \\ \rightarrow & (\text{read-rn}(\text{oplen}, rn, \text{mc-rfile}(sn)) \\ & = \text{if } \text{d4-7a2-5p}(rn) \text{ then } \text{read-rn}(\text{oplen}, rn, \text{mc-rfile}(s)) \\ & \quad \text{else get-vlst}(\text{oplen}, \\ & \quad \quad 0, \\ & \quad \quad rn, \\ & \quad \quad \text{'(2 3)}, \\ & \quad \quad \text{movem-saved}(s, 4, 8, 2)) \text{ endif) endlet} \end{aligned}$$

; the correctness of strncmp.

THEOREM: strncmp-correctness

$$\begin{aligned} & \text{let } sn \text{ be stepn}(s, \text{strncmp-t}(n, \text{lst1}, \text{lst2})) \\ & \text{in} \\ & \text{strncmp-statep}(s, \text{str1}, n1, \text{lst1}, \text{str2}, n2, \text{lst2}, n) \end{aligned}$$

```

→ ((mc-status(sn) = 'running)
   ∧ (mc-pc(sn) = rts-addr(s))
   ∧ (read-rn(32, 14, mc-rfile(sn))
      = read-rn(32, 14, mc-rfile(s)))
   ∧ (read-rn(32, 15, mc-rfile(sn))
      = add(32, read-an(32, 7, s), 4))
   ∧ ((d2-7a2-5p(rn) ∧ (oplen ≤ 32))
      → (read-rn(oplen, rn, mc-rfile(sn))
          = read-rn(oplen, rn, mc-rfile(s))))
   ∧ (disjoint(x, k, sub(32, 12, read-sp(s)), 28)
      → (read-mem(x, mc-mem(sn), k)
          = read-mem(x, mc-mem(s), k)))
   ∧ (iread-dn(32, 0, sn) = strcmp(n, lst1, lst2))) endlet

```

EVENT: Disable strcmp-t.

```

; some properties of strcmp.
; see file cstring.events.

```

EVENT: Make the library "strcmp" and compile it.

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