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|#

EVENT: Start with the library "mc20-2" using the compiled version.

; Proof of the Correctness of the STRPBRK Function

#|

This is part of our effort to verify the Berkeley string library. The Berkeley string library is widely used as part of the Berkeley Unix OS.

This is the source code of strpbrk function in the Berkeley string library.

```
char *
strpbrk(s1, s2)
register const char *s1, *s2;
{
register const char *scanp;
register int c, sc;

while ((c = *s1++) != 0) {
for (scanp = s2; (sc = *scanp++) != 0;)
```

```

if (sc == c)
return ((char *) (s1 - 1));
}
return (NULL);
}

```

Remark. Should the local variables c and sc have type register char?

The MC68020 assembly code of the C function strpbrk on SUN-3 is given as follows. This binary is generated by "gcc -O".

```

0x2688 <strpbrk>:      linkw fp,#0
0x268c <strpbrk+4>:    moveml d2-d3,sp@-
0x2690 <strpbrk+8>:    moveal fp@(8),a1
0x2694 <strpbrk+12>:   move1 fp@(12),d3
0x2698 <strpbrk+16>:   bra 0x26b0 <strpbrk+40>
0x269a <strpbrk+18>:   moveal d3,a0
0x269c <strpbrk+20>:   move1 a1,d1
0x269e <strpbrk+22>:   subl #1,d1
0x26a0 <strpbrk+24>:   bra 0x26aa <strpbrk+34>
0x26a2 <strpbrk+26>:   cmpl d0,d2
0x26a4 <strpbrk+28>:   bne 0x26aa <strpbrk+34>
0x26a6 <strpbrk+30>:   move1 d1,d0
0x26a8 <strpbrk+32>:   bra 0x26b8 <strpbrk+48>
0x26aa <strpbrk+34>:   moveb a0@+,d0
0x26ac <strpbrk+36>:   extbl d0
0x26ae <strpbrk+38>:   bne 0x26a2 <strpbrk+26>
0x26b0 <strpbrk+40>:   moveb a1@+,d2
0x26b2 <strpbrk+42>:   extbl d2
0x26b4 <strpbrk+44>:   bne 0x269a <strpbrk+18>
0x26b6 <strpbrk+46>:   clrl d0
0x26b8 <strpbrk+48>:   moveml fp@(-8),d2-d3
0x26be <strpbrk+54>:   unlk fp
0x26c0 <strpbrk+56>:   rts

```

The machine code of the above program is:

<strpbrk>:	0x4e56	0x0000	0x48e7	0x3000	0x226e	0x0008	0x262e	0x000c
<strpbrk+16>:	0x6016	0x2043	0x2209	0x5381	0x6008	0xb480	0x6604	0x2001
<strpbrk+32>:	0x600e	0x1018	0x49c0	0x66f2	0x1419	0x49c2	0x66e4	0x4280
<strpbrk+48>:	0x4cee	0x000c	0xfff8	0x4e5e	0x4e75			

'(78	86	0	0	72	231	48	0
34	110	0	8	38	46	0	12

96	22	32	67	34	9	83	129
96	8	180	128	102	4	32	1
96	14	16	24	73	192	102	242
20	25	73	194	102	228	66	128
76	238	0	12	255	248	78	94
78	117)						

|#

; in the logic, the above program is defined by (strpbrk-code).

DEFINITION:

STRPBRK-CODE

```
= ' (78 86 0 0 72 231 48 0 34 110 0 8 38 46 0 12 96 22 32
    67 34 9 83 129 96 8 180 128 102 4 32 1 96 14 16 24
    73 192 102 242 20 25 73 194 102 228 66 128 76 238 0
    12 255 248 78 94 78 117)
```

; the computation time of the program.

DEFINITION:

strpbrk-t0(*i2*, *n2*, *lst2*, *ch*)

```
= if i2 < n2
    then if get-nth(i2, lst2) = 0 then 3
        elseif get-nth(i2, lst2) = ch then 10
        else splus(5, strpbrk-t0(1 + i2, n2, lst2, ch)) endif
    else 0 endif
```

DEFINITION:

strpbrk-t1(*n2*, *lst2*, *ch*) = splus(7, strpbrk-t0(0, *n2*, *lst2*, *ch*))

DEFINITION:

strpbrk-t2(*i1*, *n1*, *lst1*, *n2*, *lst2*)

```
= if i1 < n1
    then if get-nth(i1, lst1) = 0 then 7
        elseif strchr1(0, n2, lst2, get-nth(i1, lst1))
        then strpbrk-t1(n2, lst2, get-nth(i1, lst1))
        else splus(strpbrk-t1(n2, lst2, get-nth(i1, lst1)),
                    strpbrk-t2(1 + i1, n1, lst1, n2, lst2)) endif
    else 0 endif
```

DEFINITION:

strpbrk-t(*n1*, *lst1*, *n2*, *lst2*) = splus(5, strpbrk-t2(0, *n1*, *lst1*, *n2*, *lst2*))

; two induction hints.

DEFINITION:

```

strpbrk-induct0(s, i2*, i2, n2, lst2, ch)
=  if i2 < n2
    then if get-nth(i2, lst2) = 0 then t
        elseif get-nth(i2, lst2) = ch then t
        else strpbrk-induct0(stepn(s, 5),
                                add(32, i2*, 1),
                                1 + i2,
                                n2,
                                lst2,
                                ch) endif
    else t endif

```

DEFINITION:

```

strpbrk-induct1(s, i1*, i1, n1, lst1, n2, lst2)
=  if i1 < n1
    then if (get-nth(i1, lst1) = 0)
        ∨ strchr1(0, n2, lst2, get-nth(i1, lst1)) then t
        else strpbrk-induct1(stepn(s,
                                    strpbrk-t1(n2,
                                                lst2,
                                                get-nth(i1, lst1))),
                                add(32, i1*, 1),
                                1 + i1,
                                n1,
                                lst1,
                                n2,
                                lst2) endif
    else t endif

```

; the preconditions of the initial state.

DEFINITION:

```

strpbrk-statep(s, str1, n1, lst1, str2, n2, lst2)
=  ((mc-status(s) = 'running)
    ∧ evenp(mc-pc(s))
    ∧ rom-addrp(mc-pc(s), mc-mem(s), 58)
    ∧ mcode-addrp(mc-pc(s), mc-mem(s), STRPBRK-CODE)
    ∧ ram-addrp(sub(32, 12, read-sp(s)), mc-mem(s), 24)
    ∧ ram-addrp(str1, mc-mem(s), n1)
    ∧ mem-lst(1, str1, mc-mem(s), n1, lst1)
    ∧ ram-addrp(str2, mc-mem(s), n2)
    ∧ mem-lst(1, str2, mc-mem(s), n2, lst2)
    ∧ disjoint(sub(32, 12, read-sp(s)), 24, str1, n1)
    ∧ disjoint(sub(32, 12, read-sp(s)), 24, str2, n2)

```

$$\begin{aligned}
& \wedge (\text{str1} = \text{read-mem}(\text{add}(32, \text{read-sp}(s), 4), \text{mc-mem}(s), 4)) \\
& \wedge (\text{str2} = \text{read-mem}(\text{add}(32, \text{read-sp}(s), 8), \text{mc-mem}(s), 4)) \\
& \wedge (\text{slen}(0, n1, \text{lst1}) < n1) \\
& \wedge (\text{slen}(0, n2, \text{lst2}) < n2) \\
& \wedge (n1 \in \mathbf{N}) \\
& \wedge (n2 \in \mathbf{N}) \\
& \wedge \text{uint-range}(n1, 32) \\
& \wedge \text{uint-range}(n2, 32) \\
& \wedge (\text{nat-to-uint}(\text{str1}) \neq 0) \\
& \wedge \text{uint-range}(\text{nat-to-uint}(\text{str1}) + n1, 32)
\end{aligned}$$

DEFINITION:

$$\begin{aligned}
& \text{strpbrk-s0p}(s, i1^*, i1, \text{str1}, n1, \text{lst1}, \text{str2}, n2, \text{lst2}) \\
= & ((\text{mc-status}(s) = \text{'running'}) \\
& \wedge \text{evenp}(\text{mc-pc}(s)) \\
& \wedge \text{rom-addrp}(\text{sub}(32, 40, \text{mc-pc}(s)), \text{mc-mem}(s), 58) \\
& \wedge \text{mcode-addrp}(\text{sub}(32, 40, \text{mc-pc}(s)), \text{mc-mem}(s), \text{STRPBRK-CODE}) \\
& \wedge \text{ram-addrp}(\text{sub}(32, 8, \text{read-an}(32, 6, s)), \text{mc-mem}(s), 24) \\
& \wedge \text{ram-addrp}(\text{str1}, \text{mc-mem}(s), n1) \\
& \wedge \text{mem-lst}(1, \text{str1}, \text{mc-mem}(s), n1, \text{lst1}) \\
& \wedge \text{ram-addrp}(\text{str2}, \text{mc-mem}(s), n2) \\
& \wedge \text{mem-lst}(1, \text{str2}, \text{mc-mem}(s), n2, \text{lst2}) \\
& \wedge \text{disjoint}(\text{sub}(32, 8, \text{read-an}(32, 6, s)), 24, \text{str1}, n1) \\
& \wedge \text{disjoint}(\text{sub}(32, 8, \text{read-an}(32, 6, s)), 24, \text{str2}, n2) \\
& \wedge \text{equal}^*(\text{read-an}(32, 1, s), \text{add}(32, \text{str1}, i1^*)) \\
& \wedge (\text{str2} = \text{read-dn}(32, 3, s)) \\
& \wedge (i1^* \in \mathbf{N}) \\
& \wedge \text{nat-range}(i1^*, 32) \\
& \wedge (i1 = \text{nat-to-uint}(i1^*)) \\
& \wedge (\text{slen}(i1, n1, \text{lst1}) < n1) \\
& \wedge (\text{slen}(0, n2, \text{lst2}) < n2) \\
& \wedge (n1 \in \mathbf{N}) \\
& \wedge (n2 \in \mathbf{N}) \\
& \wedge \text{uint-range}(n1, 32) \\
& \wedge \text{uint-range}(n2, 32))
\end{aligned}$$

DEFINITION:

$$\begin{aligned}
& \text{strpbrk-s1p}(s, i1^*, i1, \text{str1}, n1, \text{lst1}, i2^*, i2, \text{str2}, n2, \text{lst2}, ch) \\
= & ((\text{mc-status}(s) = \text{'running'}) \\
& \wedge \text{evenp}(\text{mc-pc}(s)) \\
& \wedge \text{rom-addrp}(\text{sub}(32, 34, \text{mc-pc}(s)), \text{mc-mem}(s), 58) \\
& \wedge \text{mcode-addrp}(\text{sub}(32, 34, \text{mc-pc}(s)), \text{mc-mem}(s), \text{STRPBRK-CODE}) \\
& \wedge \text{ram-addrp}(\text{sub}(32, 8, \text{read-an}(32, 6, s)), \text{mc-mem}(s), 24) \\
& \wedge \text{ram-addrp}(\text{str1}, \text{mc-mem}(s), n1)
\end{aligned}$$

$\wedge$ mem-*lst* (1, *str1*, mc-mem (*s*), *n1*, *lst1*)
 $\wedge$ ram-addrp (*str2*, mc-mem (*s*), *n2*)
 $\wedge$ mem-*lst* (1, *str2*, mc-mem (*s*), *n2*, *lst2*)
 $\wedge$ disjoint (sub (32, 8, read-an (32, 6, *s*)), 24, *str1*, *n1*)
 $\wedge$ disjoint (sub (32, 8, read-an (32, 6, *s*)), 24, *str2*, *n2*)
 $\wedge$ equal* (read-an (32, 1, *s*), add (32, *str1*, *i1**))
 $\wedge$ equal* (read-an (32, 0, *s*), add (32, *str2*, *i2**))
 $\wedge$ equal* (read-dn (32, 1, *s*), sub (32, 1, read-an (32, 1, *s*)))
 $\wedge$ equal* (read-dn (32, 2, *s*), ext (8, read-dn (8, 2, *s*), 32))
 $\wedge$ (*str2* = read-dn (32, 3, *s*))
 $\wedge$ (*ch* = uread-dn (8, 2, *s*))
 $\wedge$ (slen (*i1*, *n1*, *lst1*) < *n1*)
 $\wedge$ (slen (0, *n2*, *lst2*) < *n2*)
 $\wedge$ (slen (*i2*, *n2*, *lst2*) < *n2*)
 $\wedge$ (*i1** $\in \mathbf{N}$)
 $\wedge$ nat-range (*i1**, 32)
 $\wedge$ (*i1* = nat-to-uint (*i1**))
 $\wedge$ (*i2** $\in \mathbf{N}$)
 $\wedge$ nat-range (*i2**, 32)
 $\wedge$ (*i2* = nat-to-uint (*i2**))
 $\wedge$ (*n1* $\in \mathbf{N}$)
 $\wedge$ uint-range (*n1*, 32)
 $\wedge$ (*n2* $\in \mathbf{N}$)
 $\wedge$ uint-range (*n2*, 32))

; from the initial state *s* to *s0*: *s* --> *s0*.

THEOREM: strpbrk-s-s0

strpbrk-statep (*s*, *str1*, *n1*, *lst1*, *str2*, *n2*, *lst2*)
 $\rightarrow$ strpbrk-s0p (stepn (*s*, 5), 0, 0, *str1*, *n1*, *lst1*, *str2*, *n2*, *lst2*)

THEOREM: strpbrk-s-s0-else

strpbrk-statep (*s*, *str1*, *n1*, *lst1*, *str2*, *n2*, *lst2*)
 $\rightarrow$ ((linked-rt_s-addr (stepn (*s*, 5)) = rts-addr (*s*))
 $\wedge$ (linked-a6 (stepn (*s*, 5)) = read-an (32, 6, *s*))
 $\wedge$ (read-an (32, 6, stepn (*s*, 5)) = sub (32, 4, read-sp (*s*)))
 $\wedge$ (movem-saved (stepn (*s*, 5), 4, 8, 2)
 $=$ readm-rn (32, ' (2 3), mc-rfile (*s*))))

THEOREM: strpbrk-s-s0-rfile

(strpbrk-statep (*s*, *str1*, *n1*, *lst1*, *str2*, *n2*, *lst2*) $\wedge$ d4-7a2-5p (*rn*))
 $\rightarrow$ (read-rn (*oplen*, *rn*, mc-rfile (stepn (*s*, 5)))
 $=$ read-rn (*oplen*, *rn*, mc-rfile (*s*)))

THEOREM: strpbrk-s-s0-mem

```

(strpbrk-statep(s, str1, n1, lst1, str2, n2, lst2)
  ∧ disjoint(x, k, sub(32, 12, read-sp(s), 24))
→ (read-mem(x, mc-mem(stepn(s, 5)), k) = read-mem(x, mc-mem(s), k))

; from s0 to exit.
; base case. s0 --> sn, when lst1[i1] = 0.

```

THEOREM: strpbrk-s0-sn-base1

```

(strpbrk-s0p(s, i1*, i1, str1, n1, lst1, str2, n2, lst2)
  ∧ (get-nth(i1, lst1) = 0))
→ ((mc-status(stepn(s, 7)) = 'running)
    ∧ (mc-pc(stepn(s, 7)) = linked-rtt-addr(s))
    ∧ (read-dn(32, 0, stepn(s, 7)) = 0)
    ∧ (read-an(32, 6, stepn(s, 7)) = linked-a6(s))
    ∧ (read-an(32, 7, stepn(s, 7)) = add(32, read-an(32, 6, s), 8))
    ∧ (read-mem(x, mc-mem(stepn(s, 7)), k)
      = read-mem(x, mc-mem(s), k)))

```

THEOREM: strpbrk-s0-sn-rfile-base1

```

(strpbrk-s0p(s, i1*, i1, str1, n1, lst1, str2, n2, lst2)
  ∧ (get-nth(i1, lst1) = 0)
  ∧ (oplen ≤ 32)
  ∧ d2-7a2-5p(rn))
→ (read-rn(oplen, rn, mc-rfile(stepn(s, 7)))
    = if d4-7a2-5p(rn) then read-rn(oplen, rn, mc-rfile(s))
      else get-vlst(oplen, 0, rn, '(2 3), movem-saved(s, 4, 8, 2)) endif)

```

```

; induction case. s0 --> s0, when lst1[i1] =\= 0.
; from s0 to s1: s0 --> s1, when lst1[i1] =\= 0.

```

THEOREM: strpbrk-s0-s1

```

(strpbrk-s0p(s, i1*, i1, str1, n1, lst1, str2, n2, lst2)
  ∧ (get-nth(i1, lst1) ≠ 0))
→ strpbrk-s1p(stepn(s, 7),
  add(32, i1*, 1),
  1 + i1,
  str1,
  n1,
  lst1,
  0,
  0,
  str2,
  n2,
  lst2,
  get-nth(i1, lst1))

```

THEOREM: strpbrk-s0-s1-else

$$\begin{aligned}
& (\text{strpbrk-s0p}(s, i1^*, i1, str1, n1, lst1, str2, n2, lst2) \\
& \quad \wedge \text{ (get-nth}(i1, lst1) \neq 0)) \\
\rightarrow & ((\text{read-an}(32, 6, \text{stepn}(s, 7)) = \text{read-an}(32, 6, s)) \\
& \quad \wedge (\text{linked-a6}(\text{stepn}(s, 7)) = \text{linked-a6}(s)) \\
& \quad \wedge (\text{linked-rtts-addr}(\text{stepn}(s, 7)) = \text{linked-rtts-addr}(s)) \\
& \quad \wedge (\text{movem-saved}(\text{stepn}(s, 7), 4, 8, 2) = \text{movem-saved}(s, 4, 8, 2)) \\
& \quad \wedge (\text{read-mem}(x, \text{mc-mem}(\text{stepn}(s, 7)), k) \\
& \quad \quad = \text{read-mem}(x, \text{mc-mem}(s), k)))
\end{aligned}$$

THEOREM: strpbrk-s0-s1-rfile

$$\begin{aligned}
& (\text{strpbrk-s0p}(s, i1^*, i1, str1, n1, lst1, str2, n2, lst2) \\
& \quad \wedge \text{ (get-nth}(i1, lst1) \neq 0) \\
& \quad \wedge \text{ d4-7a2-5p}(rn)) \\
\rightarrow & (\text{read-rn}(oplen, rn, \text{mc-rfile}(\text{stepn}(s, 7))) \\
& \quad = \text{read-rn}(oplen, rn, \text{mc-rfile}(s)))
\end{aligned}$$

; from s1 to s0:

; base case. s1 --> s0, when lst2[i2] = 0.

THEOREM: strpbrk-s1-s0-base

$$\begin{aligned}
& (\text{strpbrk-s1p}(s, i1^*, i1, str1, n1, lst1, i2^*, i2, str2, n2, lst2, ch) \\
& \quad \wedge \text{ (get-nth}(i2, lst2) = 0)) \\
\rightarrow & (\text{strpbrk-s0p}(\text{stepn}(s, 3), i1^*, i1, str1, n1, lst1, str2, n2, lst2) \\
& \quad \wedge (\text{read-rn}(32, 14, \text{mc-rfile}(\text{stepn}(s, 3))) \\
& \quad \quad = \text{read-rn}(32, 14, \text{mc-rfile}(s))) \\
& \quad \wedge (\text{linked-a6}(\text{stepn}(s, 3)) = \text{linked-a6}(s)) \\
& \quad \wedge (\text{linked-rtts-addr}(\text{stepn}(s, 3)) = \text{linked-rtts-addr}(s)) \\
& \quad \wedge (\text{movem-saved}(\text{stepn}(s, 3), 4, 8, 2) = \text{movem-saved}(s, 4, 8, 2)) \\
& \quad \wedge (\text{read-mem}(x, \text{mc-mem}(\text{stepn}(s, 3)), k) \\
& \quad \quad = \text{read-mem}(x, \text{mc-mem}(s), k)))
\end{aligned}$$

THEOREM: strpbrk-s1-s0-rfile-base

$$\begin{aligned}
& (\text{strpbrk-s1p}(s, i1^*, i1, str1, n1, lst1, i2^*, i2, str2, n2, lst2, ch) \\
& \quad \wedge \text{ (get-nth}(i2, lst2) = 0) \\
& \quad \wedge \text{ d4-7a2-5p}(rn)) \\
\rightarrow & (\text{read-rn}(oplen, rn, \text{mc-rfile}(\text{stepn}(s, 3))) \\
& \quad = \text{read-rn}(oplen, rn, \text{mc-rfile}(s)))
\end{aligned}$$

; induction case. s1 --> s1, when lst2[i2] = 0 and lst2[i2] = ch.

THEOREM: strpbrk-s1-s1

$$\begin{aligned}
& (\text{strpbrk-s1p}(s, i1^*, i1, str1, n1, lst1, i2^*, i2, str2, n2, lst2, ch) \\
& \quad \wedge \text{ (get-nth}(i2, lst2) \neq 0) \\
& \quad \wedge \text{ (get-nth}(i2, lst2) \neq ch))
\end{aligned}$$

$\rightarrow$ (strpbrk-slp (stepn (s , 5),
 $i1^*$,
 $i1$,
 $str1$,
 $n1$,
 $lst1$,
add (32, $i2^*$, 1),
 $1 + i2$,
 $str2$,
 $n2$,
 $lst2$,
 ch)
 $\wedge$ (read-rn (32, 14, mc-rfile (stepn (s , 5)))
= read-rn (32, 14, mc-rfile (s)))
 $\wedge$ (linked-a6 (stepn (s , 5)) = linked-a6 (s))
 $\wedge$ (linked-rtts-addr (stepn (s , 5)) = linked-rtts-addr (s))
 $\wedge$ (movem-saved (stepn (s , 5), 4, 8, 2) = movem-saved (s , 4, 8, 2))
 $\wedge$ (read-mem (x , mc-mem (stepn (s , 5)), k)
= read-mem (x , mc-mem (s), k)))

THEOREM: strpbrk-s1-s1-rfile

(strpbrk-slp (s , $i1^*$, $i1$, $str1$, $n1$, $lst1$, $i2^*$, $i2$, $str2$, $n2$, $lst2$, ch)
 $\wedge$ (get-nth ($i2$, $lst2$) $\neq$ 0)
 $\wedge$ (get-nth ($i2$, $lst2$) $\neq$ ch)
 $\wedge$ d4-7a2-5p (rn)
 $\rightarrow$ (read-rn ($oplen$, rn , mc-rfile (stepn (s , 5)))
= read-rn ($oplen$, rn , mc-rfile (s)))

; put together. s1 --> s0.

THEOREM: strpbrk-slp-info

strpbrk-slp (s , $i1^*$, $i1$, $str1$, $n1$, $lst1$, $i2^*$, $i2$, $str2$, $n2$, $lst2$, ch)
 $\rightarrow$ (($i2 < n2$) = t)

THEOREM: strpbrk-s1-s0

let $s0$ **be** stepn (s , strpbrk-t0 ($i2$, $n2$, $lst2$, ch))
in
(strpbrk-slp (s , $i1^*$, $i1$, $str1$, $n1$, $lst1$, $i2^*$, $i2$, $str2$, $n2$, $lst2$, ch)
 $\wedge$ ($\neg$ strchr1 ($i2$, $n2$, $lst2$, ch)))
 $\rightarrow$ (strpbrk-s0p ($s0$, $i1^*$, $i1$, $str1$, $n1$, $lst1$, $str2$, $n2$, $lst2$)
 $\wedge$ (read-an (32, 6, $s0$) = read-an (32, 6, s))
 $\wedge$ (linked-a6 ($s0$) = linked-a6 (s))
 $\wedge$ (linked-rtts-addr ($s0$) = linked-rtts-addr (s))
 $\wedge$ (movem-saved ($s0$, 4, 8, 2) = movem-saved (s , 4, 8, 2))
 $\wedge$ (read-mem (x , mc-mem ($s0$), k) = read-mem (x , mc-mem (s), k))) **endlet**

THEOREM: strpbrk-s1-s0-rfile

```
(strpbrk-s1p(s, i1*, i1, str1, n1, lst1, i2*, i2, str2, n2, lst2, ch)
  ∧ (¬ strchr1(i2, n2, lst2, ch))
  ∧ d4-7a2-5p(rn))
→ (read-rn(oplen, rn, mc-rfile(stepn(s, strpbrk-t0(i2, n2, lst2, ch))))
    = read-rn(oplen, rn, mc-rfile(s)))
```

; from s1 to exit:

; base case. s1 --> sn, when lst2[i2] =\= 0 and lst2[i2] = ch.

THEOREM: strpbrk-s1-sn-base

```
(strpbrk-s1p(s, i1*, i1, str1, n1, lst1, i2*, i2, str2, n2, lst2, ch)
  ∧ (get-nth(i2, lst2) ≠ 0)
  ∧ (get-nth(i2, lst2) = ch))
→ ((mc-status(stepn(s, 10)) = 'running)
    ∧ (mc-pc(stepn(s, 10)) = linked-rtt-addr(s))
    ∧ (read-dn(32, 0, stepn(s, 10)) = add(32, str1, sub(32, 1, i1*)))
    ∧ (read-rn(32, 14, mc-rfile(stepn(s, 10))) = linked-a6(s))
    ∧ (read-rn(32, 15, mc-rfile(stepn(s, 10)))
        = add(32, read-an(32, 6, s), 8))
    ∧ (read-mem(x, mc-mem(stepn(s, 10)), k)
        = read-mem(x, mc-mem(s), k)))
```

THEOREM: strpbrk-s1-sn-rfile-base

```
(strpbrk-s1p(s, i1*, i1, str1, n1, lst1, i2*, i2, str2, n2, lst2, ch)
  ∧ (get-nth(i2, lst2) ≠ 0)
  ∧ (get-nth(i2, lst2) = ch)
  ∧ (oplen ≤ 32)
  ∧ d2-7a2-5p(rn))
→ (read-rn(oplen, rn, mc-rfile(stepn(s, 10)))
    = if d4-7a2-5p(rn) then read-rn(oplen, rn, mc-rfile(s))
      else get-vlst(oplen, 0, rn, '(2 3), movem-saved(s, 4, 8, 2)) endif)
```

; put together. s1 --> sn.

THEOREM: strpbrk-s1-sn

```
let sn be stepn(s, strpbrk-t0(i2, n2, lst2, ch))
in
(strpbrk-s1p(s, i1*, i1, str1, n1, lst1, i2*, i2, str2, n2, lst2, ch)
  ∧ strchr1(i2, n2, lst2, ch))
→ ((mc-status(sn) = 'running)
    ∧ (mc-pc(sn) = linked-rtt-addr(s))
    ∧ (read-dn(32, 0, sn) = add(32, str1, sub(32, 1, i1*)))
    ∧ (read-an(32, 6, sn) = linked-a6(s))
    ∧ (read-an(32, 7, sn) = add(32, read-an(32, 6, s), 8))
    ∧ (read-mem(x, mc-mem(sn), k) = read-mem(x, mc-mem(s), k))) endlet
```

THEOREM: strpbrk-s1-sn-rfile

```
(strpbrk-s1p(s, i1*, i1, str1, n1, lst1, i2*, i2, str2, n2, lst2, ch)
  ∧ strchr1(i2, n2, lst2, ch)
  ∧ d2-7a2-5p(rn)
  ∧ (oplen ≤ 32))
→ (read-rn(oplen, rn, mc-rfile(stepn(s, strpbrk-t0(i2, n2, lst2, ch))))
    = if d4-7a2-5p(rn) then read-rn(oplen, rn, mc-rfile(s))
      else get-vlst(oplen, 0, rn, '(2 3), movem-saved(s, 4, 8, 2)) endif)
```

; base case 2. from *s0* --> *sn*.

THEOREM: strpbrk-s0-sn-base2

```
let ch be get-nth(i1, lst1),
    sn be stepn(s, strpbrk-t1(n2, lst2, get-nth(i1, lst1)))
in
(strpbrk-s0p(s, i1*, i1, str1, n1, lst1, str2, n2, lst2)
  ∧ (get-nth(i1, lst1) ≠ 0)
  ∧ strchr1(0, n2, lst2, ch))
→ ((mc-status(sn) = 'running)
    ∧ (mc-pc(sn) = linked-rtt-addr(s))
    ∧ (read-dn(32, 0, sn) = add(32, str1, i1*))
    ∧ (read-an(32, 6, sn) = linked-a6(s))
    ∧ (read-an(32, 7, sn) = add(32, read-an(32, 6, s), 8))
    ∧ (read-mem(x, mc-mem(sn), k) = read-mem(x, mc-mem(s), k))) endlet
```

THEOREM: strpbrk-s0-sn-rfile-base2

```
let ch be get-nth(i1, lst1)
in
(strpbrk-s0p(s, i1*, i1, str1, n1, lst1, str2, n2, lst2)
  ∧ (get-nth(i1, lst1) ≠ 0)
  ∧ strchr1(0, n2, lst2, ch)
  ∧ (oplen ≤ 32)
  ∧ d2-7a2-5p(rn))
→ (read-rn(oplen, rn, mc-rfile(stepn(s, strpbrk-t1(n2, lst2, ch))))
    = if d4-7a2-5p(rn) then read-rn(oplen, rn, mc-rfile(s))
      else get-vlst(oplen,
                    0,
                    rn,
                    '(2 3),
                    movem-saved(s, 4, 8, 2)) endif) endlet
```

; induction case. from *s0* --> *s0*.

THEOREM: strpbrk-s0-s0

```
let ch be get-nth(i1, lst1),
```

```

    s0 be stepn(s, strpbrk-t1(n2, lst2, get-nth(i1, lst1)))
in
(strpbrk-s0p(s, i1*, i1, str1, n1, lst1, str2, n2, lst2)
  ∧ (get-nth(i1, lst1) ≠ 0)
  ∧ (¬ strchr1(0, n2, lst2, ch)))
→ (strpbrk-s0p(s0,
    add(32, i1*, 1),
    1 + i1,
    str1,
    n1,
    lst1,
    str2,
    n2,
    lst2)
  ∧ (read-an(32, 6, s0) = read-an(32, 6, s))
  ∧ (linked-a6(s0) = linked-a6(s))
  ∧ (linked-rtts-addr(s0) = linked-rtts-addr(s))
  ∧ (movem-saved(s0, 4, 8, 2) = movem-saved(s, 4, 8, 2))
  ∧ (read-mem(x, mc-mem(s0), k) = read-mem(x, mc-mem(s), k))) endlet

```

THEOREM: strpbrk-s0-s0-rfile

```

let ch be get-nth(i1, lst1)
in
(strpbrk-s0p(s, i1*, i1, str1, n1, lst1, str2, n2, lst2)
  ∧ (get-nth(i1, lst1) ≠ 0)
  ∧ (¬ strchr1(0, n2, lst2, ch))
  ∧ d4-7a2-5p(rn))
→ (read-rn(oplen, rn, mc-rfile(stepn(s, strpbrk-t1(n2, lst2, ch))))
  = read-rn(oplen, rn, mc-rfile(s))) endlet

; put together. s0 --> sn.

```

THEOREM: strpbrk-s0p-info

```

strpbrk-s0p(s, i1*, i1, str1, n1, lst1, str2, n2, lst2) → ((i1 < n1) = t)

```

THEOREM: strpbrk-s0-sn

```

let sn be stepn(s, strpbrk-t2(i1, n1, lst1, n2, lst2))
in
strpbrk-s0p(s, i1*, i1, str1, n1, lst1, str2, n2, lst2)
→ ((mc-status(sn) = 'running)
  ∧ (mc-pc(sn) = linked-rtts-addr(s))
  ∧ (read-dn(32, 0, sn)
    = if strpbrk(i1, n1, lst1, n2, lst2)
      then add(32,
        str1,

```

```

                                strpbrk*(i1*, i1, n1, lst1, n2, lst2))
                        else 0 endif)
    ∧ (read-an(32, 6, sn) = linked-a6(s))
    ∧ (read-an(32, 7, sn) = add(32, read-an(32, 6, s), 8))
    ∧ (read-mem(x, mc-mem(sn), k) = read-mem(x, mc-mem(s), k))) endlet

```

THEOREM: strpbrk-s0-sn-rfile

```

let sn be stepn(s, strpbrk-t2(i1, n1, lst1, n2, lst2))
in
  (strpbrk-s0p(s, i1*, i1, str1, n1, lst1, str2, n2, lst2)
   ∧ d2-7a2-5p(rn)
   ∧ (oplen ≤ 32))
  → (read-rn(oplen, rn, mc-rfile(sn))
      = if d4-7a2-5p(rn) then read-rn(oplen, rn, mc-rfile(s))
        else get-vlst(oplen,
                     0,
                     rn,
                     '(2 3),
                     movem-saved(s, 4, 8, 2)) endif) endlet

```

; the correctness of strpbrk.

THEOREM: strpbrk-correctness

```

let sn be stepn(s, strpbrk-t(n1, lst1, n2, lst2))
in
  strpbrk-statep(s, str1, n1, lst1, str2, n2, lst2)
  → ((mc-status(sn) = 'running)
      ∧ (mc-pc(sn) = rts-addr(s))
      ∧ (read-an(32, 6, sn) = read-an(32, 6, s))
      ∧ (read-an(32, 7, sn) = add(32, read-an(32, 7, s), 4))
      ∧ ((d2-7a2-5p(rn) ∧ (oplen ≤ 32))
          → (read-rn(oplen, rn, mc-rfile(sn))
              = read-rn(oplen, rn, mc-rfile(s))))
      ∧ (disjoint(x, k, sub(32, 12, read-sp(s)), 24)
          → (read-mem(x, mc-mem(sn), k)
              = read-mem(x, mc-mem(s), k)))
      ∧ (read-dn(32, 0, sn)
          = if strpbrk(0, n1, lst1, n2, lst2)
            then add(32,
                    str1,
                    strpbrk*(0, 0, n1, lst1, n2, lst2))
            else 0 endif) endlet

```

EVENT: Disable strpbrk-t.

; strpbrk* --> strpbrk.

THEOREM: strpbrk*-strpbrk
 (strpbrk (*i1*, *n1*, *lst1*, *n2*, *lst2*)
 $\wedge$ (*i1* = nat-to-uint (*i1**))
 $\wedge$ nat-range (*i1**, 32)
 $\wedge$ uint-range (*n1*, 32))
 $\rightarrow$ (nat-to-uint (strpbrk* (*i1**, *i1*, *n1*, *lst1*, *n2*, *lst2*))
 = strpbrk (*i1*, *n1*, *lst1*, *n2*, *lst2*))

THEOREM: strpbrk-non-zerop-la
let *sn* **be** stepn (*s*, strpbrk-t (*n1*, *lst1*, *n2*, *lst2*))
in
 (strpbrk-statep (*s*, *str1*, *n1*, *lst1*, *str2*, *n2*, *lst2*)
 $\wedge$ nat-range (*str1*, 32)
 $\wedge$ (nat-to-uint (*str1*) $\neq$ 0)
 $\wedge$ uint-range (nat-to-uint (*str1*) + *n1*, 32)
 $\wedge$ strpbrk (0, *n1*, *lst1*, *n2*, *lst2*))
 $\rightarrow$ (nat-to-uint (read-dn (32, 0, *sn*)) $\neq$ 0) **endlet**

THEOREM: strpbrk-non-zerop
let *sn* **be** stepn (*s*, strpbrk-t (*n1*, *lst1*, *n2*, *lst2*))
in
 (strpbrk-statep (*s*, *str1*, *n1*, *lst1*, *str2*, *n2*, *lst2*)
 $\wedge$ strpbrk (0, *n1*, *lst1*, *n2*, *lst2*))
 $\rightarrow$ (nat-to-uint (read-dn (32, 0, *sn*)) $\neq$ 0) **endlet**

EVENT: Disable strpbrk*.

; some properties of strrchr.
 ; see file cstring.events.

Index

- add, 4–7, 9–13
- d2-7a2-5p, 7, 10, 11, 13
- d4-7a2-5p, 6–13
- disjoint, 4–7, 13
- equal*, 5, 6
- evenp, 4, 5
- ext, 6
- get-nth, 3, 4, 7–12
- get-vlst, 7, 10, 11, 13
- linked-a6, 6–13
- linked-rtts-addr, 6–12
- mc-mem, 4–13
- mc-pc, 4, 5, 7, 10–13
- mc-rfile, 6–13
- mc-status, 4, 5, 7, 10–13
- mcode-addrp, 4, 5
- mem-lst, 4–6
- movem-saved, 6–13
- nat-range, 5, 6, 14
- nat-to-uint, 5, 6, 14
- ram-addrp, 4–6
- read-an, 5–13
- read-dn, 5–7, 10–14
- read-mem, 5, 7–13
- read-rn, 6–13
- read-sp, 4–7, 13
- readm-rn, 6
- rom-addrp, 4, 5
- rtts-addr, 6, 13
- slen, 5, 6
- splus, 3
- stepn, 4, 6–14
- strchr1, 3, 4, 9–12
- strpbrk, 12–14
- strpbrk*, 13, 14
- strpbrk*-strpbrk, 14
- strpbrk-code, 3–5
- strpbrk-correctness, 13
- strpbrk-induct0, 4
- strpbrk-induct1, 4
- strpbrk-non-zero, 14
- strpbrk-non-zero-la, 14
- strpbrk-s-s0, 6
- strpbrk-s-s0-else, 6
- strpbrk-s-s0-mem, 7
- strpbrk-s-s0-rfile, 6
- strpbrk-s0-s0, 11
- strpbrk-s0-s0-rfile, 12
- strpbrk-s0-s1, 7
- strpbrk-s0-s1-else, 8
- strpbrk-s0-s1-rfile, 8
- strpbrk-s0-sn, 12
- strpbrk-s0-sn-base1, 7
- strpbrk-s0-sn-base2, 11
- strpbrk-s0-sn-rfile, 13
- strpbrk-s0-sn-rfile-base1, 7
- strpbrk-s0-sn-rfile-base2, 11
- strpbrk-s0p, 5–9, 11–13
- strpbrk-s0p-info, 12
- strpbrk-s1-s0, 9
- strpbrk-s1-s0-base, 8
- strpbrk-s1-s0-rfile, 10
- strpbrk-s1-s0-rfile-base, 8
- strpbrk-s1-s1, 8
- strpbrk-s1-s1-rfile, 9
- strpbrk-s1-sn, 10
- strpbrk-s1-sn-base, 10
- strpbrk-s1-sn-rfile, 11
- strpbrk-s1-sn-rfile-base, 10
- strpbrk-s1p, 5, 7–11
- strpbrk-s1p-info, 9
- strpbrk-state, 4, 6, 7, 13, 14
- strpbrk-t, 3, 13, 14
- strpbrk-t0, 3, 9–11
- strpbrk-t1, 3, 4, 11, 12

strpbrk-t2, 3, 12, 13
sub, 4–7, 10, 13

uint-range, 5, 6, 14
uread-dn, 6