

Formal Verification of Communication Fabrics Micro-Architectures

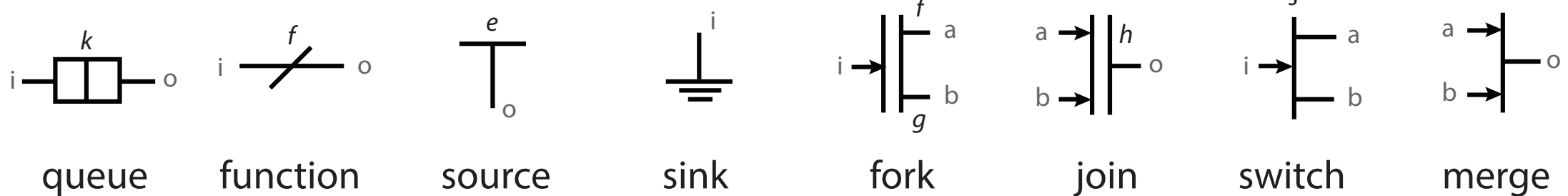
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Objective

- Develop scalable methods to verify productivity for communication fabrics
- Initially uses xMAS
 - Packet-based design language
 - Developed by industry (Intel)



- Aim is to deliver custom checkers to quickly verify *productivity*



