# Mechanized Operational Semantics

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(Lecture 2: An Operational Semantics)

#### **M1**

An M1 state consists of:

- program counter (pc)
- local variables (locals)
- push down stack (stack)
- program to run (program)

PUSH 23  $\Leftarrow pc$ LOAD 1
ADD
STORE 1

0 [17 12] pc locals

stack

program

PUSH 23
LOAD 1  $\Leftarrow pc$ ADD
STORE 1

program

1 [17 12] 23 pc locals stack PUSH 23 LOAD 1 ADD  $\Leftarrow pc$  STORE 1 ... 2 [17 12] 23 pc locals stack program

PUSH 23 LOAD 1 ADD STORE 1  $\Leftarrow pc$  .... 3 [17 12] 35 pc locals stack program

PUSH 23
LOAD 1
ADD
STORE 1
...  $\Leftarrow pc$ 4 [17 35] pc locals stack program

```
PUSH 23
LOAD 1
ADD
STORE 1
```

• • •

$$\begin{bmatrix} 4 & [17 & 35] \\ pc & locals & stack & program \end{bmatrix}$$

If locals[1] is the variable a, then this is the compiled code for "a = 23+a;"

### Consider g

```
(defun g (n a)
  (if (zp n)
          a
          (g (- n 1) (* n a))))
```

## The M1 Program

We use locals[0] to hold n and locals[1] to hold a.

```
; loop
    (LOAD 0)
    (IFLE 10)    ; if n<=0 go end
    (LOAD 0)
    (LOAD 1)
    (MUL)
    (STORE 1)    ; a := n*a</pre>
```

```
(LOAD 0)
    (PUSH 1)
    (SUB)
    (STORE 0) ; n := n-1
    (GOTO -10) ; go loop
; end
    (LOAD 1)
    (RETURN)))
```

#### The Plan

Formalize M1 states and other basic utilities

Formalize the semantics of each instruction

Formalize the "fetch-execute" cycle

## Formalizing M1

### Formalizing M1

```
(defun make-state (pc locals stack program)
  (list pc locals stack program))
```

### Formalizing M1

```
(defun opcode (inst) (car inst))
(defun arg1 (inst) (nth 1 inst))
(defun arg2 (inst) (nth 2 inst))

(opcode '(PUSH 23)) \Rightarrow PUSH
(arg1 '(PUSH 23)) \Rightarrow 23
```

```
(defun push (x stk) (cons x stk))
(defun top (stk) (car stk))
(defun pop (stk) (cdr stk))

(push 3 '(2 1)) \Rightarrow (3 2 1)
(top '(3 2 1)) \Rightarrow 3
(pop '(3 2 1)) \Rightarrow (2 1)
```

## Aside We might:

```
(defthm top-push
  (equal (top (push e stk)) e))
(defthm pop-push
  (equal (pop (push e stk)) stk))
(in-theory (disable top pop push))
to hide the representation of stacks.
```

```
(defun do-inst (inst s)
 (if (equal (opcode inst) 'PUSH)
      (execute-PUSH inst s)
   (if (equal (opcode inst) 'LOAD)
        (execute-LOAD inst s)
      (if (equal (opcode inst) 'STORE)
          (execute-STORE inst s)
        (if (equal (opcode inst) 'ADD)
            (execute-ADD inst s)
```

```
(defun do-inst (inst s)
 (if (equal (opcode inst) 'PUSH)
      (execute-PUSH inst s)
   (if (equal (opcode inst) 'LOAD)
        (execute-LOAD inst s)
      (if (equal (opcode inst) 'STORE)
          (execute-STORE inst s)
        (if (equal (opcode inst) 'ADD)
            (execute-ADD inst s)
```

#### Aside: HOL

If we had a higher order logic:

- instruction: state → state
- do-inst: apply

```
(defun do-inst (inst s)
 (if (equal (opcode inst) 'PUSH)
      (execute-PUSH inst s)
   (if (equal (opcode inst) 'LOAD)
        (execute-LOAD inst s)
      (if (equal (opcode inst) 'STORE)
          (execute-STORE inst s)
        (if (equal (opcode inst) 'ADD)
            (execute-ADD inst s)
```

```
(defun next-inst (s)
     (nth (pc s) (program s)))
(defun step (s)
     (do-inst (next-inst s) s))
```

```
(defun run (sched s)
  (if (endp sched)
      s
      (run (cdr sched) (step s))))
```

Sched is a "schedule" telling us how many steps to take.

Only its length matters.

#### **Aside**

In more sophisticated models, sched is a list of "thread identifiers" and tells us which thread to step next.

## **Terminating Computations**

When is a state halted?

```
(defun haltedp (s)
  (equal s (step s)))
```

# Recall Program g

```
(defconst *g*
 '((PUSH 1); 0
   (STORE 1); 1 a := 1
   (LOAD 0) ; 2 loop
   (IFLE 10); 3 if n<=0 go end
   (LOAD 0) ; 4
   (LOAD 1) ; 5
   (MUL) ; 6
   (STORE 1); 7 a := n*a
   (LOAD 0) ; 8
   ...))
```

How long does it take to run g?

Let's construct a schedule for g.

More precisely, let's write a function that takes g's input n and returns a schedule to run g on n.

```
'((PUSH 1); 0
 (STORE 1); 1 a := 1
 (LOAD 0) ; 2 loop
 (IFLE 10); 3 if n<=0 go end
 (LOAD 0) ; 4
 (LOAD 1)
           ; 5
 (MUL)
           ; 6
 (STORE 1); 7 a := n*a
 (LOAD 0) ; 8
 (PUSH 1) ; 9
 (SUB) ; 10
 (STORE 0); 11 n := n-1
 (GOTO -10); 12 go loop
 (LOAD 1) ; 13 end
 (RETURN))); 14 return a
```

```
'((PUSH 1); 0
 (STORE 1); 1 a := 1
 (LOAD 0) ; 2 loop
 (IFLE 10); 3 if n \le 0 go end
 (LOAD 0) ; 4
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 (LOAD 1) ; 13 end
 (RETURN))); 14 return a
```

### A Schedule for g

```
(defun g-sched (n)
  (append (repeat 0 2)
          (g-sched-loop n)))
(defun g-sched-loop (n)
  (if (zp n)
      (repeat 0 4)
    (append (repeat 0 11)
            (g-sched-loop (- n 1)))))
```

# Running g

# Demo 1

M1 inherits a lot of power from ACL2.

We're executing about 360,000 instructions/sec on this laptop.

But how does M1 compare to the JVM?

# **Operation**

Load int from local variable

Format (2 bytes)

ILOAD index

### **Form**

21 (0x15)

# **Operand Stack**

### Description

The *index* is an unsigned byte that must be an index into the local variable array of the current frame. The local variable at *index* must contain an int. The value of the local variable at *index* is pushed onto the operand stack.

### **Operation**

Load int from local variable

Format (2 bytes)

ILOAD index

#### **Form**

21 (0×15)

### **Operand Stack**

```
ILOAD
```

typed!

### **Operation**

Load int from local variable

Format (2 bytes)

ILOAD index

### **Form**

21 (0×15)

### **Operand Stack**

**Operation** 

32-bit arithmetic!

Load int from local variable

Format (2 bytes)

ILOAD index

**Form** 

 $21 (0 \times 15)$ 

**Operand Stack** 

### **Operation**

Load int from local variable

Format (2 bytes) instruction stream

ILOAD index is unparsed bytes

#### **Form**

21 (0x15)

### **Operand Stack**

### Description threads and method calls!

The *index* is an unsigned byte that must be an index into the local variable array of the current frame. The local variable at *index* must contain an int. The value of the local variable at *index* is pushed onto the operand stack.

### Comparison with the JVM

- specification style is very similar
- functionality is similar
- M1 is missing procedure call (activation stack), objects (heap), and threads (thread table)

It is possible to "grow" M1 into a complete

JVM. But we don't have time to deal with them here!

### A High Level Language

It is easy to write a compiler from a simple language of while and assignments to M1 code.

# Demo 2

To see the implementation of the compiler, read the preliminary material prepared for this Summer School.

### **Conclusion**

Two advantages of operational semantics:

- easy to relate to implementation or an informal specification
- executable

ACL2 "customers" *really like* the ability to run their models.

### **Next Time**

But can we prove anything about a model like this?