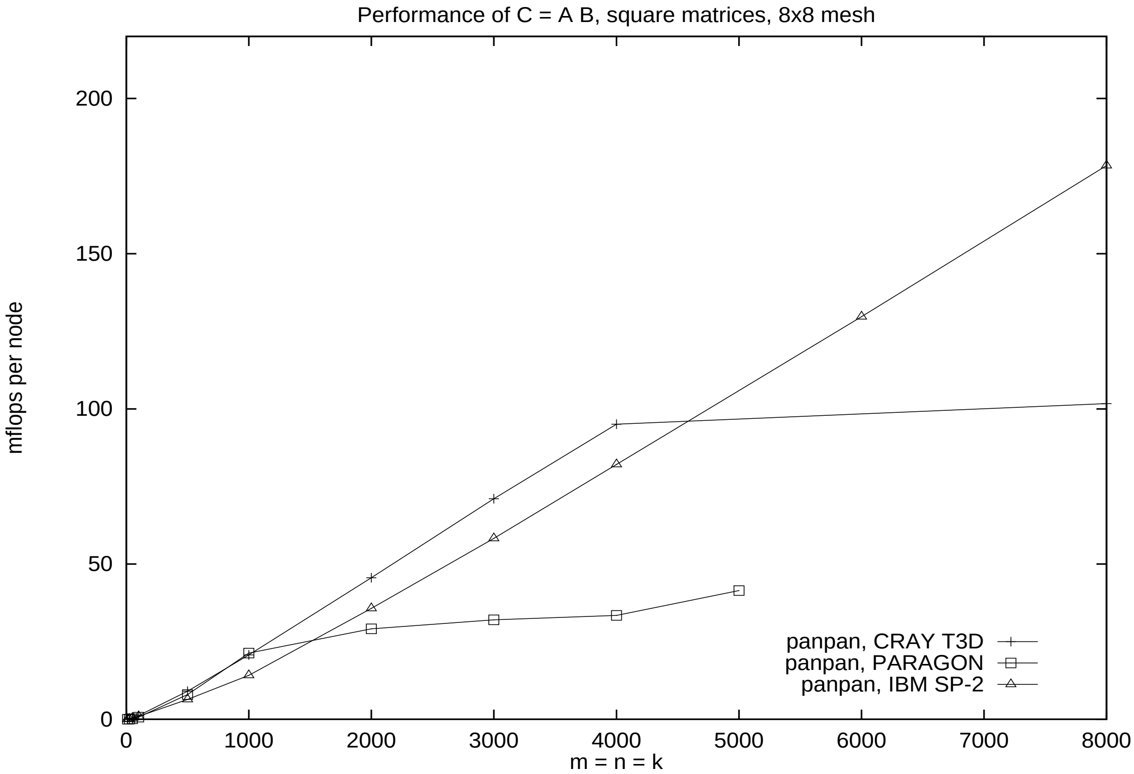

Performance of $C = \alpha AB + \beta C$ on 64 processor configurations of the Intel Paragon with GP nodes, the Cray T3D, and the IBM SP-2. The blocking sizes on the Paragon are $nb_{\mathrm{alg}} = nb = 20$ and on the T3D and SP-2 are $nb_{\mathrm{alg}} = nb = 64$.