

Generalized and Efficient Array Decision Procedures

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Leonardo de Moura and Nikolaj Bjørner
Microsoft Research

Symbolic Reasoning

Verification/Analysis tools
need some form of
Symbolic Reasoning

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need some form of
Symbolic Reasoning

Many Flavors:

SAT Solvers

SMT Solvers

First-order Theorem Provers

Computer Algebra Systems

Satisfiability Modulo Theories (SMT)

Is formula F satisfiable
modulo theory T ?

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Is formula ***F*** satisfiable
modulo theory ***T*** ?

Arithmetic,
Bit-vectors,
Arrays,
Inductive data-types,
....

Satisfiability Modulo Theories (SMT)

Example:

1 > 2

Satisfiable if the symbols **1, 2** and **>** are **uninterpreted**.

$$|M| = \{ \bullet \}$$

$$M(1) = M(2) = \bullet$$

$$M(>) = \{ (\bullet, \bullet) \}$$

Unsatisfiable modulo the **theory arithmetic**

Satisfiability Modulo Theories (SMT)

$b + 2 = c$ and $f(\text{select}(\text{store}(a, b, 3), c-2)) \neq f(c-b+1)$

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Arithmetic

Satisfiability Modulo Theories (SMT)

$b + 2 = c$ and $f(\text{select}(\text{store}(a, b, 3), c-2) \neq f(c-b+1)$

Array Theory

Satisfiability Modulo Theories (SMT)

$b + 2 = c$ and $f(\text{select}(\text{store}(a, b, 3), c-2)) \neq f(c-b+1)$

Uninterpreted
Functions

Applications

Test case generation

Verifying Compilers

Predicate Abstraction

Invariant Generation

Type Checking

Model Based Testing

Some Applications @ Microsoft



HAVOC



Hyper-V

Microsoft | Virtualization 

Terminator T-2

VCC



NModel

Vigilante

SpecExplorer



F7

SAGE

Prefix

What is a Theory?

A theory T is a set of first-order sentences.

F is satisfiable modulo T

iff

$T \cup F$ is satisfiable.

Array Theory

$\forall a, i, v. \text{select}(\text{store}(a, i, v), i) = v$

$\forall a, i, j, v: i = j \vee \text{select}(\text{store}(a, i, v), j) = \text{select}(a, j)$

Array Theory

$$\forall a, i, v. \text{select}(\text{store}(a, i, v), i) = v$$

$$\forall a, i, j, v: i = j \vee \text{select}(\text{store}(a, i, v), j) = \text{select}(a, j)$$

We say *store* is a
combinator.

Array Theory: a more familiar notation

$$\forall a, i, v. \text{select}(\text{store}(a, i, v), i) = v$$

$$\forall a, i, j, v: i = j \vee \text{select}(\text{store}(a, i, v), j) = \text{select}(a, j)$$



$$\forall a, i, v. \text{store}(a, i, v)[i] = v$$

$$\forall a, i, j, v: i = j \vee \text{store}(a, i, v)[j] = a[j]$$

Why array theory is useful?

It is used to model the memory
in
Hardware/Software verification/analysis tools

Extentional Array Theory

$$\forall a, b: (\forall i: a[i] = b[i]) \Rightarrow a = b$$

Arrays are actually “maps”

We have arrays from T_1 to T_2

T_1 does not need to be the Integers

Models for arrays as “finite graphs”

$a = \text{store}(b, 0, 5), b = \text{store}(c, 1, 10), c[0] = 2$

$M(a) = \{ 0 \rightarrow 5, 1 \rightarrow 10, \text{else} \rightarrow 0 \}$

$M(b) = \{ 0 \rightarrow 2, 1 \rightarrow 10, \text{else} \rightarrow 0 \}$

$M(c) = \{ 0 \rightarrow 2, \text{else} \rightarrow 0 \}$

A “Timeline” (Related Work)

1962 - McCarthy proposes the Basic Array Theory.

1968 - Kaplan solves the satisfiability problem.

1981 - Nelson propose a simple procedure based on (lazy) **instantiation** (PhD thesis).

2001 - Stump, Barrett, Dill and Levitt propose a procedure for extentional arrays.

2005 - **Lazy instantiation** is used in Yices (it wins all array divisions in SMT-COMP from 2005 - 2007).

2005 - Kapur and Zarba propose the reduction approach (many array-like theories are described).

2006 - Bradley, Manna and Sipma propose a procedure for a rich decidable array fragment.

A “Timeline” (Related Work)

2008 - Goel, Krstic and Fuchs formalize the lazy instantiation approach.

2008 - Bofill, Nieuwenhuis, Oliveras, Rodriguez-Carbonell and Rubio propose the store-reduction approach

“Model-Based” approaches:

2007 - Ganesh and Dill, “a decision procedure for bitvectors and arrays”, CAV’07

2008 - Brummayer and Biere, “lemmas on demand for the extentional theory of arrays”, SMT’08

A “Timeline” (Related Work)

“Rewrite-Based” approaches:

2002 - Lynch and Morawska, “Automatic Decidability”, LICS

2005 - Armando, Bonacina, Ranise and Schulz propose the rewrite based approach.

Arrays in hardware verification:

1994 - Burch and Dill, “Automatic Verification of pipelined microprocessor control”, CAV

2006 - Manolios, Srinivasan, Vroon, “Automatic memory reductions for RTL model verification”, ICCAD

More relevant work can be found in our paper...

Naïve instantiation

Recipe: Given a formula F

- 1) Collect all array terms in F
- 2) Collect all indices in F
- 3) Instantiate array axioms using 1 and 2

$$F' = F \cup \text{Instances}$$

- 4) Execute EUF solver on F'

Array theory is a **local theory extension**.

Naïve instantiation: Example

$a = \text{store}(b, i, v), a[j] \neq v, c[k] = v, i = j$

array terms: $a, b, \text{store}(b, i, v), c$

indices: i, j, k

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array terms: $a, b, \text{store}(b, i, v), c$

indices: i, j, k

Instances:

$\text{store}(a, i, v)[i] = v, \text{store}(a, j, v)[j] = v, \dots$

$i = j \vee \text{store}(a, i, v)[j] = a[i], \dots$

Problem: **Many useless instances!**

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$a = \text{store}(b, i, v), a[j] \neq v, c[k] = v, i = j$

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Lazy instantiation: select a small subset of instances.
(more later)

Problem: **Many useless instances!**

Our contributions

A generalization of the Array theory

CAL: Combinatory Array Logic

New filters for minimizing the number of instances

A simple architecture for non-stably infinite theories

We want arrays of bit-vectors.

CAL: Combinatory Array Logic

$$\forall v, i: K(v)[i] = v$$

$$\forall a_1, \dots, a_n, i: \mathit{map}_f(a_1, \dots, a_n)[i] = f(a_1[i], \dots, a_n[i])$$

CAL: Combinatory Array Logic

Suggested by Stump, Barrett, Dill, Levitt
Their procedure works for
infinite-domain satisfiability.

$$\forall v, i: K(v)[i] = v$$

$$\forall a_1, \dots, a_n, i: \mathit{map}_f(a_1, \dots, a_n)[i] = f(a_1[i], \dots, a_n[i])$$

CAL: Combinatory Array Logic

$$\forall v, i: K(v)[i] = v$$

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“Family” of combinators.
We can instantiate it with any f .

map_f is the pointwise function application

$$map_f(\begin{array}{|c|c|c|c|c|c|c|} \hline \dots & v_1 & v_2 & v_3 & v_4 & v_5 & \dots \\ \hline \end{array} , \begin{array}{|c|c|c|c|c|c|c|} \hline \dots & w_1 & w_2 & w_3 & w_4 & w_5 & \dots \\ \hline \end{array})$$

=

$$\begin{array}{|c|c|c|c|c|c|c|} \hline \dots & f(v_1, w_1) & f(v_2, w_2) & f(v_3, w_3) & f(v_4, w_4) & f(v_5, w_5) & \dots \\ \hline \end{array}$$

CAL is powerful: Sets as arrays

Set of T as an Array from T to Boolean

$$\begin{aligned}\emptyset &\equiv K(\text{false}) \\ \{a\} &\equiv \text{store}(\emptyset, a, \text{true}) \\ a \in S &\equiv S[a] \\ S_1 \cup S_2 &\equiv \text{map}_{\vee}(S_1, S_2) \\ S_1 \cap S_2 &\equiv \text{map}_{\wedge}(S_1, S_2)\end{aligned}$$

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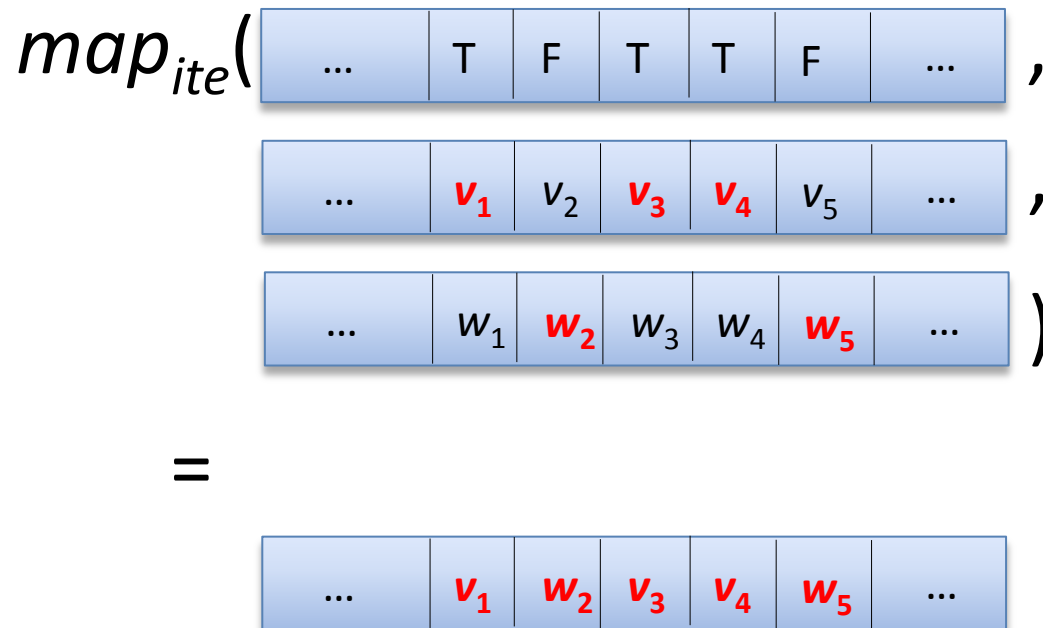
But not cardinality $|S|$, power-set, ...

CAL is powerful: Bags as arrays

Bag of T as an Array from T to Integer

$$\begin{aligned}\emptyset &\equiv K(0) \\ \{a\} &\equiv \text{store}(\emptyset, a, 1) \\ \text{mult}(a, B) &\equiv B[a] \\ B_1 \oplus B_2 &\equiv \text{map}_+(B_1, B_2) \\ B_1 \prod B_2 &\equiv \text{map}_{\min}(B_1, B_2)\end{aligned}$$

CAL is powerful: a multiplexer



Core solver

Support for equality and uninterpreted functions (EUF)

Set of strongly disjoint theories (more later)

Clauses and literals

Boolean terms

$a \equiv t$ – a is a name for the term t

$a: \sigma$ – a has sort σ

$a \sim b$ – a and b are equal in the current context

$$\frac{w_1 \equiv f(v_1, \dots, v_n), w_2 \equiv f(v'_1, \dots, v'_n), v_1 \sim v'_1, \dots, v_n \sim v'_n}{w_1 \simeq w_2}$$

Array Saturation Rules

(this is not new)

$$\begin{array}{c} \text{idx} \frac{a \equiv \text{store}(b, i, v)}{a[i] \simeq v} \\ \Downarrow \frac{a \equiv \text{store}(b, i, v), \quad w \equiv a'[j], \quad a \sim a'}{i \simeq j \vee a[j] \simeq b[j]} \\ \Uparrow \frac{a \equiv \text{store}(b, i, v), \quad w \equiv b'[j], \quad b \sim b'}{i \simeq j \vee a[j] \simeq b[j]} \\ \text{ext} \frac{a: (\sigma \Rightarrow \tau), \quad b: (\sigma \Rightarrow \tau)}{a \simeq b \vee a[k_{a,b}] \not\simeq b[k_{a,b}]} \end{array}$$

$a \sim b$ – a and b are equal in the current context

$a \equiv t$ – a is a name for the term t

$a: (\sigma \Rightarrow \tau)$ – a is an array from σ to τ

Bottlenecks

$$\text{ext} \frac{a: (\sigma \Rightarrow \tau), \quad b: (\sigma \Rightarrow \tau)}{a \simeq b \vee a[k_{a,b}] \not\simeq b[k_{a,b}]}$$

Extensionality is applied to every pair of array constants.

$$\uparrow \frac{a \equiv \text{store}(b, i, v), \quad w \equiv b'[j], \quad b \sim b'}{i \simeq j \vee a[j] \simeq b[j]}$$

Upwards propagation distributes index over all modifications of same array.

Bottlenecks: simple “tricks”

$$\text{ext} \frac{a: (\sigma \Rightarrow \tau), \quad b: (\sigma \Rightarrow \tau)}{a \simeq b \vee a[k_{a,b}] \not\simeq b[k_{a,b}]}$$

Extensionality is applied to every pair of array constants.

Delay the application of ext and \uparrow .

Only works for unsatisfiable instances.

$$\uparrow \frac{a \equiv \text{store}(b, i, v), \quad w \equiv b'[j], \quad b \sim b'}{i \simeq j \vee a[j] \simeq b[j]}$$

Upwards propagation distributes index over all modifications of same array.

Bottlenecks: simple “tricks”

Ignore “congruent” axiom instances

$$i \simeq j \vee a[j] \simeq b[j]$$

$$i' \simeq j' \vee a'[j'] \simeq b'[j']$$

$$a \sim a', b \sim b', i \sim i', \text{ and } j \sim j'$$

Bottlenecks

$$\text{ext} \frac{a: (\sigma \Rightarrow \tau), \quad b: (\sigma \Rightarrow \tau)}{a \simeq b \vee a[k_{a,b}] \not\approx b[k_{a,b}]}$$

Extensionality is applied to every pair of array constants.

$$\text{ext}_{\neq} \frac{p \equiv a \simeq b, \quad \Gamma(p) = \text{false}}{a \simeq b \vee a[k_{a,b}] \not\approx b[k_{a,b}]}$$

$$\text{ext}_r \frac{a: (\sigma \Rightarrow \tau), \quad b: (\sigma \Rightarrow \tau), \quad \{a, b\} \subseteq \text{foreign}}{a \simeq b \vee a[k_{a,b}] \not\approx b[k_{a,b}]}$$

Restrict to constants asserted to be different or foreign.

We say a is foreign if there is b s.t. $a \sim b$ and

b is the argument of an uninterpreted function symbol. Microsoft **Research**

Why do we need ext_r ?

$$\text{ext}_{\neq} \frac{p \equiv a \simeq b, \quad \Gamma(p) = \text{false}}{a \simeq b \vee a[k_{a,b}] \not\approx b[k_{a,b}]}$$

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Example:

$$a = \text{store}(b, i, v), \quad b[i] = v, \quad f(a) \neq f(b)$$

Another optimization...

We do not need to add the extensionality axiom for (a,b) if they are already known to be disequal.

Definition 9 (Already Disequal) Given a state Γ , $(a,b) \in$ already-diseq iff there are two definitions $v_1 \equiv a_1[i_1]$ and $v_2 \equiv a_2[i_2]$ in Γ such that $v_1 \not\sim v_2$, $a \sim a_1$, $b \sim b_1$, and $i_1 \sim i_2$.

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Typo in the paper!
Should be b_1

Why is $\uparrow\uparrow$ expensive?

$$\uparrow\uparrow \frac{a \equiv \text{store}(b, i, v), \quad w \equiv b'[j], \quad b \sim b'}{i \simeq j \vee a[j] \simeq b[j]}$$

Scenario from software verification

Bunch of facts about the initial state of the heap

$$a_0[i_0] = v_0, \quad a_0[i_1] = v_1, \quad a_0[i_2] = v_2, \quad \dots$$

Perform a series of updates

$$a_1 = \text{store}(a_0, j_1, w_1), \quad a_2 = \text{store}(a_1, j_2, w_2), \quad \dots$$

Check some property on the final heap

$$a_n[k] \neq v$$

Why do we need $\uparrow\uparrow$?

$$\text{store}(a, i, v_1) = \text{store}(b, i, v_2), i \neq k, a[k] \neq b[k]$$

Definition 10 (Linearity) Given a state Γ , the set non-linear of *non-linear constants* is the least set such that:

1. $a_1 \equiv \text{store}(b_1, i_1, v_1)$, $a_2 \equiv \text{store}(b_2, i_2, v_2)$, a_1 is not a_2 and $a_1 \sim a_2$ implies $\{a_1, a_2\} \subseteq \text{non-linear}$,
2. $a \equiv \text{store}(b, i, v)$ and $a \in \text{non-linear}$ implies $b \in \text{non-linear}$,
3. $a \in \text{non-linear}$ and $a \sim b$ implies $b \in \text{non-linear}$.

We say a is *linear* if $a \notin \text{non-linear}$.

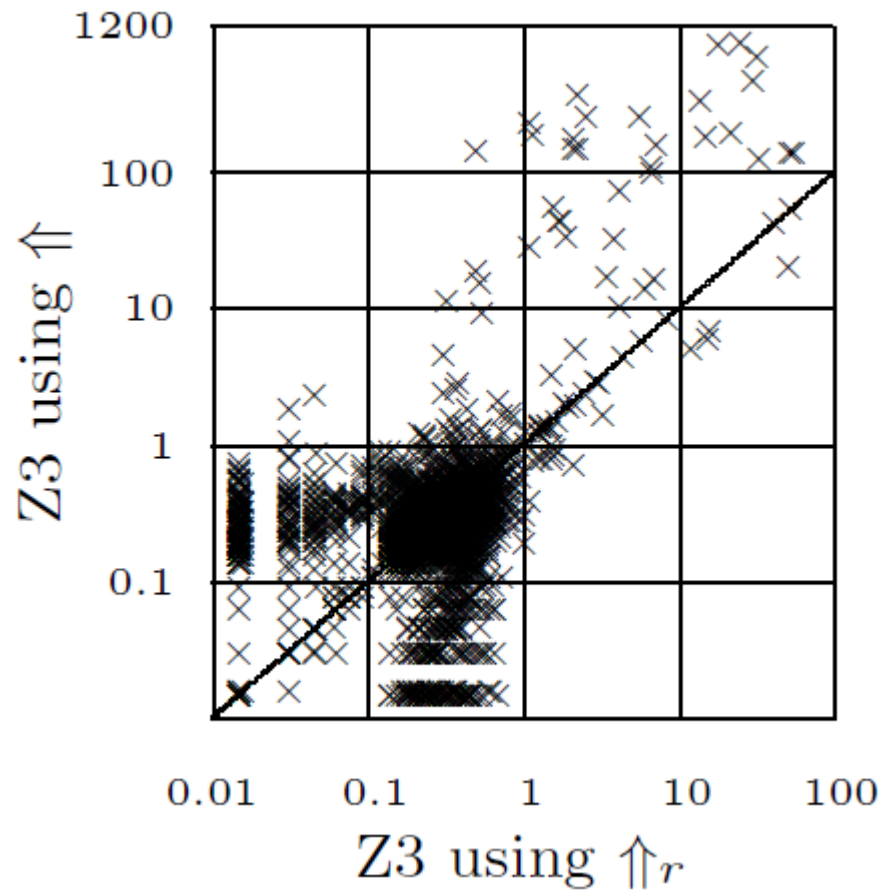
Restricting \Uparrow

$$\Uparrow \frac{a \equiv \text{store}(b, i, v), \quad w \equiv b'[j], \quad b \sim b'}{i \simeq j \vee a[j] \simeq b[j]}$$



$$\Uparrow_r \frac{a \equiv \text{store}(b, i, v), \quad w \equiv b'[j], \quad b \sim b', \quad \boxed{b \in \text{non-linear}}}{i \simeq j \vee a[j] \simeq b[j]}$$

Effect on Benchmarks



Saturating CAL

$$\text{K}\Downarrow \frac{a \equiv K(v), \quad w \equiv a'[j], \quad a \sim a'}{a[j] \simeq v}$$

$$\text{map}\Downarrow \frac{a \equiv \text{map}_f(b_1, \dots, b_n), \quad w \equiv a'[j], \quad a \sim a'}{a[j] \simeq f(b_1[j], \dots, b_n[j])}$$

$$\text{map}\Uparrow \frac{\begin{array}{l} a \equiv \text{map}_f(b_1, \dots, b_n), \quad w \equiv b'_k[j], \\ b_k \sim b'_k, \text{ for some } k \in \{1, \dots, n\} \end{array}}{a[j] \simeq f(b_1[j], \dots, b_n[j])}$$

$$\epsilon_{\neq} \frac{v \equiv a[i], \quad i:\sigma, \quad i \text{ is not } \epsilon_{\sigma}}{\epsilon_{\sigma} \neq i} \quad \epsilon_{\delta} \frac{a: (\sigma \Rightarrow \tau)}{a[\epsilon_{\sigma}] \simeq \delta_a}$$

Saturating CAL

$$\epsilon_{\neq} \frac{v \equiv a[i], \quad i:\sigma, \quad i \text{ is not } \epsilon_{\sigma}}{\epsilon_{\sigma} \neq i}$$

Potentially unsound
if F only has models
 M where $M(\sigma)$ is
finite.

$$\text{blast} \frac{a: (\sigma \Rightarrow \tau), \quad \text{size}(\sigma) = k}{a[\sigma_1] \simeq \delta_{a,1}, \quad \dots, \quad a[\sigma_k] \simeq \delta_{a,k}}$$

Saturating CAL

We also have a restricted version of $\text{map}\hat{\uparrow}$ using linear **stratification** (see paper for details).

$$a \simeq \text{map}_{ite}(a, b, c) \wedge b[j] \simeq \perp \wedge c[j] \simeq \top$$

Default-value extension (new theory symbol δ), and alternative for ϵ_{\neq} and ϵ_{δ}

$$\text{U}\delta \frac{a \equiv \text{store}(b, i, v)}{\delta(a) \simeq \delta(b)} \quad \text{K}\delta \frac{a \equiv K(v)}{\delta(a) \simeq v}$$

$$\text{map}\delta \frac{a \equiv \text{map}_f(b_1, \dots, b_n)}{\delta(a) \simeq f(\delta(b_1), \dots, \delta(b_n))}$$

Theory combination in Z3

Efficient Core

Strongly disjoint theories + Uninterpreted functions

Strongly disjoint theory \equiv Sort disjoint

Examples: Arithmetic, Bitvectors and Booleans

$$f(\top) \simeq w \wedge f(\perp) \simeq w \wedge f(v) \not\simeq w$$

All other theories are reduced to this core.

Not covered today: inductive datatypes.

Conclusion

Arrays are useful in practice.

They are used in many verification tools at Microsoft.

CAL is a useful extension of the array theory.

Simple combination architecture.

Efficient and easy to implement.

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Thank You!