

Preprocessing for first-order clausification

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University of Manchester

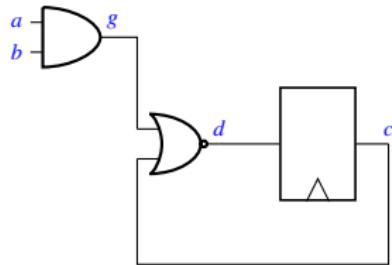
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Outline

- EPR-based BMC
- EPR-preserving clausification
- Definition inlining
- Equivalence discovery using SAT sweeping
- Quantified AIGs (QAIGs) and QBDDs
- QAIGs + QBDDs
- Evaluation

Bounded model checking



Symbolic representation:

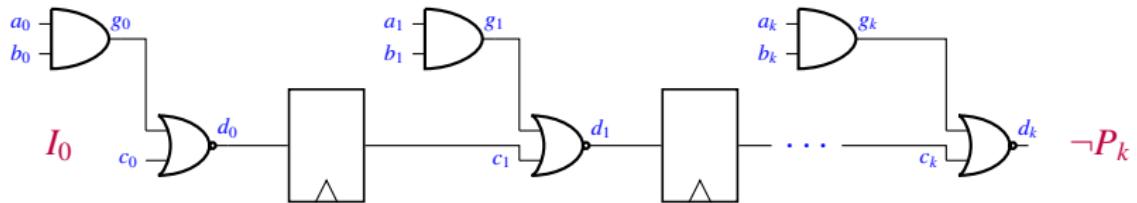
$$I = (a_0 \leftrightarrow \neg c_0) \wedge (c_0 \rightarrow b_0) \\ (g_0 \leftrightarrow a_0 \wedge b_0) \wedge (d_0 \leftrightarrow \neg g_0 \wedge \neg c_0)$$

$$T =$$

$$\begin{aligned} a' \leftrightarrow a &\quad \wedge \\ b' \leftrightarrow b &\quad \wedge \\ g' \leftrightarrow a' \wedge b' &\quad \wedge \\ c' \leftrightarrow d &\quad \wedge \\ d' \leftrightarrow \neg c' \wedge \neg g' &\quad \end{aligned}$$

$$P = (d \leftrightarrow \neg g)$$

Bounded model checking (Unrolling)



The system is **unsafe** if and only if

$$I_0 \wedge T_{<1,2>} \wedge \dots \wedge T_{} \wedge \neg P_k$$

is **satisfiable** for some k .

Biere, Cimatti, Clarke, Zhu

First-order encoding of BMC

First-order encoding:

- s_0, \dots, s_k constants denoting states at unrolling bounds
- first-order formulas $I(S), P(S), T(S, S')$
- next state predicate $\text{Next}(S, S')$

BMC can be encoded

$$I(s_0); \neg P(s_k); \quad \text{initial and final states}$$

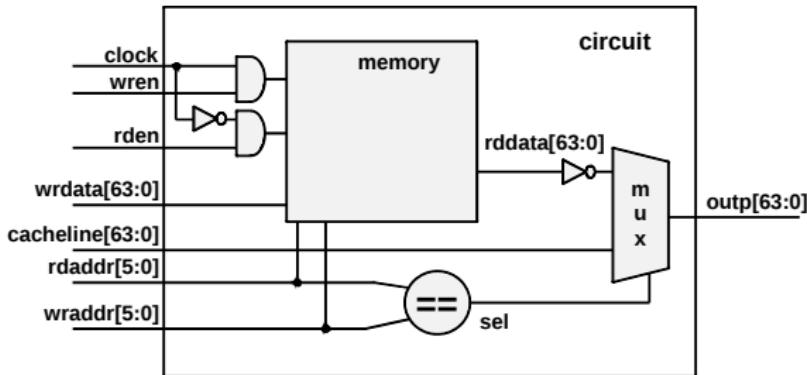
$$\forall S, S' (\text{Next}(S, S') \rightarrow T(S, S')); \quad \text{transition relation}$$

$$\text{Next}(s_0, s_1); \text{Next}(s_1, s_2); \dots \text{Next}(s_{k-1}, s_k); \quad \text{next state relation}$$

- FOL encoding provides succinct representation
- Reasoning can be done at higher level

Navarro-Perez, Voronkov

Word level


$$\begin{aligned} \forall s, s' (Next(s, s') \rightarrow & \quad // \text{ write is enabled} \\ \forall y (\text{Assoc}_{\text{wraddr}}(s', y) \rightarrow & \\ \forall A (\text{clock}(s') \wedge \text{wren}(s') \wedge A = y \rightarrow & \\ \forall B (\text{range}_{[0,63]}(B) \rightarrow (\text{mem}(s', A, B) \leftrightarrow \text{wrdata}(s, B)))))). \end{aligned}$$

Memories and bit-vectors are represented by first-order predicates:
 $\text{mem}(S, A, B)$, $\text{wrdata}(S, B)$.

Emmer, Korovin, Khasidashvili, Sticksel, Voronkov;

relevant complexity results: Kovasznai, Frohlich, Biere

Effectively Propositional Fragment (EPR)

EPR: $\exists \bar{y} \forall \bar{x} F(\bar{x}, \bar{y})$, where F is function-free.

Skolemized: $\forall \bar{x} F(c, \bar{x})$

EPR

$$\forall S, B (\text{mem}(S, \text{rdaddr}, B) \leftrightarrow \text{rddata}(S, B))$$

represents propositional:

$$\begin{aligned} & (\text{mem}(s_1, \text{rdaddr}, i_1) \leftrightarrow \text{rddata}(s_1, i_1)) \\ & (\text{mem}(s_1, \text{rdaddr}, i_2) \leftrightarrow \text{rddata}(s_1, i_2)) \\ & (\text{mem}(s_2, \text{rdaddr}, i_3) \leftrightarrow \text{rddata}(s_2, i_3)) \\ & \dots \end{aligned}$$

Efficient solvers for EPR: iProver, Darwin, Equinox

CASC competition: EPR division

EPR-preserving Skolemization

Most formulas obtained from hardware designs are **definitions**.

Skolemization of definitions can introduce **functions**!

$$\text{prop}(s) \leftrightarrow \forall B(\text{range}_{[0:63]}(B) \rightarrow (\text{outp}(s, B) \leftrightarrow \text{cacheline}(s, B)))$$

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Inline all negative occurrences of **prop**:

$$\neg \text{prop}(s_k)$$

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$$\exists B(\text{range}_{[0:63]}(B) \wedge \neg(\text{outp}(s_K, B) \leftrightarrow \text{cacheline}(s_k, B)))$$
$$(\text{range}_{[0:63]}(\text{sk}) \wedge \neg(\text{outp}(s_K, \text{sk}) \leftrightarrow \text{cacheline}(s_k, \text{sk})))$$

After inlining:

- 1) obtain an **EPR** formula and
- 2) we can **remove** \leftarrow from the definition.

Conditional Inlining

$$\forall S (\text{reach}(S) \rightarrow \forall B (\text{range}_{[0:63]}(B) \rightarrow (\text{bv}_1(S, B) \leftrightarrow \text{mem}_1(S, \text{wraddr}, B)))).$$
$$\forall S (\text{reach}(S) \rightarrow \forall B (\text{range}_{[0:63]}(B) \rightarrow (\text{bv}_1(S, B) \vee (\neg \text{mem}_2(S, \text{rdaddr}, B) \wedge \text{bv}_2(S, B))))).$$

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(Conditional) inlining:

- EPR - preserving Skolemization
- definition elimination
- non-growing inlining

Equivalence discovery with SAT sweeping

Given a first-order formula F which literals in F are equivalent?

SAT sweeping:

- clauseify F
- abstract first-order literals to propositional variables
- use SAT-based techniques for equivalence checking

First-order F :

$$\begin{array}{l} p(x) \vee \neg q(x, c) \vee \neg r(x) \quad \wedge \\ \qquad q(x, c) \vee r(x) \quad \wedge \\ \qquad q(x, c) \vee \neg r(x) \quad \wedge \\ \neg p(x) \vee \neg q(x, c) \vee r(x) \end{array}$$

Propositional abstraction S :

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If $S \models v_{p(x)} \leftrightarrow v_{r(x)}$ then $F \models \forall x (p(x) \leftrightarrow r(x))$.

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Eliminate equivalent literals

Extension: equivalences of subformulas

(simultaneous prop. equivalence checking: Khasidashvili, Nadel)

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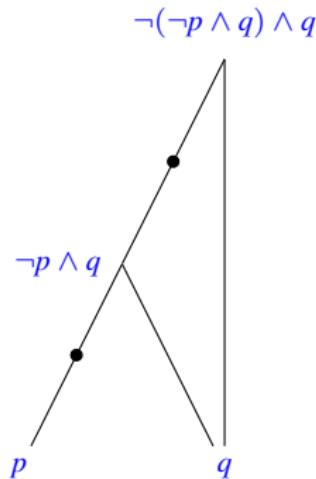
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And-Inverter Graphs (AIG)

And-Inverter graphs for propositional formula representation.

Only two nodes: \wedge and \neg



AIGs:

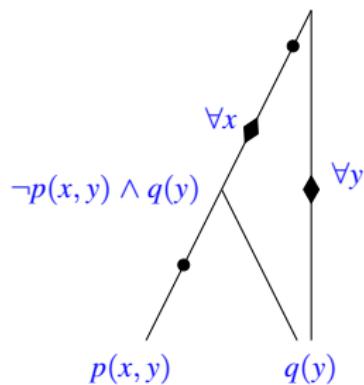
- compact representation
- sharing structurally isomorphic subformulas
- simplifications

Quantified AIGs

Quantified AIGs for first-order formula representation.

Three nodes: \wedge , \neg and \forall

$$\neg \forall x(p(x, y) \wedge \neg q(y)) \wedge \forall y q(y)$$



QAIGs:

- compact representation of FO formulas
- definition inlining on QAIGs
- variable instantiations

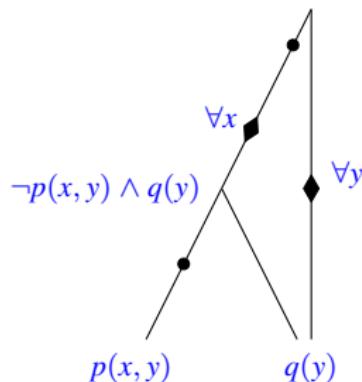
Shortcomings: (Q)AIGs are not canonical even for simple propositional cases.

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Quantified BDDs

Main idea: abstract quantified formulas by propositional variables.

$$\begin{aligned} \text{atb}(\text{atom}(a)) &= \text{bdd}_{\text{var}}(\text{atom}(a)) \\ \text{atb}((\forall x, q)) &= \text{bdd}_{\text{var}}((\forall x, \text{atb}(q))) \\ \text{atb}(\text{neg}(q)) &= \text{bdd}_{\text{neg}}(\text{atb}(q)) \\ \text{atb}(\text{and}(q_1, q_2)) &= \text{bdd}_{\text{and}}(\text{atb}(q_1), \text{atb}(q_2)) \end{aligned}$$

Example:

$$\forall x[(p(x, y) \vee \neg \exists x q(x)) \wedge (\exists x q(x) \vee p(x, y)) \wedge q(y)] \wedge \neg \forall x(p(x, y) \wedge q(y))$$

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Example:

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Quantified BDDs

Main idea: abstract quantified formulas by propositional variables.

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QBDDs: Canonical wrt. Boolean structure.

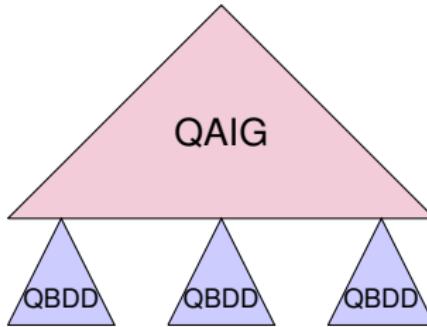
Shortcomings: BDDs can be exponential in size.

QAIGs+QBDDs

Combination QAIGs+QBDDs.

For each node in **QAIG** we try to build **QBDD**.

If QBDD is larger than a threshold then replace QBDD with **QAIG**.



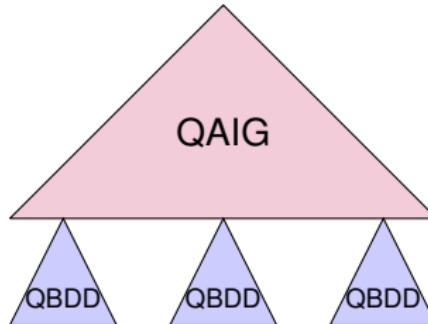
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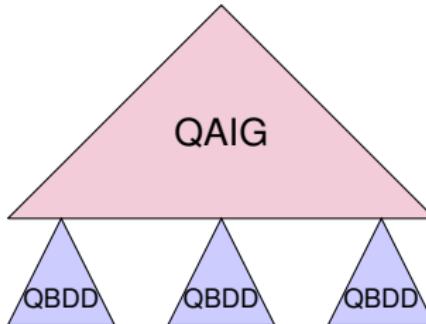
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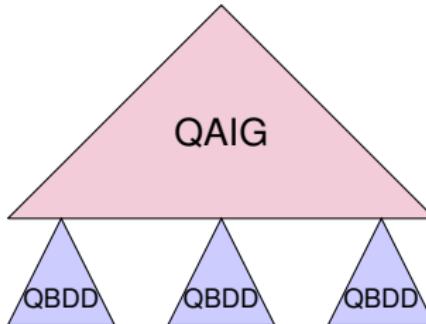
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Optimization: When

$$| \text{AIG} | < | \text{QBDD} |$$

- keep QBDD for **canonicity**;
- use AIG as the **normal form**.

Evaluation (EPR-BMC)

Intel industrial hardware benchmarks:

Design block	FOF size	Bound		CNF size	
		Base	Prep	Base	Prep
BPB2	913	7	9	1977	2921
DCC1	1093	4	4	3209	1615
DCC2	431	7	10	861	370
DCI1	4678	0	1	15899	9852
PMS1	574	5	7	1295	1016
ROB2	713	5	7	1717	1157
SCD1	736	8	9	1908	1328
SCD2	267	8	15	755	524
TOTAL	9404	44	62	27621	18783

Table : BMC1 results on industrial benchmarks.

iProver was used for the EPR solving.

Evaluation TPTP/SMT

ACR – AIGs conditional rewriting

ACR(ERI) – AIGs conditional rewriting restricted to EPR-restoring

		Base+ACR	Prep	Prep+ACR	Prep+ACR(ERI)
$\geq 2x$	faster	100	10	74	122
$> 1x$	faster	4890	1527	4847	4941
$\geq 2x$	slower	36	33	36	36

Table : QA_UF SMT problems

Z3 was used for SMT solving.

	Prep only	Base only
iProver	482	83
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Table : TPTP first-order problems

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Summary

First-order preprocessing techniques:

- Definition inlining
- Equivalence discovery using SAT sweeping
- Quantified AIGs
- Quantified BDDs
- QAIGs + QBDDs