# Exploiting Craig Interpolants in Unbounded Model Checking of Hardware Designs

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September 28th, 2015 Austin (TX), USA

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FMCAD 2015

## Outline

- Background
  - Verification of hardware designs
  - Craig Interpolants in UMC
- Contributions
  - Redundancy removal and reduction in UNSAT proofs and ITPs
    - Heuristic procedure for scalable *ITP* compaction
  - Abstraction and refinement techniques for ITPs
    - Heuristic procedure for abstracting *without resorting to resolution proofs*
- Experimental results & Conclusions

## **ITP Proof Compaction**

- Proof reduction
  - Recycle-pivots [Bar-Inal & al. HVCo8]
    - Exploiting proof topology: proof node chains



- Logic synthesis manipulations on the proof
  - Constant propagation
  - BDD-based sweeping (for equivalences)
  - Observability Don't Care (lightweight)

## **ITP Circuit Compaction**

- Proof into AIG
  - ODC (structural)
  - Logic synthesis
    - rewrite / refactor
      - using ABC tool
    - AIG balance
  - ITE-based decomposition <sup>xN</sup>
    - iff necessary



## **Interpolant Abstraction**

- ITP+: take on improved Craig's interpolation
- Incremental computation of interpolants using alternative techniques
  - Equivalence classes, mutual implications of state variables
  - Cube-based over-approximation, based on the detection of those state variables that are stuck at constant values



### ITP<sup>+</sup> - Abst. by iterative refinement



... or return Craig's ITP

 $IMG_{+Adq} (From, T, Cone^{k})$   $To + = Full_state_space$ Foreach Class  $\in$  Abstraction \_classes Select abstraction  $To_{+Class} = IMG_{+} using abstraction$   $To_{+} = To_{+} \wedge To_{+Class}$ if UNSAT(To\_{+} \wedge Cone^{k}) return To\_{+}
return (To\_{+} \wedge ITP(From  $\wedge T$ , Cone^{k}))

## ITP<sup>+</sup> - Abstraction classes

#### • *Tightening* abstraction classes

- Equivalent state variables
- Constant state variables
- SAT-based enumeration
- Loosening abstraction classes
  - Localization abstraction
  - Ternary abstraction

#### ITP<sup>+</sup> - Constant state variables



### Some experimental results...

#### Interpolant abstraction



Circuit name

Come to my poster presentation for more details and experimental results

### Thanks for your attention

### Some experimental results...

#### Interpolant compaction



## Conclusions

- ITP-based MC heavily relies on scalability
- We developed effective techniques to improve standard ITPs.
  - Scalable techniques, applied incrementally
- Best suited as a second engine
  - Hard-to-prove properties (hard for IC<sub>3</sub>)
  - Explosion of standard interpolation
  - Can afford extra time (for memory)