Determining End-to-End Delay Bounds In Heterogeneous Networks

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Abstract

We define a class of Guaranteed Rate (GR) scheduling algorithms. The GR class includes Virtual Clock, Packet-by-Packet Generalized Processor Sharing and Self Clocked Fair Queuing. For networks that employ scheduling algorithms belonging to GR, we present a method for determining an upper bound on end-to-end delay. The method facilitates determination of end-to-end delay bounds for a variety of sources. We illustrate the method by determining end-to-end delay bounds for sources conforming to Leaky Bucket and Exponentially Bounded Burstiness.

1 Introduction

Computer networks have advanced to a point where they can support multimedia applications like audio and video conferencing and multimedia information retrieval. Such applications require the network to provide a wide range of Quality of Service (QoS) guarantees (including minimum bandwidth, packet delay, delay jitter and loss). Whereas the minimum guaranteed bandwidth must be large enough to accommodate motion video of acceptable resolution, the end-to-end delay must be small enough for interactive communication. In order to avoid breaks in continuity of audio and video playback, delay jitter and loss must be sufficiently small. Techniques for determining an upper bound on end-to-end delay in a network is the subject matter of this paper.

The end-to-end delay of a packet depends on the source traffic characteristics and the scheduling algorithm at the network switches. In the recent past, several source specifications including Leaky Bucket [10], Exponentially Bounded Burstiness (EBB) [13], Flow Specification [9] and the Tenet model [5] have been studied. The scheduling algorithms that have been proposed include Stop and Go Queuing [6], Delay EDD [15], Jitter EDD [15], Hierarchical Round Robin [8], Rate Control Static Priority Queuing [14], Self Clocked Fair Queuing (SCFQ) [7], Virtual Clock [16] and Packet-by-Packet Generalized Processor Sharing (PGPS) [10]. The problem of determining an upper bound on end-to-end delay has also received considerable attention [2, 3, 9, 10, 13]. However, most of the techniques determine end-to-end delay by considering a specific source traffic specification and scheduling algorithm. In an integrated network supporting audio, video and data services, the sources have widely varying characteristics. Moreover, in a wide area networking environment, each switch may employ a different scheduling algorithm. Methods for determining end-to-end delay of packets in such heterogeneous environments have not received much attention.

In this paper, we take a step towards addressing the above limitation by (1) defining a class of *Guaranteed Rate (GR)* scheduling algorithms, and (2) developing a method for determining an upper bound on end-to-end delays for a network of switches each of which employs a scheduling algorithm in the GR class. We demonstrate that many of the scheduling algorithms proposed in the literature (e.g. Virtual Clock, PGPS and SCFQ) belong to the class of GR scheduling algorithms. We also show that the method for determining an upper bound on end-to-end delay is general and can be used to determine delay bounds for various source traffic specifications. We employ the method to derive a deterministic end-to-end delay bound for Leaky Bucket sources (a deterministic source characterization) and an upper bound on the tail distribution of the delay for EBB sources (a stochastic characterization). The end-to-end delay bounds that we derive are parameterized by the scheduling algorithm used at each switch and can be instantiated to derive delay bounds for a specific scheduling algorithm or a combination of scheduling algorithms.

The rest of the paper is organized as follows. In Section 2, we define the class of GR scheduling algorithms. In section 3, we present a method for determining end-to-end delay bounds and finally, Section 4 summarizes our results.

2 Guaranteed Rate Scheduling Algorithms

Each unit of data transmission at the network level is a packet. We refer to the sequence of packets transmitted by a source as a *flow* [16]. Each packet within a flow is serviced by a sequence of servers (switching elements) along the path from the source to the destination in the network. To provide guaranteed performance, the servers reserve a rate for a flow and employ a rate-based scheduling algorithm. Based on this rate reservation, many scheduling algorithms can guarantee a deadline by which a packet of a flow will be transmitted. This guarantee is referred to as *delay guarantee*. We refer to the class of rate based scheduling algorithms which provide such guarantees as *Guaranteed Rate (GR)* scheduling algorithms.

The delay guarantees provided by these algorithms are based on the *guaranteed rate clock*[1] values (intuitively, expected arrival time values) associated with each packet. To define the guaranteed rate clock values, consider a flow f that is associated with a rate r_f (in bits/sec). Let p_f^j and l_f^j denote the j^{th} packet of flow f and its length, respectively. Additionally, let $GRC^i(p_f^j)$ and $A^i(p_f^j)$ denote the guaranteed rate clock value and arrival time of packet p_f^j at server i, respectively. Then, guaranteed rate clock value for a packet is given by:

$$GRC^i(p_f^0) = 0 \tag{1}$$

$$GRC^i(p_f^j) = \max\{A^i(p_f^j), GRC^i(p_f^{j-1})\} + \frac{l_f^j}{r_f} \quad j \geq 1 \tag{2}$$

We use the guaranteed rate clock value of a packet to define the class of GR scheduling algorithms as follows.

Definition 1 *A scheduling algorithm at server i belongs to class GR for flow f if it guarantees that packet p_f^j will be transmitted by $GRC^i(p_f^j) + \beta^i$ where β^i is a constant which depends on the scheduling algorithm and the server.*

The concept of delay guarantee to a packet based on its expected arrival time was introduced in [9, 12]. As is evident from the definition (also observed in [9]), a key property of the class of GR scheduling algorithms is that they provide a delay guarantee for a source independent of the behavior of other sources in the network, and hence isolate the sources. Isolation of sources has been considered to be a very desirable property of scheduling algorithms, especially in large heterogeneous networks where sources may be malicious [1, 4, 9, 11]. This property is the basis for development of a conceptually simple method for determining end-to-end delay bounds in section 3.

In the next few subsections we demonstrate that the GR class includes Virtual Clock, PGPS and SCFQ with certain rate assignments.

[1] The concept of guaranteed rate clock is the same as the concept of virtual clock in [16]. We coin a new term for the virtual clock concept to avoid any confusion between the general concept of virtual clock and as it relates to the Virtual Clock scheduling algorithm.

2.1 Virtual Clock

The Virtual Clock scheduling algorithm assigns each packet a virtual clock value on its arrival and orders the transmission of packets by increasing virtual clock values. If flow f is assigned rate r_f at server i, then the virtual clock value for packet p_f^j of flow f at server i, denoted by $VC^i(p_f^j)$, is computed as follows [16]:

$$VC^i(p_f^0) = 0 \tag{3}$$

$$VC^i(p_f^j) = \max\{A^i(p_f^j), VC^i(p_f^{j-1})\} + \frac{l_f^j}{r_f} \quad j \geq 1 \tag{4}$$

The following delay guarantee was presented in [12]. Define a flow to be active at time t if $VC^i(p_f^j) \geq t$ where p_f^j is the last packet of flow f that has arrived before time t. Let $a^i(t)$ denote the set of active flows at server i at time t. Server i with capacity C^i is defined to have exceeded its capacity at time t if $\sum_{n \in a^i(t)} r_n > C^i$. Let l_{max}^i be the maximum length of the packet served by server i and $L_{VC}^i(p_f^j)$ be the time at which the transmission of packet p_f^j is completed. Then, if a server's capacity is not exceeded and flow f is assigned rate r_f,

$$L_{VC}^i(p_f^j) \leq VC^i(p_f^j) + \frac{l_{max}^i}{C^i} \tag{5}$$

Since the equations for virtual clock and guaranteed rate clock are the same, it can be easily observed that if rate r_f is assigned to flow f, then the Virtual Clock scheduling algorithm belongs to GR for flow f with $\beta^i = \frac{l_{max}^i}{C^i}$.

2.2 Packet-by-Packet Generalized Processor Sharing

The Packet-by-Packet Generalized Processor Sharing scheduling algorithm is a practical realization of Generalized Processor Sharing(GPS) service discipline [10]. In fact, PGPS simulates GPS such that

$$L_{PGPS}^i(p_f^j) \leq L_{GPS}^i(p_f^j) + \frac{l_{max}^i}{C^i} \tag{6}$$

where $L^i_{PGPS}(p^j_f)$ and $L^i_{GPS}(p^j_f)$ are the times at which packet p^j_f leaves server i employing the PGPS scheduling algorithm and the GPS service discipline, respectively, and l^i_{max} is the maximum length of the packet served by server i [10]. Hence, to show that PGPS belongs to the class of GR scheduling algorithms, we establish a relationship between $L^i_{GPS}(p^j_f)$ and $GRC^i(p^j_f)$.

In GPS, each flow f is associated with a constant ϕ^i_f at server i. GPS is defined such that if flow f is backloged at time t, it receives service at the rate of $\frac{\phi^i_f C^i}{\sum_{k\in b^i(t)}\phi^i_k}$ where C^i is the capacity of the server and $b^i(t)$ is the set of backlogged flows at GPS server i at time t. Thus, a GPS server is defined to have assigned rate r_f to flow f if $\frac{\phi^i_f C^i}{\sum_{k\in b^i(t)}\phi^i_k} \geq r_f$ whenever flow f is backlogged. Hence, if flow f has been assigned rate r_f, then

$$L^i_{GPS}(p^j_f) \leq \max\{A^i(p^j_f), L^i_{GPS}(p^{j-1}_f)\} + \frac{l^j_f}{r_f} \quad j \geq 1 \tag{7}$$

Let $L^i_{GPS}(p^0_f) = GRC^i(p^0_f) = 0$. From (7) and (2) it can be easily shown that

$$L^i_{GPS}(p^j_f) \leq GRC^i(p^j_f) \quad j \geq 1 \tag{8}$$

From (8) and (6) we get

$$L^i_{PGPS}(p^j_f) \leq GRC^i(p^j_f) + \frac{l^i_{max}}{C^i} \quad j \geq 1 \tag{9}$$

Hence if rate r_f is assigned to flow f at server i , then the PGPS scheduling algorithm belongs to GR for flow f with $\beta^i = \frac{l^i_{max}}{C^i}$.

2.3 Self Clocked Fair Queuing

The Self Clocked Fair Queuing scheme, proposed in [7], was designed to facilitate the implementation of a fair queuing scheme in broadband networks. The scheme is defined as follows.

1. On arrival, a packet p^j_f is stamped with service tag $F^i(p^j_f)$, computed as:

$$F^i(p^0_f) = 0 \tag{10}$$

$$F^i(p^j_f) = \max\{F^i(p^{j-1}_f), v^i(A^i(p^j_f)\} + \frac{l^j_f}{\phi^i_f} \quad j \geq 1 \tag{11}$$

where ϕ^i_f is a constant associated with flow f at server i.

2. The server virtual time at time t, $v^i(t)$, is defined to be equal to the service tag of the packet being service at time t. $v^i(t) = t$ when the server is idle.

3. Packets are serviced in increasing order of their service tags.

The following theorem proves that SCFQ algorithm also belongs to the class of GR scheduling algorithms. Let c^i be the set of flows serviced by server i employing SCFQ scheduling algorithm.

Theorem 1 *If $\sum_{m\in c^i} r_m \leq C^i$ and $\forall m \in c^i : \phi^i_m = r_m$, then the departure time of packet p^j_f in SCFQ (denoted by $L^i_{SCFQ}(p^j_f)$) is given by*

$$L^i_{SCFQ}(p^j_f) \leq GRC^i(p^j_f) + \sum_{m\in c^i \wedge m\neq f} \frac{l^{max}_m}{C^i} \tag{12}$$

where l^{max}_m is the maximum length for packets in flow m.

Proof: Let set B^i_f be defined as follows.

$$B^i_f = \{n | n > 0 \wedge F^i(p^{n-1}_f) < v^i(A^i(p^n_f))\}$$

Let $k \leq j$ be largest integer in B^i_f. Since packets of a flow are serviced in FCFS order, they form a queue. Let R_m be the set of packets of flow m, $m \neq f$, served in the interval $[A^i(p^k_f), L^i_{SCFQ}(p^j_f)]$, excluding the packet that was at the head of its flow queue at time $A^i(p^k_f)$. Let $v_m(t)$ be the finish time of the packet at the head of the queue of flow m at time t. If no packet is present, then set $v_m(t) = v^i(t)$. Since SCFQ services packets in increasing order of the service tags,

$$v_m(A^i(p^k_f)) + \sum_{n\in R_m} \frac{l^n_m}{r_m} \leq v^i(A^i(p^k_f)) + \sum_{n=0}^{n=j-k} \frac{l^{k+n}_f}{r_f} \tag{13}$$

Since $v_m(A^i(p^k_f)) \geq v^i(A^i(p^k_f))$ we have

$$\sum_{n\in R_m} \frac{l^n_m}{r_m} \leq \sum_{n=0}^{n=j-k} \frac{l^{k+n}_f}{r_f} \tag{14}$$

$$\Rightarrow \sum_{n\in R_m} l^n_m \leq \sum_{n=0}^{n=j-k} \frac{l^{k+n}_f}{r_f} r_m \tag{15}$$

By adding (15) over all flows other than f we have

$$\sum_{m\in c^i \wedge m\neq f} \sum_{n\in R_m} l^n_m \leq \sum_{n=0}^{n=j-k} \frac{l^{k+n}_f}{r_f} \sum_{m\in c^i \wedge m\neq f} r_m \tag{16}$$

Adding $\sum_{n=0}^{n=j-k} l_f^{k+n}$ to both sides of the equation

$$\sum_{m \in c^i \wedge m \neq f} \sum_{n \in R_m} l_m^n + \sum_{n=0}^{n=j-k} l_f^{k+n} \leq \sum_{n=0}^{n=j-k} \frac{l_f^{k+n}}{r_f} \sum_{m \in c^i} r_m \tag{17}$$

$$\Rightarrow \quad \frac{\sum_{m \in c^i \wedge m \neq f} \sum_{n \in R_m} l_m^n + \sum_{n=0}^{n=j-i} l_f^{k+n}}{C^i} \leq \frac{\sum_{n=0}^{n=j-k} \frac{l_f^{k+n}}{r_f} \sum_{m \in c^i} r_m}{C^i} \tag{18}$$

Since $\sum_{m \in c^i} r_m \leq C^i$,

$$\frac{\sum_{m \in c^i \wedge m \neq f} \sum_{n \in R_m} l_m^n + \sum_{n=0}^{n=j-i} l_f^{k+n}}{C^i} \leq \sum_{n=0}^{n=j-k} \frac{l_f^{k+n}}{r_f} \tag{19}$$

The left hand side of (19), denoted by T, is the time spent in servicing the packets of flow f and packets of flows $m (m \neq f)$, excluding the packet which was at the head of the flow queue at time $A^i(p_f^k)$, before p_f^j departs. Since the server may spend some time servicing the packets that were at the head of the flow queues, we have

$$A^i(p_f^k) + \sum_{m \in c^i \wedge m \neq f} \frac{l_m^{max}}{C^i} + T \geq L^i_{SCFQ}(p_f^j) \tag{20}$$

where l_m^{max} is the maximum length of flow m packet. From (19), we get

$$A^i(p_f^k) + \sum_{m \in c^i \wedge m \neq f} \frac{l_m^{max}}{C^i} + \sum_{n=0}^{n=j-k} \frac{l_f^{k+n}}{r_f} \geq L^i_{SCFQ}(p_f^j) \tag{21}$$

From (2) we get

$$GRC^i(p_f^j) + \sum_{m \in c^i \wedge m \neq f} \frac{l_m^{max}}{C^i} \geq L^i_{SCFQ}(p_f^j) \tag{22}$$

■

Hence if $\sum_{m \in c^i} r_m \leq C^i$ and $\forall m \in c^i : \phi_m^i = r_m$, then SCFQ scheduling algorithm belongs to GR for flow f with $\beta^i = \sum_{m \in c^i \wedge m \neq f} \frac{l_m^{max}}{C^i}$. Note also that theorem 1 provides a tight bound over all rate assignments. An arrival sequence in which the upper bound is realized can be constructed from the proof steps.

3 Determining End-to-End Delay Bounds

3.1 Method

A simple method for determining a bound on end-to-end packet delays is to consider each server in isolation, and compute the summation of the maximum delay at each server along the path from the source to the destination [2, 3]. Though this may be the only feasible approach for many scheduling algorithms, it has the following drawbacks:

- Due to the variability in the delay experienced by packets at a server, the shape of the traffic gets distorted as it traverses through the network. Therefore, even if a source traffic specification is known at the first server on the path of the flow, it is difficult to determine the specification at a server further down on the path. This makes determining the delay at each of the server difficult.
- In many service disciplines, if a packet experiences a high delay at a server, it may experience a lower delay at the next server along the path. If each server is considered in isolation, the dependence between the delay experienced by packets at different servers is not accounted for, and hence the bound on the delay may be very conservative.

In this section, we address these limitations by presenting a method for determining end-to-end delay bound by analyzing the path as a whole. If the scheduling algorithm at each of the server on the path of the flow belongs to GR, then the delay guarantee of the algorithm can be exploited to determine end-to-end delay bound. This was illustrated in [9] for the Virtual Clock scheduling algorithm in Burst Scheduling networks for a particular source specification. Our key insight is that by deriving a delay guarantee for a network of servers, the problem of determining end-to-end delay can be reduced to the problem of determining delay at a single server.

Let K be the total number of servers along the path of a flow, and let the i^{th} server on the path be denoted by i. Also, let server 0 be the source and server $K+1$ be the destination. Since server K guarantees that packet p_f^j will be transmitted by $GRC^K(p_f^j) + \beta^K$ and the packet arrives at the first server at time $A^1(p_f^j)$, the end-to-end delay of p_f^j is given by

$$d_f^j \leq GRC^K(p_f^j) + \alpha^K - A^1(p_f^j) \tag{23}$$

where $\alpha^K = \beta^K + \tau^{K,K+1}$ and $\tau^{K,K+1}$ is the propagation delay between server K and the destination.

Observe that $GRC^K(p_f^j)$ depends on $A^K(p_f^j)$, which in turn depends on $GRC^{K-1}(p_f^j)$. Applying this argument recursively $GRC^K(p_f^j)$ can be related to $GRC^1(p_f^j)$. Since $GRC^1(p_f^j)$ is completely determined by the arrival characteristics of the source and the rate associated with the flow, the end-to-end delay can be determined if source specification is known. In what follows, we first relate

$GRC^{i+1}(p_f^j)$ to $GRC^i(p_f^j)$ and then relate the end-to-end delay to $GRC^1(p_f^j)$.

Lemma 1 *If the scheduling algorithm at server i belongs to GR for flow f , then*

$$GRC^{i+1}(p_f^j) \leq GRC^i(p_f^j) + \max_{k\in[1..j]} \frac{l_f^k}{r_f} + \alpha^i \quad j \geq 1 \tag{24}$$

where $\alpha^i = \beta^i + \tau^{i,i+1}$ and $\tau^{i,i+1}$ is the propagation delay between servers i and $i+1$.

Proof: The proof is by induction on j.
Base Case: $j = 1$

$$GRC^{i+1}(p_f^1) = A^{i+1}(p_f^1) + \frac{l_f^1}{r_f} \tag{25}$$

Since scheduling algorithm at server i belongs to GR for flow f, $A^{i+1}(p_f^1) \leq GRC^i(p_f^1) + \alpha^i$. Hence

$$\begin{aligned} GRC^{i+1}(p_f^1) &\leq GRC^i(p_f^1) + \frac{l_f^1}{r_f} + \alpha^i & (26)\\ &\leq GRC^i(p_f^1) + \max_{k\in[1..1]} \frac{l_f^k}{r_f} + \alpha^i & (27)\end{aligned}$$

Therefore (24) holds for $j = 1$
Induction Hypothesis: Assume (24) holds for $1 \leq j \leq m$.
Induction: We need to show (24) holds for $1 \leq j \leq m+1$.

$$\begin{aligned} GRC^{i+1}(p_f^{m+1}) &= max\{A^{i+1}(p_f^{m+1}), \\ &\quad GRC^{i+1}(p_f^m)\} + \frac{l_f^{m+1}}{r_f} \quad (28)\end{aligned}$$

Since scheduling algorithm at server i belongs to GR for flow f, $A^{i+1}(p_f^{m+1}) \leq GRC^i(p_f^{m+1}) + \alpha^i$. Hence

$$\begin{aligned} GRC^{i+1}(p_f^{m+1}) &\leq max\{GRC^i(p_f^{m+1}) + \alpha^i, \\ &\quad GRC^{i+1}(p_f^m)\} + \frac{l_f^{m+1}}{r_f} \quad (29)\end{aligned}$$

Thus, there are two cases to consider:

1. If $GRC^i(p_f^{m+1}) + \alpha^i > GRC^{i+1}(p_f^m)$, then from (29) we get

$$GRC^{i+1}(p_f^{m+1}) \leq GRC^i(p_f^{m+1}) + \frac{l_f^{m+1}}{r_f} + \alpha^i \tag{30}$$

Since $\max_{k\in[1..m+1]} \frac{l_f^k}{r_f} \geq \frac{l_f^{m+1}}{r_f}$,

$$\begin{aligned} GRC^{i+1}(p_f^{m+1}) &\leq GRC^i(p_f^{m+1}) + \\ &\quad \max_{k\in[1..m+1]} \frac{l_f^k}{r_f} + \alpha^i \quad (31)\end{aligned}$$

2. If $GRC^i(p_f^{m+1}) + \alpha^i \leq GRC^{i+1}(p_f^m)$, then from (29) we get

$$GRC^{i+1}(p_f^{m+1}) \leq GRC^{i+1}(p_f^m) + \frac{l_f^{m+1}}{r_f} \tag{32}$$

Using induction hypothesis we get,

$$\begin{aligned} GRC^{i+1}(p_f^{m+1}) &\leq GRC^i(p_f^m) + \\ &\quad \max_{k\in[1..m]} \frac{l_f^k}{r_f} + \frac{l_f^{m+1}}{r_f} + \alpha^i \quad (33)\end{aligned}$$

Since $GRC^i(p_f^{m+1}) \geq GRC^i(p_f^m) + \frac{l_f^{m+1}}{r_f}$ and $\max_{k\in[1..m+1]} \frac{l_f^k}{r_f} \geq \frac{l_f^{m+1}}{r_f}$,

$$\begin{aligned} GRC^{i+1}(p_f^{m+1}) &\leq GRC^i(p_f^{m+1}) + \\ &\quad \max_{k\in[1..m+1]} \frac{l_f^k}{r_f} + \alpha^i \quad (34)\end{aligned}$$

From (31), (34) and the induction hypothesis, we conclude that (24) holds for $1 \leq j \leq m+1$. Hence the lemma follows. ■

Theorem 2 *If the scheduling algorithm at each of the servers on the path of a flow belongs to GR for flow f, then the end-to-end delay of packet p_f^j denoted by d_f^j is given by*

$$d_f^j \leq GRC^1(p_f^j) - A^1(p_f^j) + (K-1) \max_{n\in[1..j]} \frac{l_f^n}{r_f} + \sum_{n=1}^{n=K} \alpha^n \tag{35}$$

where $\alpha^n = \beta^n + \tau^{n,n+1}$ and K is the number of servers on the path of the flow.

Proof: Since server K guarantees that packet p_f^j will be transmitted by time $GRC^K(p_f^j) + \beta^K$ and the packet arrives at the first node at time $A^1(p_f^j)$,

$$d_f^j \leq GRC^K(p_f^j) + \alpha^K - A^1(p_f^j) \tag{36}$$

Since the scheduling algorithm at each server on the path of the flow belongs to GR, by repeated application of lemma 1, we conclude that:

$$GRC^K(p_f^j) \leq GRC^1(p_f^j) + (K-1) \max_{n\in[1..j]} \frac{l_f^n}{r_f} + \sum_{n=1}^{n=K-1} \alpha^n \tag{37}$$

The theorem follows from (36) and (37). ■

Notice that $\alpha^n = \beta^n + \tau^{n,n+1}$. Hence, it is completely characterized by the scheduling algorithm and the propagation delay in the network. The remaining terms in (35) depend on the source traffic specifications, and are evaluated in the next section.

3.2 Source Traffic Specifications

A source traffic is specified by characterizing the number of bits that arrive at the network over an interval of time. Let $AP_f^i(t_1, t_2)$ be a function that denotes the flow f bits that arrive in the interval $[t_1, t_2]$ at server i. The bits of a packet are considered to have arrived only after the arrival of the last bit of the packet. Hence, the arrival function consists of an impulse at each packet arrival instants and is right continuous. Also $AP_f^i(t, t)$ is the length of the packet that arrives at time instant t.

As may be evident from theorem 2, to determine an upper bound on end-to-end packet delays, we must relate the guaranteed rate clock value of a packet to its arrival time at the first server. To achieve this objective, we define set S_f^i for flow f at server i as follows:

$$S_f^i = \{n | n > 0 \wedge GRC^i(p_f^{n-1}) \leq A^i(p_f^n)\}$$

From the definitions of guaranteed rate clock and set S_f^i, it can be shown that for each packet p_f^j

$$GRC^i(p_f^j) = A^i(p_f^k) + \sum_{n=0}^{n=j-k} \frac{l_f^{k+n}}{r_f} \qquad (38)$$

where $k \leq j$ is the largest integer belonging to set S_f^i. Since $\sum_{n=0}^{n=j-k} l_f^{k+n} = AP_f^i(A^i(p_f^k), A^i(p_f^j))$, we get

$$GRC^i(p_f^j) = A^i(p_f^k) + \frac{AP_f^i(A^i(p_f^k), A^i(p_f^j))}{r_f} \qquad (39)$$

Using theorem 2 and (39), we will now determine deterministic end-to-end delay bound for Leaky Bucket sources (a deterministic source characterization) and upper bound on the tail distribution of the delay for EBB sources (a stochastic characterization).

3.2.1 Leaky Bucket

A flow f conforms to Leaky Bucket [10] with parameters (σ_f, r_f) if

$$AP_f(t_1, t_2) \leq \sigma_f + r_f(t_2 - t_1) \quad t_1 \leq t_2, \quad t_1 \geq 0 \quad (40)$$

Theorem 3 *If flow f conforms to a Leaky Bucket with parameters (σ_f, r_f) and the scheduling algorithm at each of the server on the path of a flow belongs to GR for the flow, then the end-to-end delay of packet p_f^j denoted by d_f^j is given by*

$$d_f^j \leq \frac{\sigma_f + (K-1)\max_{n \in [1..j]} l_f^n}{r_f} + \sum_{n=1}^{n=K} \alpha^n \qquad (41)$$

where $\alpha^n = \beta^n + \tau^{n,n+1}$ and K is the number of servers on the path of the flow.

Proof: Let $k \leq j$ be largest integer belonging to set S_f^1. Clearly, such a k must exist. Since flow f conforms to Leaky Bucket specification, we get

$$AP_f^1(A^1(p_f^k), A^1(p_f^j)) \leq \sigma_f + r_f(A^1(p_f^j) - A^1(p_f^k)) \qquad (42)$$

Hence, from (39) we get,

$$GRC^1(p_f^j) \leq \frac{\sigma_f}{r_f} + A^1(p_f^j) \qquad (43)$$

which implies

$$GRC^1(p_f^j) - A^1(p_f^j) \leq \frac{\sigma_f}{r_f} \qquad (44)$$

Theorem 3 follows from (44) and theorem 2. ■

We note that the if the scheduling algorithm used at each of the servers is either of Virtual Clock and PGPS , then bound in (41) is as tight as bound in [10] for Rate Proportional Processor Sharing (RPPS) rate assignment of PGPS networks when the sources conform to Leaky Bucket.

3.2.2 Exponentially Bounded Burstiness

A flow f conforms to an Exponentially Bounded Burstiness (EBB) [13] process with parameters $(r_f, \Lambda_f, \gamma_f)$, if

$$Pr\left(AP_f^i(t_1, t_2) \geq r_f(t_2 - t_1) + x\right) \leq \Lambda_f e^{-\gamma_f x} \quad x \geq 0 \;\; t_1 \leq t_2 \qquad (45)$$

Theorem 4 *If flow f conforms to EBB with parameters $(r_f, \Lambda_f, \gamma_f)$ and the scheduling algorithm at each of the server on the path of a flow belong to GR for the flow , then the end-to-end delay of packet p_f^j denoted by d_f^j is given by*

$$Pr\left(d_f^j \geq x + (K-1)\frac{\max_{n \in [1..j]} l_f^n}{r^f} + \sum_{n=1}^{n=K} \alpha^n\right) \leq \Lambda_f e^{-\gamma_f x r_f} \quad x \geq 0 \qquad (46)$$

where $\alpha^n = \beta^n + \tau^{n,n+1} and\ K$ is the number of servers on the path of the flow.

Proof: Let $k \leq j$ be the largest integer belonging to set S_f^1. Clearly such a k must exist. By the definition of EBB process, we have

$$Pr\left(AP_f^1(A^1(p_f^k), A^1(p_f^j)) \geq r_f(A^1(p_f^j) - A^1(p_f^k)) + y\right) \leq \Lambda_f e^{-\gamma_f y} \quad y \geq 0 \quad (47)$$

which implies

$$Pr\left(\frac{AP_f^1(A(p_f^k), A^1(p_f^j))}{r_f} + A^1(p_f^k) - A^1(p_f^j)\frac{y}{r_f}\right) \leq \Lambda_f e^{-\gamma_f y} \quad y \geq 0 \quad (48)$$

From (39 we have

$$Pr\left(GRC^1(p_f^j) - A^1(p_f^j) \geq \frac{y}{r_f}\right) \leq \Lambda_f e^{-\gamma_f y} \quad y \geq 0 \quad (49)$$

The theorem follows from (49) and theorem 2. ■

3.3 Discussion

Theorem 2 demonstrates that a heterogeneous network of servers, each of which employs a scheduling algorithm in the GR class, can provide an upper bound on end-to-end packet delays for heterogeneous sources. Additionally, the salient features of the class of GR scheduling algorithms and the method for determining end-to-end delay bounds include:

- An end-to-end delay bound can be determined for any source traffic specification for which a bound on difference between the guaranteed rate clock and the arrival time of a packet at the first server can be determined. Since this difference is only a function of the source traffic characteristics and the rate associated with the flow, it is simple to determine an end-to-end delay bound. Moreover, this difference can be interpreted as delay experienced by a packet at a single queue server with capacity r_f. Hence, queuing analysis can also be used to determine a bound on the tail distribution of delays experienced by packets for a conventional stochastic process characterization.

- A source can determine end-to-end delays without specifying the shape of the traffic to the network by keeping track of guaranteed rate clock values associated with it's flow at the first server. Such a capability is important as the source may not have a good characterization of the traffic or the characterization may not be known a priori. Moreover, even if the characterization is known, it may not conform to the set of characterizations supported by the network. Finally, this capability allows the source to decide when to renegotiate the reserved rate.

The method derives it's simplicity from the constraint placed on the rate assignments in the scheduling algorithms. Note that the constraint we have placed on rate assignments for PGPS so that it belongs to GR is similar to Rate Proportional Processor Sharing (RPPS) [10]. Such constraints allow each flow to be considered in isolation and hence facilitate the determination of delay bounds for each flow independently. If this constraint is relaxed to derive delay bounds for more general rate assignments, assumptions about the behavior of the other sources in the network may have to be made. In such scenarios, however, the QoS guarantees provided to each flow may be conditional since sources may be greedy and not conform to the specifications [11]. Moreover, if hardware traffic enforcement mechanisms are used, the probability of failure of one of the numerous pieces of enforcement hardware may not be negligible, thereby weakening the guarantees. Hence, we believe that the constraints on rate assignments described above are not a limitation, but in fact are highly desirable.

4 Conclusions

In this paper, we have defined a class of Guaranteed Rate scheduling algorithms. The GR class includes Virtual Clock, Self Clocked Fair Queuing and Packet-by-Packet Generalized Processor Sharing. For networks that employ scheduling algorithms belonging to GR, we have presented a method for determining an upper bound on end-to-end packet delays. The method facilitates determining end-to-end delay for a variety of sources. We have illustrated the method by determining end-to-end delay for sources conforming to Leaky Bucket and Exponentially Bounded Burstiness. The delay bounds that we have derived are parameterized by the scheduling algorithms which when instantiated with Virtual Clock and SCFQ scheduling algorithms, lead to many new results.

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