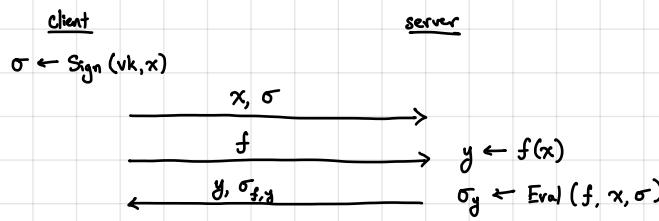


Next up: homomorphic signatures



↓  
checks that  $\sigma_{f,y}$  is a signature on  $y$  with respect to function  $f$

↳ can view as signature on pair  $(f, y)$     ↳ Why not just on  $y$  alone?

Requirements: Unforgeability: Cannot construct signature  $\sigma$  on  $(f, y)$  where  $y \neq f(x)$ .  
(Will formalize later)

Succinctness: Size of  $\sigma_{f,y}$  should be  $|y| \cdot \text{poly}(\lambda)$ . In particular, should not depend on  $|x|$  or  $|f|$ .  
↳ Otherwise trivial to construct! (Outputting  $(\sigma, x, f(x))$  suffices).

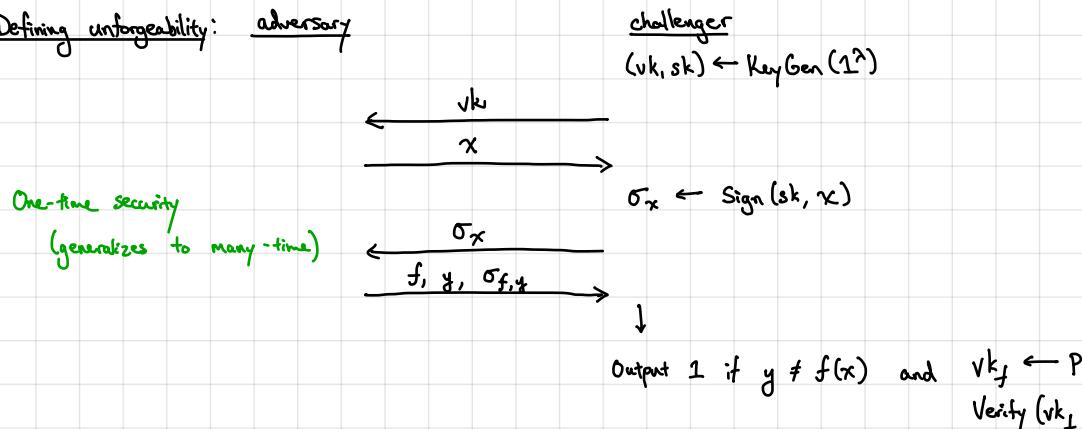
Efficient verification: Can decompose verification algorithm as follows:    ↳ Also the case for FHE!

- Preprocess  $(vk, f) \rightarrow vk_f$       Generates short function verification key  $vk_f$  ( $|vk_f| = \text{poly}(\lambda, d)$ )
- Verify  $(vk_f, y, \sigma) \rightarrow 0/1$       Runs in time  $\text{poly}(\lambda, d, |y|)$

depth of circuit  
computing  $f$

Homomorphic signatures allow computations on authenticated data.

Defining unforgeability: adversary



↓  
Output 1 if  $y \neq f(x)$  and  $vk_f \leftarrow \text{Preprocess}(vk, f)$   
 $\text{Verify}(vk_f, y, \sigma_{f,y}) = 1$

Construction: relies on similar homomorphic structure as GSW (for message space  $\{0,1\}^{\ell}$ )

-  $\text{KeyGen}(1^\lambda)$ : Set lattice parameters  $n = n(\lambda)$ ,  $g = g(\lambda)$ . Let  $s = s(\lambda)$  be Gaussian width parameter for preimage sampling.

Sample  $(A, T) \leftarrow \text{TrapGen}(n, g)$        $[A \in \mathbb{Z}_g^{n \times m}, T \in \{0,1\}^{m \times t}]$

Sample  $B_1, \dots, B_\ell \leftarrow \mathbb{Z}_g^{n \times t}$

↳  $AT = G \in \mathbb{Z}_g^{n \times t}$ ;  $t = n \lceil \log g \rceil$

Output  $vk = (A, B_1, \dots, B_\ell)$ ,  $sk = T$

-  $\text{Sign}(sk, x)$ : Compute  $R_i \leftarrow \text{SamplePre}(A, T, B_i - x_i G)$  for  $i \in [\ell]$

In particular:

$$\begin{aligned} A[R_1 | \dots | R_\ell] &= [B_1 - x_1 G | \dots | B_\ell - x_\ell G] & (R_i \in \mathbb{Z}_g^{n \times t}) \\ &= [B_1 | \dots | B_\ell] - x \otimes G \end{aligned}$$

Output  $\sigma = (R_1, \dots, R_\ell)$

-  $\text{Verify}(vk, x, \sigma)$ : Check that  $\|R_i\| \leq B$  where  $B = s \cdot \omega(\sqrt{\log n})$  and that  $A[R_1 | \dots | R_\ell] = [B_1 | \dots | B_\ell] - x \otimes G$

Signatures verification keys

Homomorphic evaluation:  $A[R_1 | \dots | R_n] = [B_1 - x_1 G | \dots | B_n - x_n G]$

To derive a signature on the sum of two bits ( $x_i + x_j$ ): new verification component associated with  
 $R_+ = R_i + R_j$  } Verification:  $A R_+ \stackrel{?}{=} B_+ - (x_i + x_j) G$  addition operation  
 $B_+ = B_i + B_j$  new signature

To derive a signature on the product of two bits ( $x_i; x'_j$ ):

$$AR_i = B_i - x_i G \quad \Rightarrow \text{ desire something of the form} \\ AR_j = B_j - x_j G \quad \quad \quad AR_x = B_x - x_i x_j \cdot G$$

$$\begin{aligned} \text{AR}_i &= B_i - x_i G \rightarrow B_i = \text{AR}_i + x_i G \\ \text{AR}_j G^{-1}(B_i) &= (B_j - x_j \cdot G) G^{-1}(B_i) \\ &= B_j G^{-1}(B_i) - x_j \cdot B_i \\ &= B_j G^{-1}(B_i) - A(x_j R_i) - x_i \cdot x_j G \\ \Rightarrow A \underbrace{(R_j G^{-1}(B_i) + x_j R_i)}_{R_x} &= B_j G^{-1}(B_i) - \underbrace{x_i \cdot x_j \cdot G}_{B_x} \\ R_x &= R_i G^{-1}(B_i) + x_i R_i; \quad B_x = B_i G^{-1}(B_i) \end{aligned}$$

$$\|R_x\|_{\infty} \leq \|R_y\|_{\infty} \cdot t + \|R\|_{\infty}$$

function of public key only  
(this is CSE homomorphic multiplication)

Observation:  $R_+ = R_i + R_j$

$$R_x = R_i (x_j I_t) + R_j G^{-1}(R_i) = [R_i \mid R_j] \begin{bmatrix} x_j I_t \\ G^{-1}(R_i) \end{bmatrix}$$

$R_x$  can depend on  $R_i, R_j, x$

Compose above operations to compute signature on  $R_{f,x}$  on evaluation  $f(x)$

By above analysis, multiplication scales noise by a factor of  $t$  so if  $f$  can be computed by a circuit of depth  $d$ ,  $\|R_{f,x}\|_\infty \leq t^{O(d)}$

To verify a signature  $R_{f,x}$  on  $(f, z = f(x))$ , verifier computes  $B_f$  from  $B_1, \dots, B_L$  and checks that

$\|R_{f,x}\|_\infty$  sufficiently small (bound  $\sim t^{o(1)}$ )

$$AR_{f,r} = B_f - z \cdot G$$

More generally:

$R_{f,x} = [R_1 | \dots | R_d] \cdot H_{f,x}$  where  $H_{f,x} \in \mathbb{Z}_q^{dt \times t}$  and  $\|R_{f,x}\|_\infty \leq t^{O(d)} = (n \log q)^{O(d)}$   
 where  $d$  is the (multiplicative) depth of  
 the circuit computing  $f$

Now, if  $AR_i = B_i - x_i G$ , then from the above,

$$AR_{fx} = B_f - f(x) \cdot G$$

where  $B_f$  is the matrix obtained by evaluating  $f$  on  $B_1, \dots, B_L$

This can be expanded as

$$AR_{fx} = A[R_1 | \dots | R_e] H_{fx} = [B_1 - x \cdot G | \dots | B_e - x_e \cdot G] H_{fx}$$

$$= B_f - f(x) \cdot G$$