

# How We Extended ACL2 to Verify Block Cipher Implementations

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# Block Cipher Verification

- 2008 FMCAD paper: “Automatic Formal Verification of Block Cipher Implementations” by Eric Smith and David Dill
- Verifies JVM code for a block cipher (AES, DES, Blowfish).
- Proves equivalence to a formal ACL2 spec.
  - Bit-for-bit equivalence of outputs, for all inputs (too many to test).
- Nearly automatic
  - Verified Skipjack cipher from scratch in 3 hours.
- Couldn't use ACL2's term representation or rewriter.
  - Defined my own.

# Block Cipher Verification (cont.)

- “Unroll” both computations completely.
- ACL2 specification:
  - Open all functions (except bit-vector and array primitives).
  - Q: Why does this work for recursive functions?
  - A: Because the depth of each recursion is fixed
    - e.g, 10 rounds for AES-128
  - Result: A huge term with BV and array operators.
- JVM code
  - Symbolically simulate the whole program.
  - Q: Why does this work for loops?
  - A: Because the number of loop iterations is fixed
  - Result: Another huge term with BV and array operators.

# Block Cipher Verification (cont.)

- To prove equality of two huge terms:
  - Rewrite terms using bit-vector library.
  - Bit-blast and rewrite again.
    - Sufficient to verify several ciphers.
  - Then use techniques from combinational equivalence checking.
    - Run test cases and find nodes that are “probably equal.”
    - Repeatedly prove two nodes equal and “merge” them.
    - Call STP (a SAT-based decision procedure for bit-vectors and arrays) to do the individual proofs.

# Huge terms

- The terms being compared:
  - are completely unrolled (have only BV and array operators)
  - represent the ciphertext as a function of the inputs (plaintext and key).
  - are *really complicated*.
  - have massive sharing.
- Result of round n appears many times in the expression for round n+1.
- Each round multiplies the term size.
  - Exponential explosion!
- Unrolled ACL2 specs:
  - 128-bit AES: 14033950307046 nodes (3948 unique nodes)
  - 192-bit AES: 3631268598854768 nodes (4683 unique nodes)
  - 256-bit AES: 350943360883919420 nodes (5647 unique nodes)
  - 64-bit Blowfish:  $\sim 2^{17235}$  nodes (220817 unique nodes)

# 64-bit Blowfish node count =

1024822625182104036460593754615016929511412475316201129628525747322369022664200327591542384423978026717371878568804620768217701833707573786933653  
210653515628728000122362637952361950171731456568753014052065152775468811406305203976423936461505279124029580654626585235322098729757444696981254  
9631589069188700607691552374320753991862969126651817843443792762362999531330095753015742721295320438456175092161158993941217791696544189173947570  
529255944777252733397048785812152220358237088671491480457604651164423794188213262360090145335814255308740405385986507247270792872733179186388298  
093012252305162635462400154071829681595780811423817129223463400342944545386161045894990594626486309453093873281195869721319935989184673982843133  
947258269846928153396252153602090257294389161774909681809764078221678374443761137549672184952669581434836759834301270549844840614282619746429883  
810078698472245397562204570688818530875838959156629596272394816645260977135207505792458379807510341206099248586865437687082968709337304554872291  
4106592189132782062983934733543328003431273225452294923469412276425960504924523118383787113608081247290559783921648516752581121589681792205615360  
714399883554282278732593730716039389112234238146483654675956573444064193339392138418327372561485709733604529381231505738240525868955196854476176  
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87618723751452575349111664074628736550277458635297070251630793042012091810932045007948185209005074160551261430442902747707852140526614471510468695  
6320513681607754376483901112440334650376364164288339347933125522603932765968474162611035196019342961208909844527334138700233542087243522926053418  
55758941699159311771440035408162247631534113517753178519768656067030546592697698257032489574269951347009290756244381265224235194645418890016918837  
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1135330928041242068924618901203805764726457206195509628670387082670180957012761203031174523258600014854047799948575803516209373889047505044679787  
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4579998065412725507273272424714610100295724566460939162792666764538246041638148368755057815550359122509031211458727243817097684403486894532993261  
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642736258292474423203029275691141334740547037652652414927820829791636380604260934706114332520363335645651520468466755697005980559699185492503792  
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1273345004941425221116435678644265440551583666381745068224251446462855439324979323023040405622643777954006647090707265851661878471215544011257178  
9012085127644642505289965304964784861335814756564612936652598436937659158730653242693223669775175128254398041648111000488450509842165206414424551  
3672290550804438539591133548732147854138592412503563310840215461543081121671674326361837950048198824579327355327145163958077589874389790477370723  
7881343204471848152728312253414328478673724659447020068816861158887223728775750304876808353429148089072641051103672494064572279518176630667354522  
7148192226066461700551729853174906005581525856373321371353708396176679774661433731921797009609783638190409021682760730965442688408430103600984083  
2619307084840910656318192634697235696370766330398897806049589569822165083141597876977642884116174175422890336160180073208550510348317357910260540  
173755158620097313015070064984412936780542942923557169582437462286279850548867898730586960812485927720127211866240912942165

# Huge terms

- If you naively process a term, you die.
  - try to print it, evaluate it, rewrite it, get its size, get its variables, etc.
- ACL2's rewriter dies on:
  - Unrolling the spec.
  - Symbolically simulating the bytecode.
- My solution:
  - Define a representation of terms that shares subterms.
    - A subterm can have many parents.
    - These “terms” aren't trees.
    - They are directed, acyclic graphs (DAGs)
  - Define a rewriter on DAGs.

# DAGs

- Nodes are numbered.
- Each node is:
  - a variable,
  - a function call (whose arguments are node numbers and/or quoted constants)
- DAGs are compact: No two nodes are identical.
- DAGs are convenient to print and read back in.

Term:

```
(foo (bar y '100) (bar y '100))
```

DAG:

```
((2 F00 1 1)  
 (1 BAR 0 '100)  
 (0 . Y))
```

# DAG Rewriter

- Takes a DAG and a set of rules.
- Sweeps up the DAG.
- Builds a simplified DAG.
  
- Can symbolically simulate JVM code.
- Can unroll ACL2 specifications.
- Can simplify the resulting DAGs.
- Useful in other tools as well.
  - (e.g., JVM bytecode decompiler)

# DAG Rewriter (cont.)

- Similarities to ACL2's rewriter:
  - Applies standard ACL2 `:rewrite` and `:definition` rules.
  - Rewrites inside-out.
  - Relieves hypotheses recursively via rewriting.
  - Matches free variables from assumptions.
  - Has a version of `syntxp`.
  - Has a version of `bind-free`.
  - Allows staged simplification
    - But implementation is different.

# DAG Rewriter (cont.)

- Differences with ACL2:
  - Represents terms as DAGs.
  - No type-prescription, forward-chaining, or linear reasoning.
  - No splitting on ifs (!)
    - Either just one case or exponentially many.
    - Let SAT handle the cases.
  - User can change the order that rules are applied.
    - Better than ACL2's behavior?
  - Special purpose code for normalizing nests of XORs
    - Huge XOR nests arise in verifying block ciphers.
    - Sorting the arguments using rules is quadratic.
    - Could extend this to any associative/commutative function.
  - Easy to say “simplify this term.”

# DAG Rewriter (cont.)

- Fairly efficient:
  - Uses ACL2 arrays under the hood.
  - Uses the “parent trick” to check whether a node already exists.
- Tail-recursive (when rewriting right hand sides)
  - Allows long chains of rewrites (e.g., in symbolic simulations)
- Memoizes rewrites.
- Performance:
  - Unrolls the spec. for AES-128 in ~7 seconds.
  - Symbolically executes JVM bytecode in ~14 seconds (~9900 bytecode instructions).

# Bit vector rules and connection to STP

- ACL2 library of bit-vector and array operations
  - Many general-purpose rules.
    - (Some special rules for ciphers.)
  - Used in my modification of the M5 JVM model.
  - Operators mostly match STP's operators.
- Easy to translate from DAGs (with only these operators) to STP.
- Each DAG node becomes a LET.
  - (tricky to handle equality of arrays, constant arrays)
- STP uses DAGs internally.

# Future Work

- Make DAG rewriter faster
  - Stobj arrays? Hashing? Honsing?
- Verify the DAG rewriter?
  - Would love to make it a verified clause processor.
  - How do we justify using rules from the ACL2 world?
  - Issues with evaluation of ground terms.
  - Do I have to fix the set of functions on which it operates?
- Modern tools seem to use DAGs (STP, ABC). Can we make ACL2 use DAGs?
  - Maybe just use hons and memoization?
  - Maybe just prevent LETs from expanding in proofs?
    - \_ But we might still build the same term twice?
    - \_ We'd need rewrite rules to fire despite intervening LETs.
  - Not sure how to handle a shared node that appears in many different IF contexts.

The End

# Tools I've developed

- DAG rewriter.
- Supporting library of bit-vector and array functions.
- Java class file parser (written in ACL2)
- JVM model (improved version of M5)
- Formal specifications for many ciphers.
- Translator from DAGs to STP.
- JVM bytecode to ACL2 decompiler.
- ACL2 build system (written in ACL2).